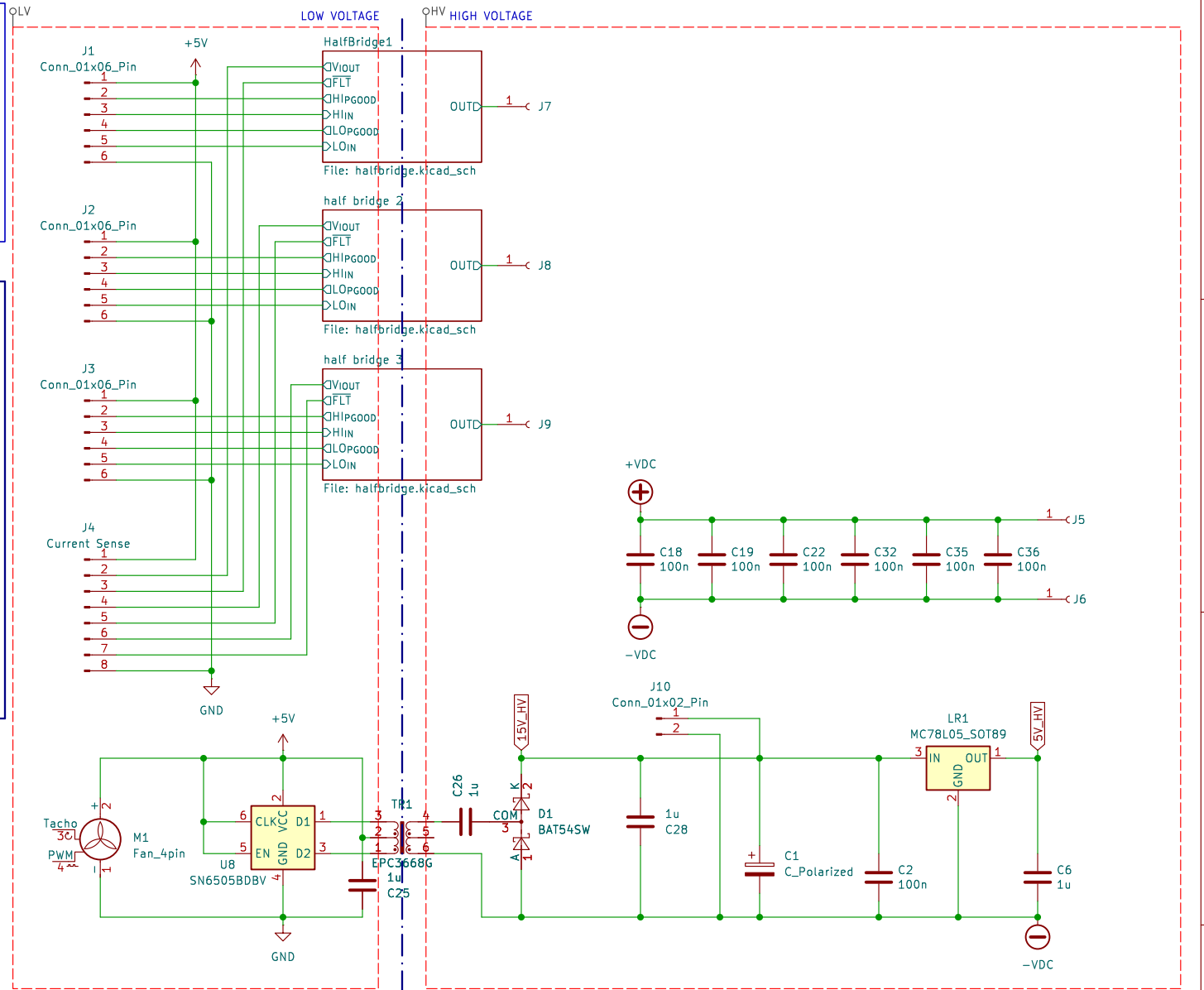
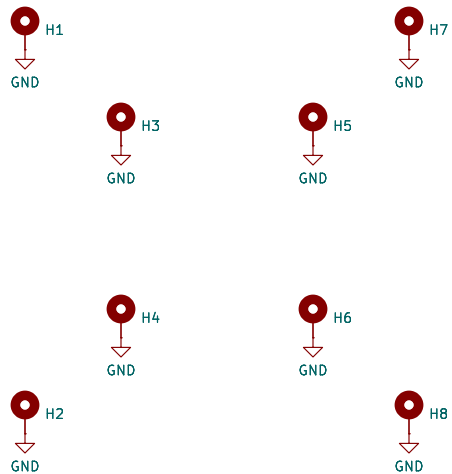


NOTE 1  
J1 thru J4 are ELV and isolated from HVDC

WARNING:  
J10 referenced to HVDC supply rail, primarily intended for supplying additional power to HV section if needed (pushing limits on switching frequency).

### 3: Hole arrangement



Sheet: /  
File: three\_half\_bridges.kicad\_sch

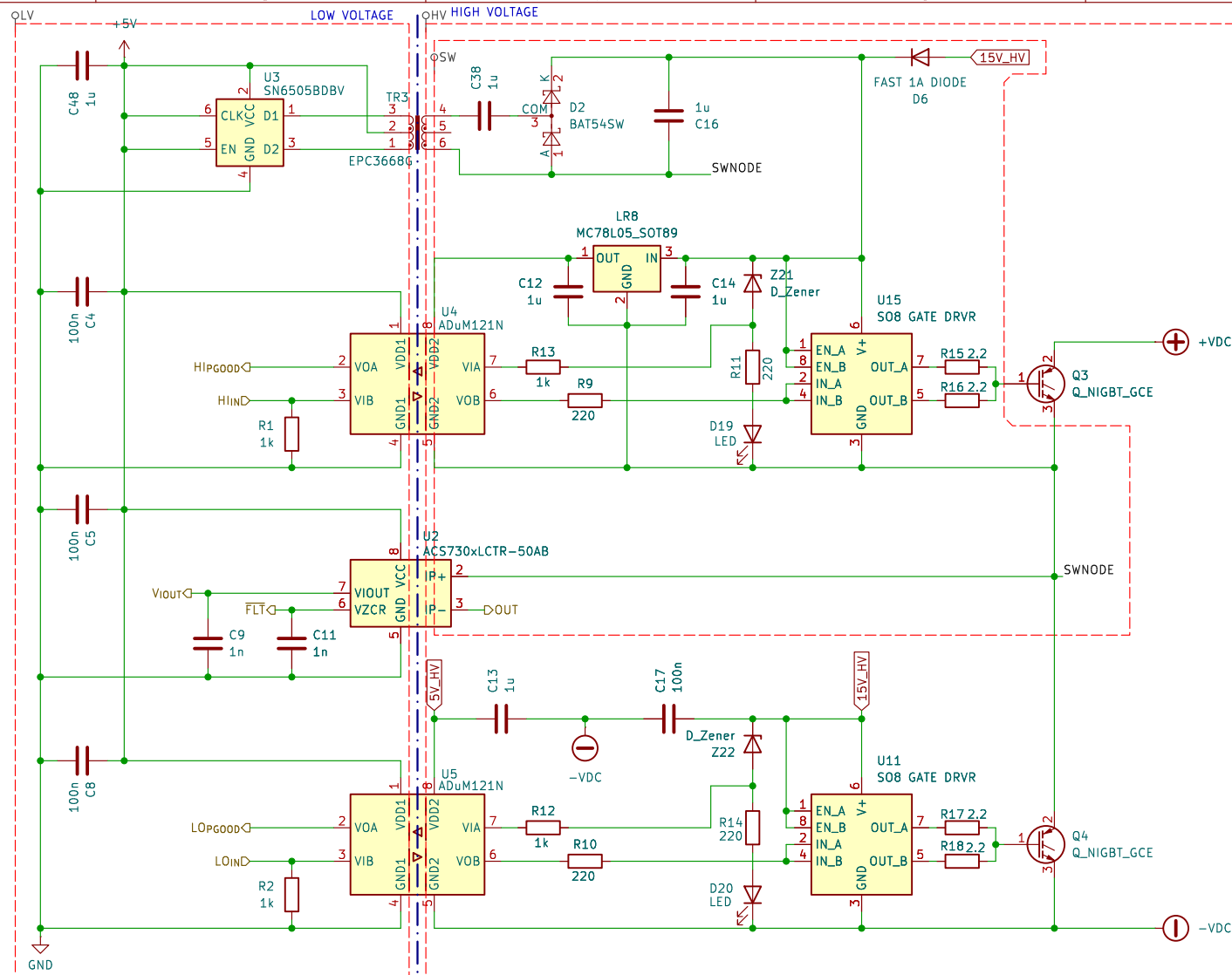
### Title:

Size: A Date:  
KiCad E.D.A. 9.0.0

Rev:  
Id: 1/4

**WARNING:**  
Do not use TO-3P and certainly not old SOT83 or TO218 transistors here, as there is no clearance between adjacent low side switches and the metal tab is connected to each half bridge SWNODE, which can be at different potentials when each half bridge is used independently.

**PP note:**  
Logic Threshold for ADuM121 input should be 0.3Vin and 0.7Vin, which roughly translates to 1.5 and 3.5V. In addition to the 9V zener diode and 2V amber LED adds 11V to the logic threshold. So 14.5V to trigger Pgood and 12.5V to set it low. Can be fine-tuned with the current limit resistor, will be lower in practice.



Sheet: /half bridge 2/  
File: halfbridge.kicad\_sch

**Title:**

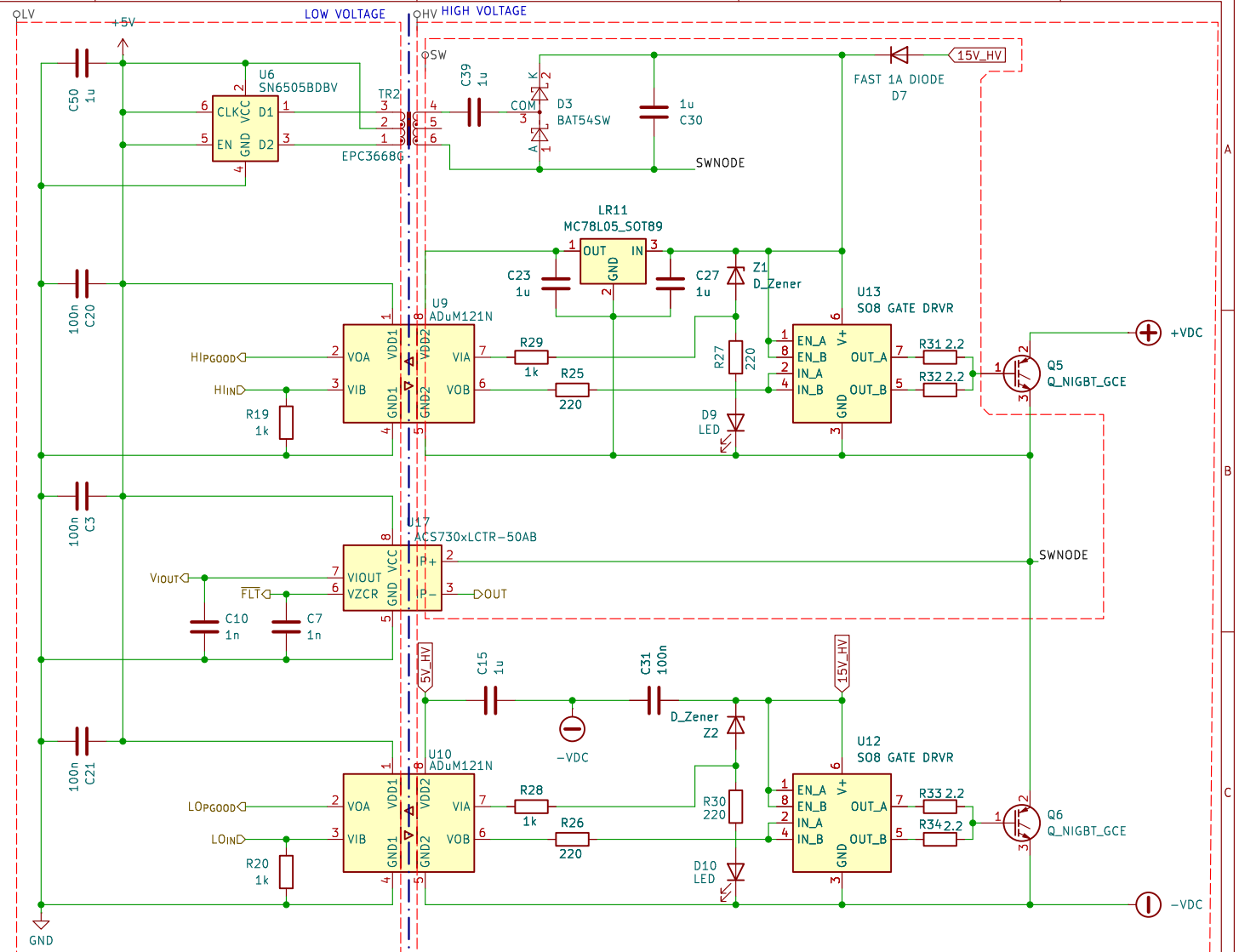
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**Rev:**

Id: 3/4

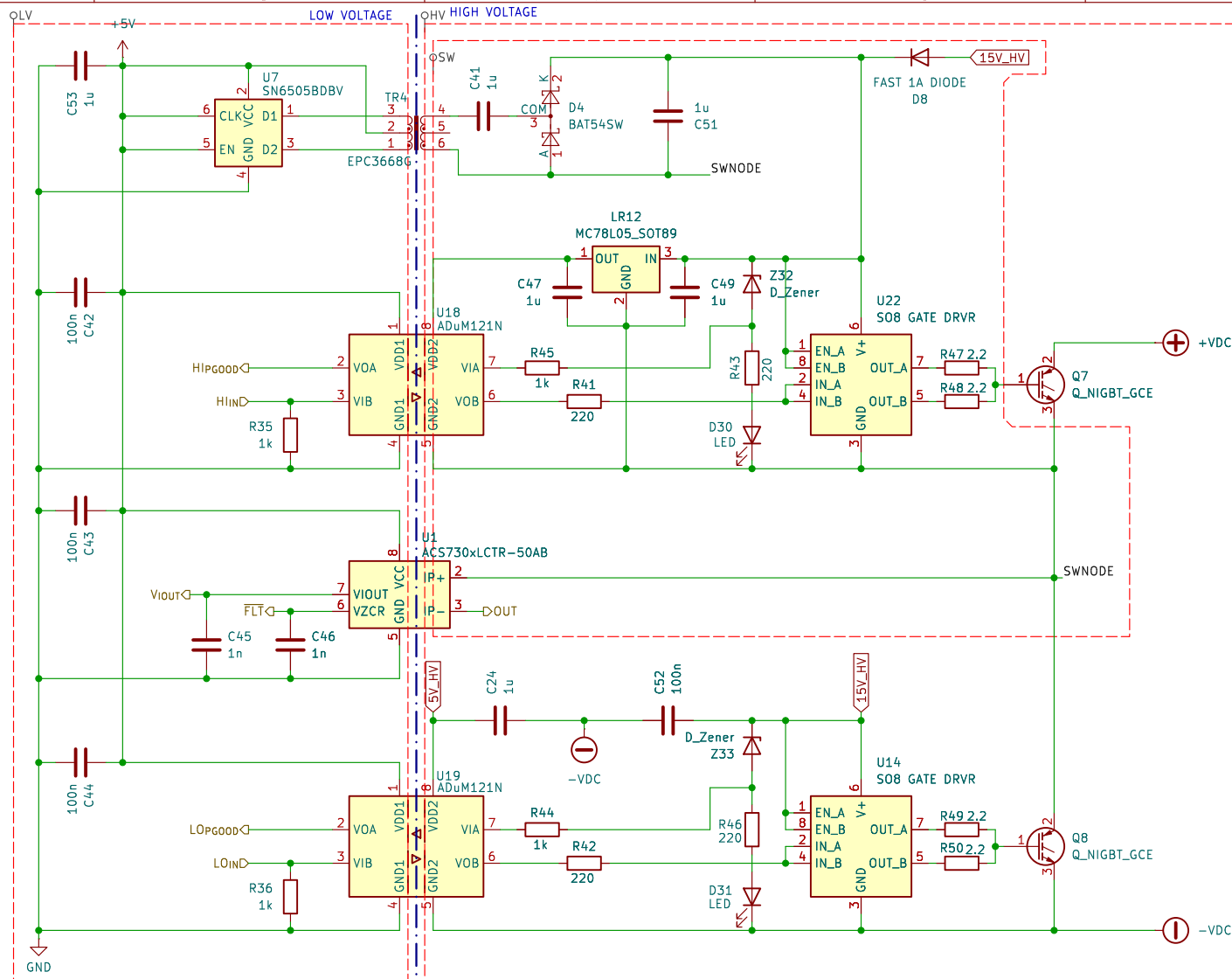
PP note:  
Logic Threshold for ADuM121 input should be 0.3Vin and 0.7Vin, which roughly translates to 1.5 and 3.5V. In addition to the 9V zener diode and 2V amber LED adds 11V to the logic threshold. So 14.5V to trigger Pgood and 12.5V to set it low. Can be fine-tuned with the current limit resistor, will be lower in practice.



Id: 4/4

**WARNING:**  
Do not use TO-3P and certainly not old SOT83 or TO218 transistors here, as there is no clearance between adjacent low side switches and the metal tab is connected to each half bridge SWNODE, which can be at different potentials when each half bridge is used independently.

**PP note:**  
Logic Threshold for ADuM121 input should be 0.3Vin and 0.7Vin, which roughly translates to 1.5 and 3.5V. In addition to the 9V zener diode and 2V amber LED adds 11V to the logic threshold. So 14.5V to trigger Pgood and 12.5V to set it low. Can be fine-tuned with the current limit resistor, will be lower in practice.



Sheet: /half bridge 3/  
File: halfbridge.kicad\_sch

**Title:**

Size: A4 Date:

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**Rev:**

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