

Test Test

Birth Date	21 March 1980
Marital Status	Married
Nationality	Polish
Availability	since: 1 October 2009
Salary Expectations	5700 PLN netto/month
Objective/Position	Embedded Developer

Education

	Name of Institution	Faculty/Major	Specialization/ Master's Thesis Title
03.08 - 06.08	AGH University of Science and Technology	Post graduate studies "Software Engineering"	
10.02 - 10.03	University of Hannover, Germany	One-year studying abroad at University of Hannover under the Erasmus-Socrates scheme	
10.99 - 10.04	Poznan University of Technology, Poland	Course: Electronics and Telecommunication	Multimedia and Consumer Electronics / Bandwidth Extension Tools For Digital Audio Signals

Employment History

Delphi Poland S.A, department Controls & Security, Cracow, Poland **06.07 - now**

Company Description:	Automotive supplier of mobile electronics and transportation systems, including powertrain, safety, steering, thermal, and controls & security systems, electrical/electronic architecture, and in-car entertainment technologies.
Position held:	Software Engineer
Job Responsibilities (general):	Software Engineer for Ultrasonic Interior Protection module
Job Responsibilities:	<ul style="list-style-type: none"> • Design, develop, test software for Ultrasonic Interior Protection module with only LIN interface according to Delphi quality standards; High volume production with low tolerance for errors • Collect, analyze and write SW requirements and SW documentation, • Build manager in projects • Cooperate with German team (Delphi Deutschland GmbH) • Support system engineering, participate in meeting with customer, clarify and negotiate SW requirements • Program part of LIN driver, implement deviation from LIN standard

- Manufacturing support (MPI file, ICT, EOL)
- Software testing on real HW

Verocel-Polska Sp. z o.o, Poznan, Poland
05.06 - 06.07

Company Description:	VEROCEL-POLSKA Sp. z o.o., located in Poznan, Poland, is a subsidiary of VEROCEL, Inc. (headquartered in Westford, Massachusetts, USA). VEROCEL is a company which provides expertise and services for Software Verification. This includes development and review of plans and standards, development of tests, software structural coverage analyses, traceability, and outsource support for software verification activities.
Position held:	Tester-Programmer
Job Responsibilities (general):	verify software in the safety critical software industry in accordance with DO-178B
Job Responsibilities:	<ul style="list-style-type: none"> • analyze source code, write tests for the specified requirements, test and verify the tools for coverage analysis • analyze incompletely covered source code (written in C language) and disassembled code (PowerPC instructions)

Institute of Electronics and Telecommunications, Poznan University of Technology, Poznan, Poland
02.04 - 04.06

Position held:	Software developer
Job Responsibilities (general):	Software developer
Job Responsibilities:	<ul style="list-style-type: none"> • design and develop high efficient video and audio real-time encoder and decoder conformed with H.263, MPEG-2, MPEG-4 (AVC, AAC) standards • integrate video codec into DirectShow transform filter (Windows XP) and Video for Windows • implement text editor for the embedded system (μC Atmel AT91SAM7S128 + FreeRTOS core, 4MB Flash, IAR Embedded Workbench)

Institut Allgemeine Nachrichtentechnik, Universität Hannover, Hannover
11.03 - 12.04

Position held:	PHP programmer
Job Responsibilities (general):	design and develop online institute library, full development cycle, from the database design to PHP coding

Summary of Technical Knowledge

Programming Languages:	<ul style="list-style-type: none"> • Assemblers (very good) • bash (good) • Perl (good) 	<ul style="list-style-type: none"> • C++ (average) • ABAP (basic) • C (basic)
Development Tools:	<ul style="list-style-type: none"> • Apache Ant (excellent) • ArgoUML (excellent) • Microsoft Visual Studio (very good) 	<ul style="list-style-type: none"> • CVS (Concurrent Versions System) (average) • make (average) • gcc (GNU Compiler Collection) (basic)

Web Application Technologies:	<ul style="list-style-type: none"> • (X)HTML (excellent) 	
Databases and Related Technologies:	<ul style="list-style-type: none"> • MySQL 4 (good) 	<ul style="list-style-type: none"> • ODBC (basic)
GUI Frameworks and Libraries:	<ul style="list-style-type: none"> • Apple Cocoa (excellent) • GTK+ (excellent) 	<ul style="list-style-type: none"> • Java AWT (excellent)
Low-level/Embedded Technologies:	<ul style="list-style-type: none"> • 8051 microcontroller (excellent) • ARM7 microcontrollers (excellent) 	<ul style="list-style-type: none"> • AVR microcontrollers (excellent)
Operating Systems:	<ul style="list-style-type: none"> • Windows (usage) (very good) 	
Other Technical Knowledge:	<ul style="list-style-type: none"> • - Experience in projects for high volume production with low tolerance for errors (very good) • in-circuit emulator (good) • ICE (In-circuit emulator), IDE for NEC μC (average) • 8-bit μC NEC (78K0/KC2) (basic) • Ada (basic) • AUTOSAR (basic) • Code Composer Studio (basic) • Hardware: TMS320C642x (fixed point processor) (basic) 	<ul style="list-style-type: none"> • integrated debugger ID78K0-QB (basic) • Intel ASM (processor: Prescott) (basic) • Microsoft DirectShow 9.0 (basic) • PowerPC (basic) • requirements definition and management: Telelogic DOORS (basic) • software revision control system: Telelogic CM/Synergy and Change/Synergy (basic) • SPICE (basic) • VxWorks OS (basic)

Languages

	Written	Spoken
German:	advanced	advanced
English:	advanced	advanced

Personal Achievements

	Name of Event	Related Organization
02.99	Third place in regional physic contest	
04.94	Laureate of the regional competition in Mathematics	

Other Information

Interests:	Music: guitar , psychology
Driving License:	Yes

Project Portfolio

Ultrasonic Interior Protection module 09.07 - now

Company Name:	Delphi Poland S.A
Project Introduction:	Design, develop, test software for Ultrasonic Interior Protection module with only LIN interface according to Delphi quality standards; High volume production with low tolerance for errors
Position/-s Held:	Software Engineer
Responsibilities:	<ul style="list-style-type: none"> • collect, analyze and write SW requirements and SW documentation • build manager in projects • cooperate with German team (Delphi Deutschland GmbH) • support system engineering, participate in meeting with customer, clarify and negotiate SW requirements • program part of LIN driver, implement deviation from LIN standard • Design, develop, test software for Ultrasonic Interior Protection module • Test software on real HW • Manufacturing support (MPI file, ICT, EOL)

Used Technologies

• 8-bit μ C NEC (78K0/KC2)	• in-circuit emulator	• software revision control system: Telelogic CM/Synergy and Change/Synergy
• Assemblers	• integrated debugger ID78K0-QB	• SPICE
• AUTOSAR	• Perl	
• C	• requirements definition and management: Telelogic DOORS	

Software Verification 07.06 - 06.07

Company Name:	Verocel-Polska Sp. z o.o, Poznan, Poland
Company Description:	VEROCEL is a company which provides expertise and services for Software Verification. This includes development and review of plans and standards, development of tests, software structural coverage analyses, traceability, and outsource support for software verification activities.

Project Introduction: Verification of maps application according to DO-178B (level C)

Position/-s Held: Tester-Programmer

Responsibilities:

- verify software in the safety critical software industry in accordance with DO-178B
- analyze source code, write tests for the specified requirements, test and verify the tools for coverage analysis
- analyze incompletely covered source code (written in C language) and disassembled code (PowerPC instructions)

Used Technologies

- Ada
- C
- PowerPC
- VxWorks OS

MPEG2 TO AVC TRANSCODER

11.05 - 03.06

Company Name: Poznan University of Technology, Institute of Electronics and Telecommunication

Project Introduction: MPEG-2 to AVC transcoding allows converting an MPEG-2 video bitstream to another bitstream that is fully compliant to the H.264/AVC video coding standard. Goal of the project: Increasing of compression ratio equals to 70 percent of input MPEG-2 bitstream. Memory: 16MB of RAM required. Achievements: The transcoding process introduces some quality deteriorations in comparison to the input MPEG-2 encoded sequence (difference PSNR of Y equals 2dB).

Position/-s Held: C programmer

Used Technologies

- Assemblers
- C
- Microsoft Visual Studio

H.263 CODEC

03.05 - 11.05

Company Name: Poznan University of Technology, Institute of Electronics and Telecommunication

Project Introduction: H263 codec with 5 optional modes, which are described as annexes: D, F, I, T, U. Implementation of following modules: 1) Rate control algorithm 2) Encapsulation into RTP packets 3) Error protection 4) RTP error detection and concealment 5) API function for controlling of the encoding and decoding process Goal of the project: power consumption less than 10% processor power (CIF format)

Position/-s Held: SW developer

Responsibilities:

- Bitstream formatter for encoder (high-optimized, ASM)
- Encapsulation into RTP packets
- Implementation and conception of error protection (decoder, ASM)
- RTP error detection and concealment
- Testing on hardware board (TMS320C64xx)

Used Technologies

- Assemblers
- C
- Code Composer Studio
- DSP microprocessors
- Hardware: TMS320C642x (fixed point processor)
- Microsoft Visual Studio

AVC ENCODER AND DECODER

10.04 - 03.05

Company Name:	Poznan University of Technology, Institute of Electronics and Telecommunication
Project Introduction:	AVC encoder and decoder on PC platform Goal: real-time performance for decoder, high performance optimization for encoder
Position/-s Held:	SW developer
Responsibilities:	<ul style="list-style-type: none"> • Testing, finding and removing bugs in core AVC encoder (Intel ASM) • DirectShow filter implementation for AVC encoder and decoder • Property page window for AVC encoder filter (GOP, search range of motion vectors, number of slice, number of threads, quantization parameter, group of pictures, fields, deblocking loop filter, etc.) • DirectShow filters implementation for encoding, transmission over TCP-IP, decoding (real-time system), color space converter

Used Technologies

- C
- C++
- Intel ASM (processor: Prescott)
- Microsoft Visual Studio
- Microsoft DirectShow 9.0

DECODER AAC

06.04 - 10.04

Company Name:	Poznan University of Technology, Institute of Electronics and Telecommunication
Project Introduction:	Decoder AAC-LC and AAC-HE on fixed-point processor with following modules: <ul style="list-style-type: none"> • SBR module (Spectral Band Replication) • Error protection and concealment in AAC-LC • Error protection and concealment in AAC-SBR
Position/-s Held:	SW developer
Responsibilities:	<ul style="list-style-type: none"> • Implementation of bitstream parser for SBR data (ASM) • Description and testing of SBR technique (explanation of the SBR decoding algorithm in detail, including both theoretical background and motivation, examination of MPEG-4 SBR technique shortcomings) • Float value representation in fixed-point processor (implementation of addition and multiplication), FFT, error estimation of fixed-point implementation (ASM) • Implementation of analysis and synthesis QMF filters, using DCT and FFT (ASM)

Used Technologies

- Assemblers
- C
- Code Composer Studio
- Hardware: TMS320C642x (fixed point processor)

Online Institute Library

02.04 - 06.04

Company Name:	Institut Allgemeine Nachrichtentechnik, Universität Hannover, Hannover
Project Introduction:	Online library has following features: - option 1 - option 2 - option 3. multiple criteria search, sorting the answers into different order user accounts (logging, password changing, personal data editing, mail sending to another user and administrator, books ordering) administrator accounts (full access to database,

users monitoring) hackers resistant (removing dangerous query) user-friendly website layout, users asking before command execution

Position/-s Held: PHP programmer

Used Technologies

- HTTP
- Iterative Model
- MySQL 4
- PHP
- Structural Programming

I. Wyrażam zgodę na przetwarzanie moich danych osobowych przez „Power Media” S.A. z siedzibą we Wrocławiu w ramach stworzonej przez nią bazy /baz/ danych osób o określonych specjalnościach /umiejętnościach/ i do przekazywania tych danych podmiotom poszukującym osób do pracy /w tym podmiotom zagranicznym/.

II. „Power Media” S.A. z siedzibą we Wrocławiu, ul. Kiełbaśnicza 24, 50-110 Wrocław, wpisana do Krajowego Rejestru Sądowego / rejestru przedsiębiorców/ pod numerem 0000281947, informuje, iż ma Pan/Pani prawo wglądu do swoich danych osobowych oraz do ich poprawiania.