

Bottleneck Architecture: Efficient Design

PURPOSE

Reduce computational cost while maintaining expressiveness

Bottleneck Structure



DIMENSIONAL FLOW EXAMPLE

256 channels → **64** channels → **64** channels → **256** channels



Computational Savings

~70% fewer FLOPs



Trade-off

More layers, fewer ops



Used In

ResNet, Inception, etc.

Modern Standard: Almost all efficient architectures use bottlenecks