William Powers MS EE

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Summary

A dynamic and results-drive Electrical Engineering MS student graduating early 2026 who is seeking a research and development role where I can leverage my experience in adaptive signal processing and embedded development. With a strong background in estimation and detection theory, advanced optimization techniques, machine learning, and statistical modeling, I am well-equipped to deliver innovative, industry-grade solutions and excel in a fast-paced environment.

Experience

SIGNAL PROCESSING ENGINEER | MIT LINCOLN LABORATORY **MAY 2025 - PRESENT**

Specializing in the development of space-time adaptive processing (STAP) algorithms and their real-time deployment on embedded FPGA and GPU platforms under the Advanced Sensor Systems & Test Beds Group. This role involves design and optimization of signal processing techniques for radar and electronic sensing applications to support advanced U.S. defense capabilities.

RF SYSTEMS ENGINEER | KANSAS APPLIED RESEARCH LAB **JAN 2024 - PRESENT**

Developed advanced radar waveform generation algorithms utilizing signal digital processing, random process analysis, and information theory techniques. Implemented a variety of frontend radar architectures using modern Software-Defined Radio and FPGA platforms. Applied machine learning methodologies to address classification problems and distortion modeling within the radar transmit-receive framework.

EMBEDDED SYSTEMS DEVELOPER | CRESIS **MAY 2023 - JAN 2024**

Collaborated with a small team to develop and deploy embedded radar systems using a combination of FPGA, GPU, and CPU real-time processing techniques. Included hands on experience with the Xilinx embedded development platform, application of digital signal processing algorithms, and hardware implementation in C and HDL languages.

Education

MASTER OF SCIENCE | UNIVERSITY OF KANSAS **MAY 2024 – MAR 2026**

Major: Electrical Engineering | GPA: 4.00

Coursework: Adaptive Signal Processing, Random Process Theory and Estimation, Machine Learning, Optimization, Scientific Computing, Embedded Digital Signal Processing, and Implementation

BACHELOR OF SCIENCE | UNIVERSITY OF KANSAS **AUG 2020 - MAY 2024**

Major: Computer Engineering | Academic Honors

Skills

FPGA Development, C, CUDA, Python, TensorFlow, PyTorch, Optimization, DSP, Probabilistic Modeling