2764
27128 <-> 27512
27256

2764 <-> 27256
27128   27512

R7 1k   JP1   JP2   A14 A15.0   A14.0
R9 1k   JP5   JP6   A14 A15.1   A14.1
R11 1k JP9   JP10 A14 A15.2   A14.2
R13 1k JP13 JP14 A14 A15.3   A14.3

{-CS[0..7] -OE}

BANK #0 U12   BANK #1 U14   BANK #2 U16   BANK #3 U18
BANK #4 U13   BANK #5 U15   BANK #6 U17   BANK #7 U19

74LS138 U1   R2 1k +5V C3 100n
74LS138 U9   C11 100n +5V -OE
74LS173 U5   C6 100n +5V
74LS173 U6   C7 100n
74LS173 U7   C8 100n
74LS173 U10 C12 100n +5V
74LS173 U11 C13 100n
74LS245 U2   R1 1k +5V C4 100n
74LS245 U3   C5 100n +5V

PIO/2   J3   J1   J5
{AD[0..7] BD[0..7]}
{DM[0..2]}
{-E[0..4] CLK RST}
{DA[0..3]}
{D[0..7]}
{A[0..15] A14.[0..7] A15.[0..7]}

U12 C14 100n +5V
U14 C16 100n +5V
U16 C18 100n +5V
U18 C20 100n +5V
U13 C15 100n +5V
U15 C17 100n +5V
U17 C19 100n +5V
U19 C21 100n +5V

-E0 -E1 -E2 -E3 -E4
-CS0 -CS1 -CS2 -CS3 -CS4 -CS5 -CS6 -CS7
DS0 DS1 DS2 DS3

J2
PS
SWITCH "POWER ON"
L1 100n L2
C1 C2 220u
J4
U4 DS1833 RESET VCC GND
-> +5V

U8 74LS122
R3 33k   C10 100n +5V
C9 10u
R/Cext Rint Cext
A1 A2 B1 B2 Clr
R4 470 R5 470 R6 470
D1 "RD" D2 "ON"

R8 1k   JP3 JP4   A14.4 A14 A15.4   +5V
R10 1k JP7 JP8   A14.5 A14 A15.5   +5V
R12 1k JP11 JP12 A14.6 A14 A15.6   +5V
R14 1k JP15 JP16 A14.7 A14 A15.7   +5V

2764 27128 <-> 27256 27512
2764 27128 <-> 27512 27256