A

B

NMOS
M21

A

B

NMOS
M22

C

NMOS
M1

C

NMOS
M2

A

NMOS
M19

C

NMOS
M3

A

B

NMOS
M20

C

NMOS
M4

VDD

PMOS
M7

VDD

PMOS
M8

VDD

M9
PMOS

M6
NMOS

Sum_bar

VDD

M10
PMOS

M5
NMOS

Sum

B

A

NMOS
M11

A

B

NMOS
M12

VDD

NMOS
M15

NMOS
M16

C

NMOS
M23

C

NMOS
M24

VDD

PMOS
M31

VDD

M29
PMOS

M28
NMOS

Carry_bar

A

B

NMOS
M13

A

B

NMOS
M14

B

NMOS
M17

A

NMOS
M18

C

NMOS
M25

C

NMOS
M26

PMOS
M32

VDD

M30
PMOS

M27
NMOS

Carry

A

A

PULSE(1.8 0 0 2p 2p 50n 100n)

A

A1

PULSE(0 1.8 0 2p 2p 50n 100n)

B

B

PULSE(1.8 0 0 2p 2p 100n 200n)

B

B1

PULSE(0 1.8 0 2p 2p 100n 200n)

C

C

PULSE(1.8 0 0 2p 2p 200n 400n)

C

C1

PULSE(0 1.8 0 2p 2p 200n 400n)

VDD

V
1.8

.INCLUDE tsmc018.lib

.tran 400n

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