

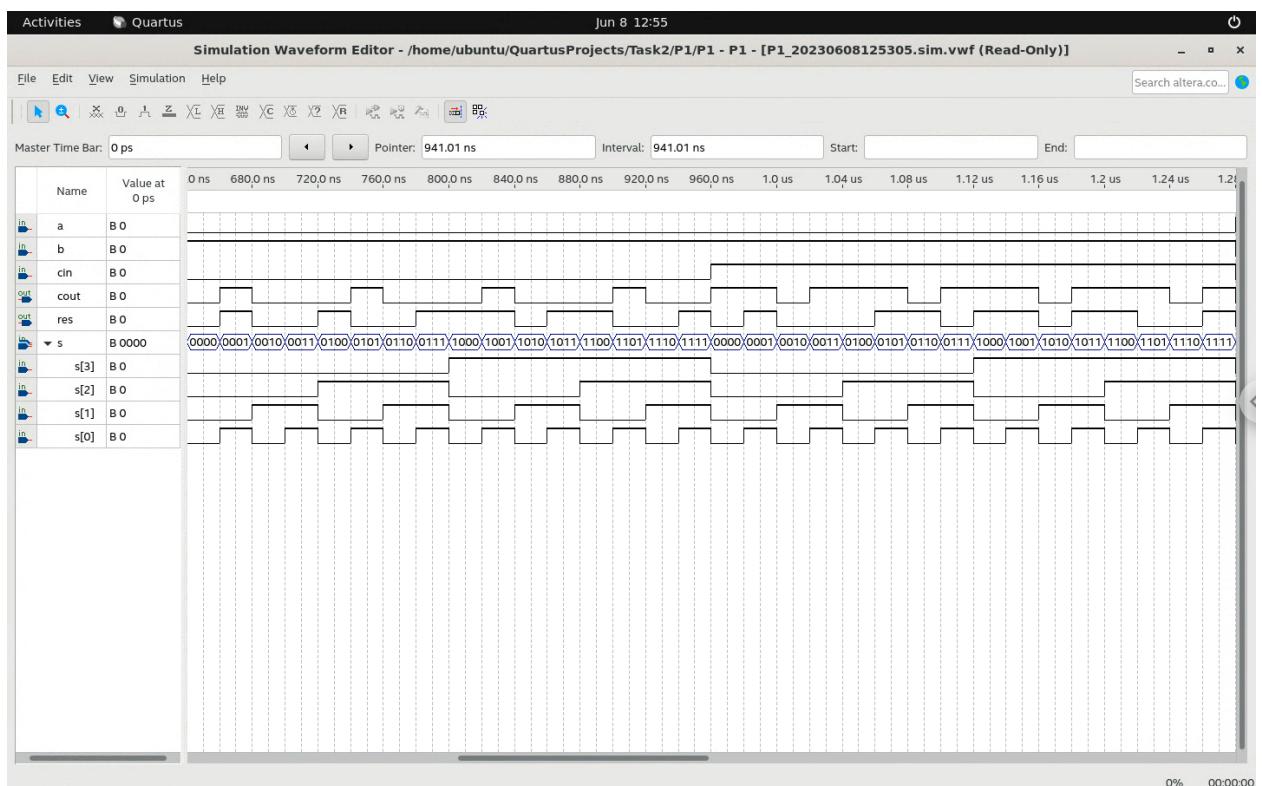
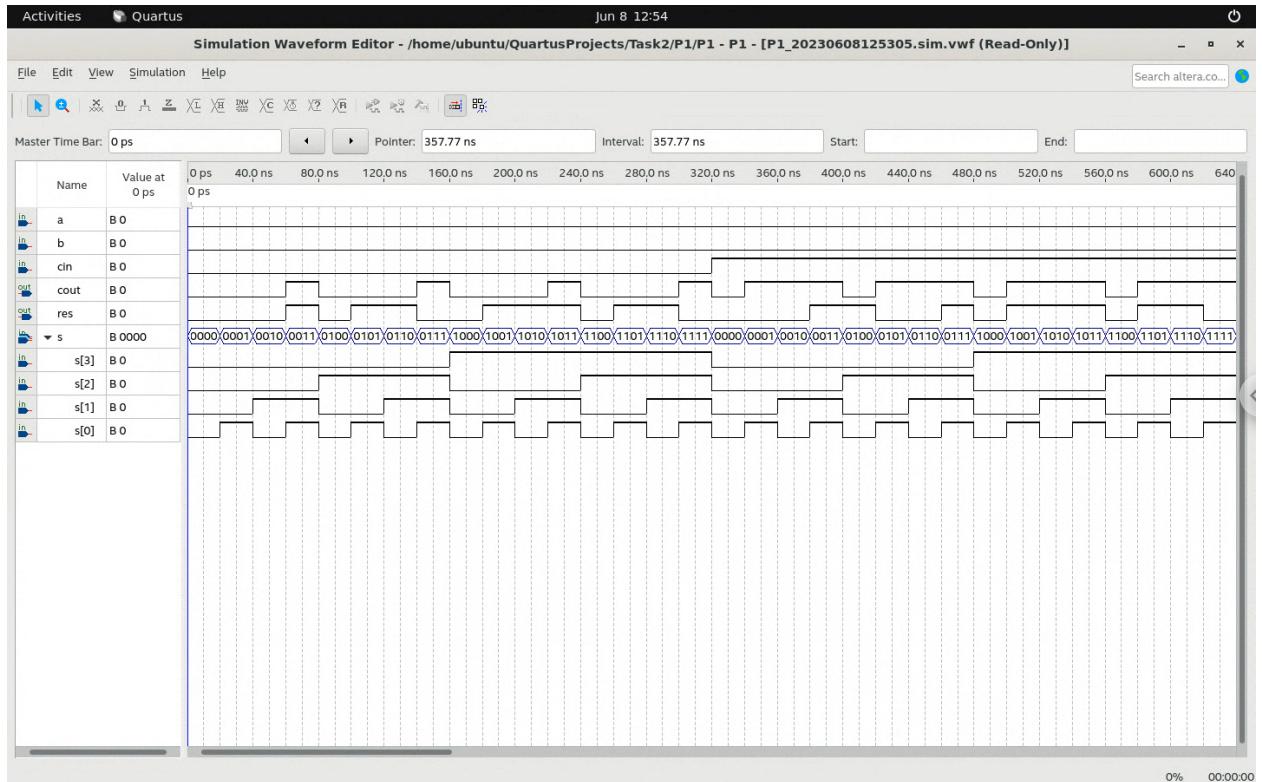
**Σχεδίαση Ψηφιακών Συστημάτων**

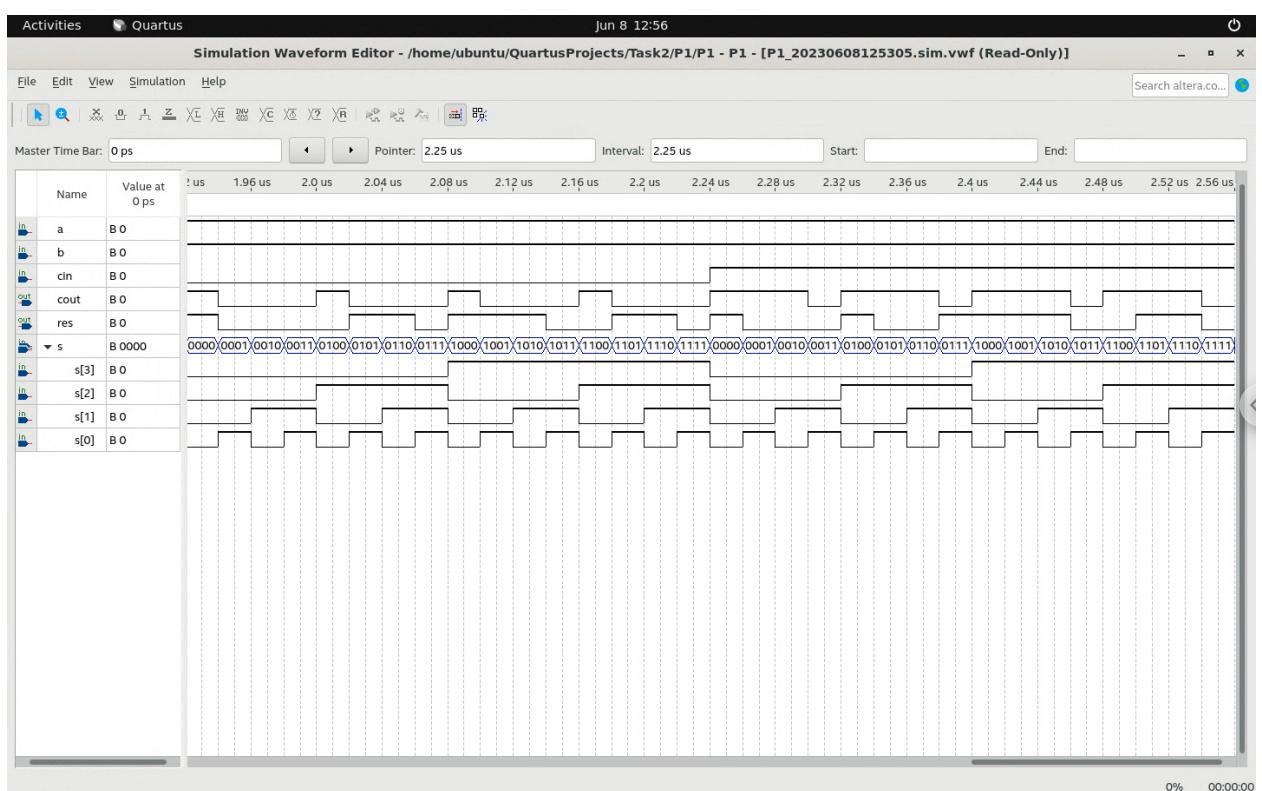
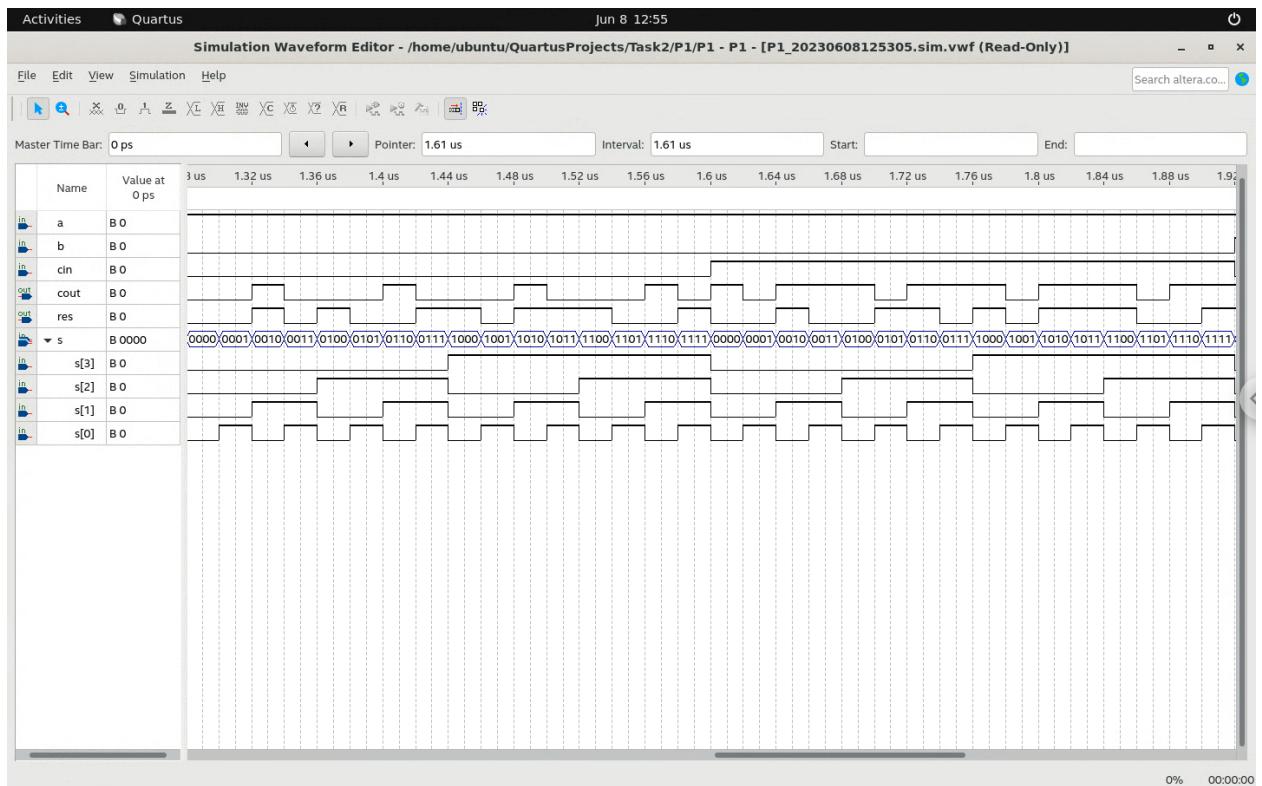
**Εργασία 2**

**Μέλη ομάδας**

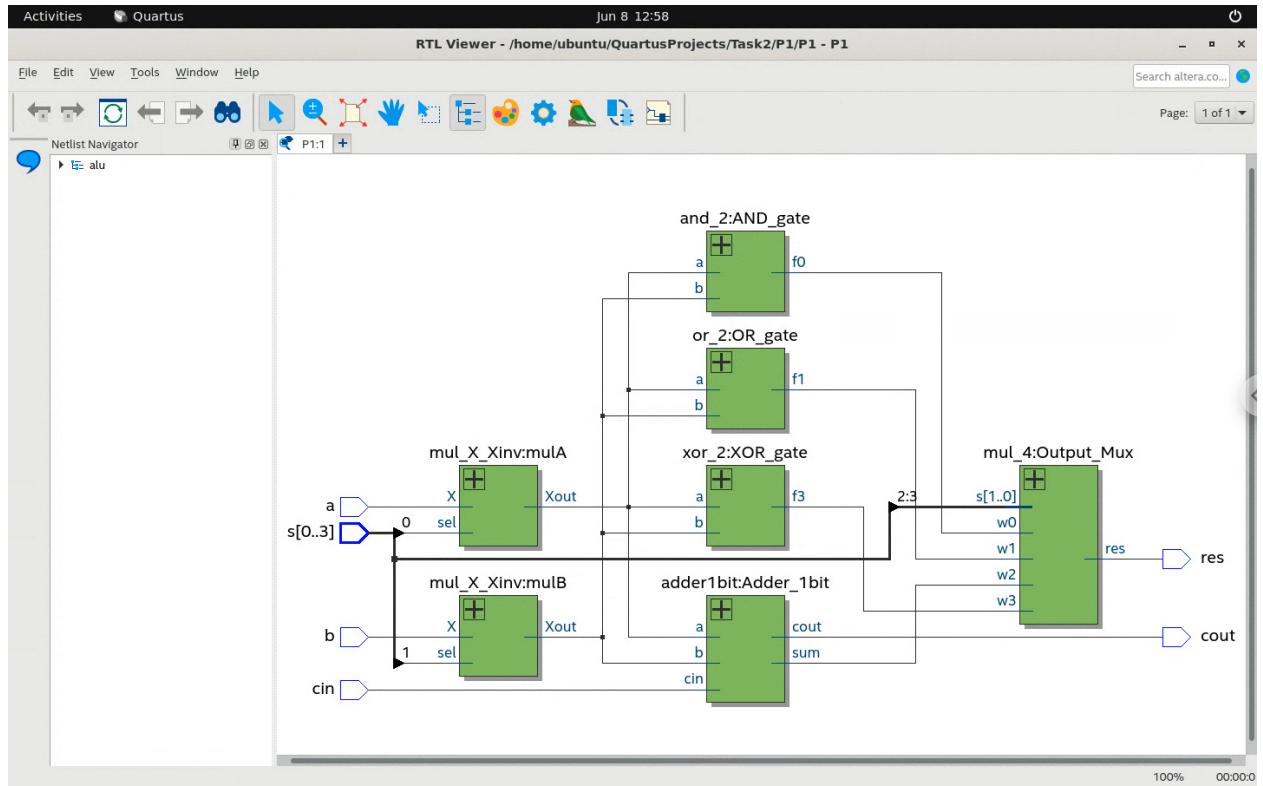
<b>ΑΜ</b>	<b>Όνομα</b>	<b>E-mail</b>
3220120	ΜΑΤΣΑΣ ΜΑΡΙΟ	matsamarios@gmail.com
3220150	ΠΑΠΑΔΗΜΑΣ ΒΑΣΙΛΕΙΟΣ	dsd@excession.gr
3220281	ΠΑΠΑΓΕΩΡΓΙΟΥ ΠΑΝΑΓΙΩΤΗΣ	panagot94@gmail.com

# ALU1



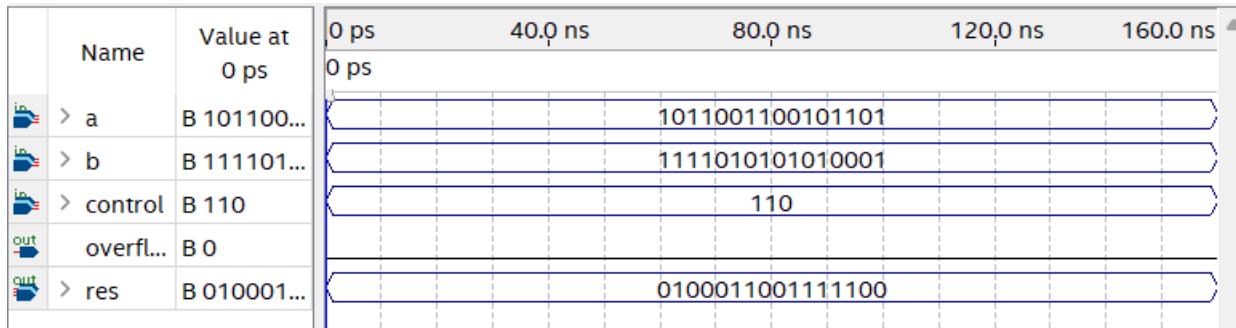


# RTL

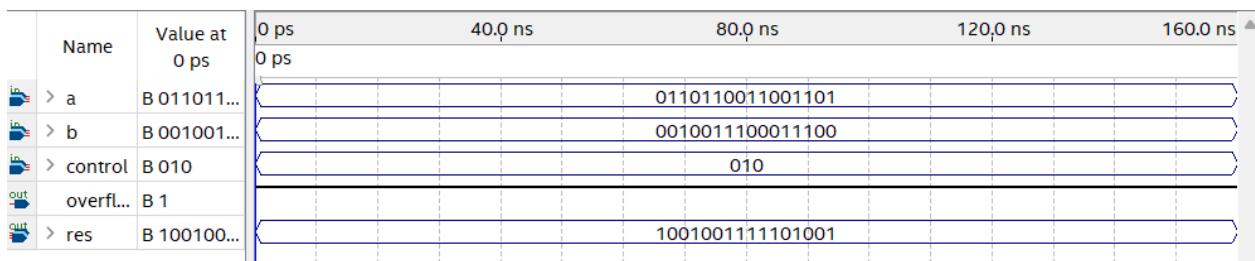


## ALU16

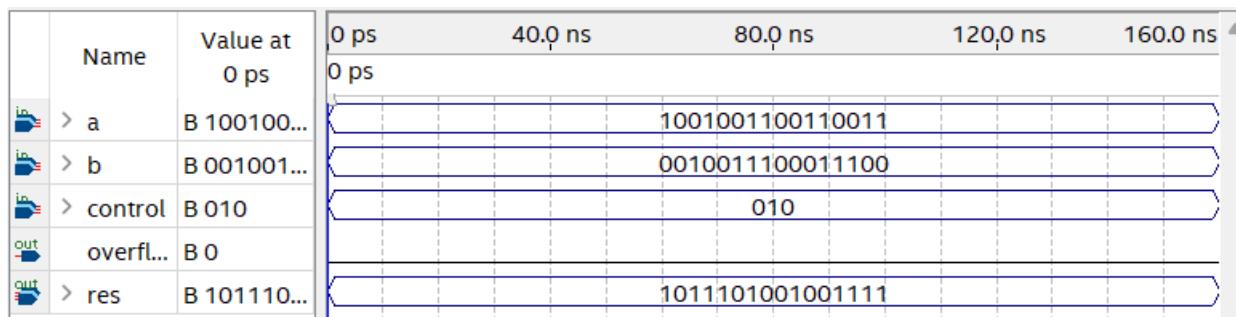
XOR A-B



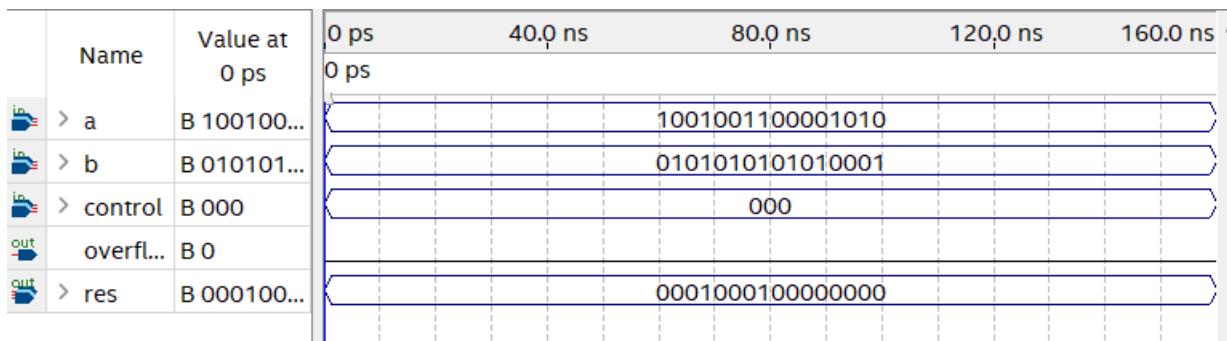
ADD A-B



ADD C-D



AND A-B



### NAND A-B

	Name	Value at 0 ps	0 ps	40.0 ns	80.0 ns	120.0 ns	160.0 ns
in > a		B 101100...	0 ps		1011001100101101		
in > b		B 111101...			1111010101010001		
in > control		B 101				101	
out overflow		B 0					
out > res		B 010011...			0100111011111110		

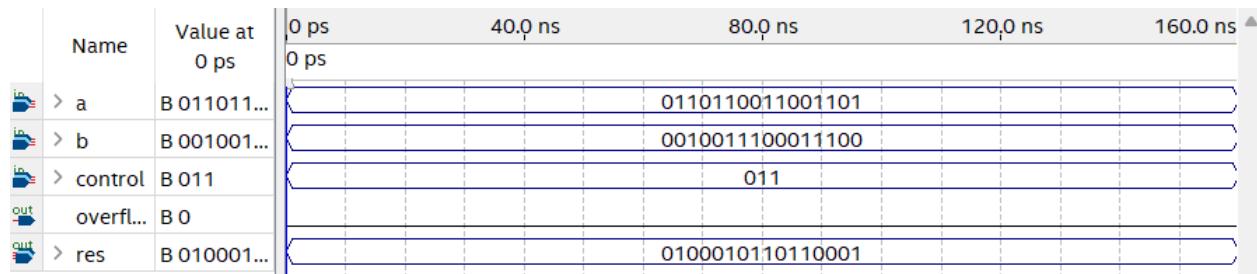
### OR A-B

	Name	Value at 0 ps	0 ps	40.0 ns	80.0 ns	120.0 ns	160.0 ns
in > a		B 100100...	0 ps		1001001100001010		
in > b		B 010101...			0101010101010001		
in > control		B 001				001	
out overflow		B 0					
out > res		B 110101...			1101011101011011		

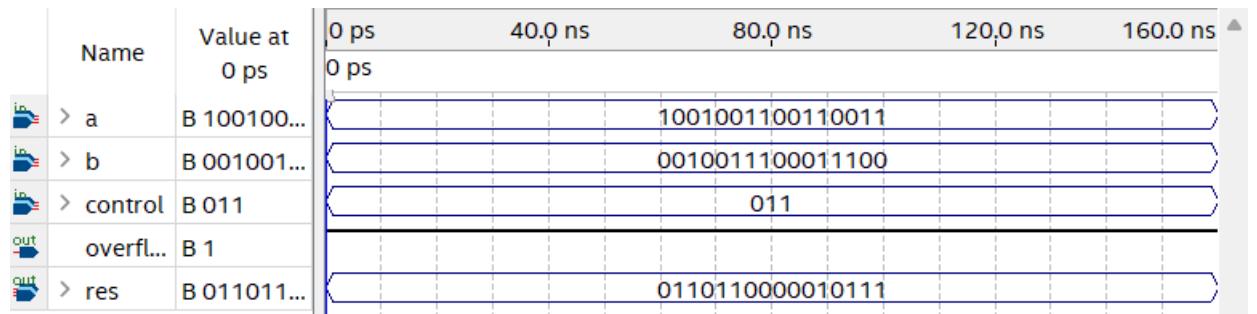
### NOR A-B

	Name	Value at 0 ps	0 ps	40.0 ns	80.0 ns	120.0 ns	160.0 ns
in > a		B 101100...	0 ps		1011001100101101		
in > b		B 111101...			1111010101010001		
in > control		B 100				100	
out overflow		B 0					
out > res		B 000010...			0000100010000010		

### SUB A-B



### SUB C-D



## RTL DIAGRAM

