## Adder:

АВ	Cin	Cout	Sum	ExCout	ExSum
0 0	0	0	0	0	0
0 0	1	0	1	0	1
0 1	0	0	1	0	1
0 1	1	1	0	1	0
10	0	0	1	0	1
1 0	1	1	0	1	0
1 1	0	1	0	1	0
1 1	1	1	1	1	1

## Decoder:

En	Α0	A1	00	01	02	03		Expected Output
0	0	0	0	0	0	0	ĺ	All false
0	1	0	0	0	0	0		All false
0	0	1	0	0	0	0		All false
0	1	1	0	0	0	0	ĺ	All false
1	0	0	1	0	0	0		00 Only
1	1	0	0	1	0	0		01 Only
1	0	1	0	0	1	0		02 Only
1	1	1	0	0	0	1	ĺ	03 Only

## Multiplexer:

Α0	Α1		in0	in1	in2	in3	EX_OUT   Ou	ıt
0	0		1	0	0	Θ	1   1	L
0	1		0	1	0	Θ	1   1	L
1	0	ĺ	0	0	1	0	1   1	L
1	1	ĺ	0	0	0	1	1   1	L
0	0		0	1	1	1	0   0	)
0	1		1	0	1	1	0   0	)
1	0	Ĺ	1	1	0	1	0   0	)
1	1	ĺ	1	1	1	0	0   0	)