CSC 3101- Spring Summer 2016
Lab Assignment 1

Please read the requirements carefully. Any missing point will result in a grade reduction. The **deadline** is 11:00pm of the day before next class and this homework has to be submitted to your **blackboard**. Hand in paper is **not accepted**.
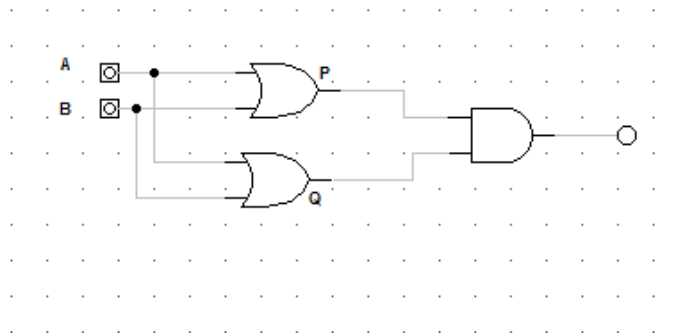I **strongly suggest** you submit it at least 10 minutes **earlier** in case any kind of accidents would happen like network delay.
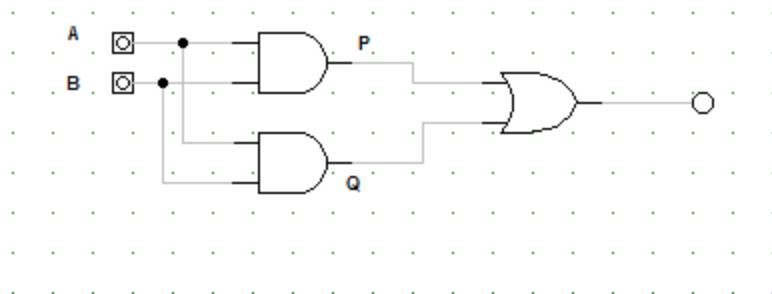
Question1 (100)

Requests:
1. Write down the logic expressions of the circuits below, and fill out the truth tables with your expressions. Then, finishing the circuits on Logicly and correcting your truth table with the results you collect.
2. Attach the screenshot of your digital circuit. An example of the screenshot is listed below.
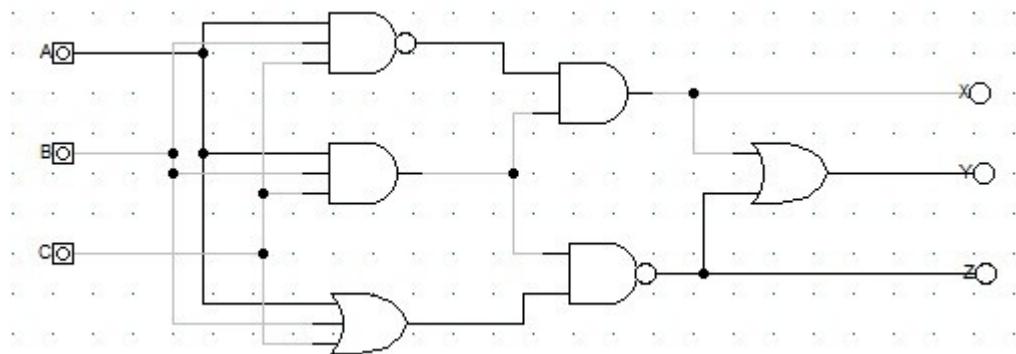
A) (30)

Inputs		Intermediate Values		Output
A	B	P	Q	
0	0			
0	1			
1	0			
1	1			

B) (30)

Inputs		Intermediate Values		Output
A	B	P	Q	
0	0			
0	1			
1	0			
1	1			

C) (40)

Inputs			Output		
A	B	C	X	Y	Z
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	1			