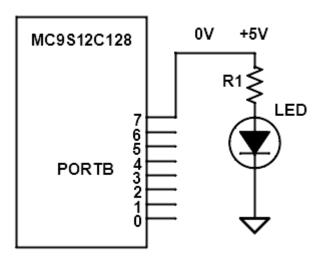
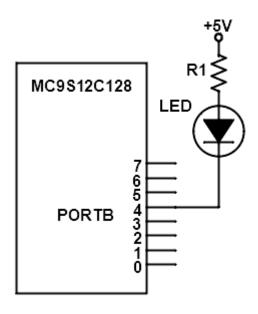
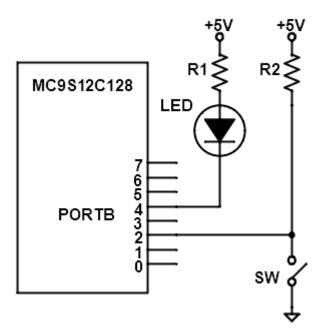
Understanding LED Circuits



Pull Up



Pull Down



Switch Circuit

Understanding Homework 2 Program

```
main.asm - Notepad
File Edit Format View Help
Title:
                 LED Light Blinking
                 CSE472 Homework 2 sample program
 Objective:
                 (in-class-room demonstration)
  Revision:
                 V3.1
  Date:
                 Aug. 17, 2016
                 Kyusun Choi
  Programmer:
                 The Pennsylvania State University
  Company:
                 Department of Computer Science and Engineering
                 Simple Parallel I/O in a nested delay-loop, demo
  Algorithm:
                 A: Light on/off state and Switch SW1 on/off state
  Register use:
                 X,Y: Delay loop counters
                 RAM Locations from $3000 for data,
 Memory use:
                              from $3100 for program
  Input:
                 Parameters hard coded in the program,
                 Switch SW1 at PORTP bit 0
  Output:
                 LED 1,2,3,4 at PORTB bit 4,5,6,7
  Observation:
                 This is a program that blinks LEDs and blinking period can
                 be changed with the delay loop counter value.
 Note:
                 All Homework programs MUST have comments similar
                 to this Homework 2 program. So, please use those
                 comment format for all your subsequent CMPEN472
                 Homework programs.
                 Adding more explanations and comments help - you and
                 others to understand your program later.
                 This program is developed and simulated using CodeWorrior
  Comments:
                 development software and targeted for Axion
                 Manufacturing's APS12C128 board (CSM-12C128 board
                 running at 24MHz bus clock.
  *************************
  Parameter Declearation Section

    Export Symbols

           XDEF
                                     ; export 'pgstart' symbol
                       pgstart
           ABSENTRY
                       pgstart
                                     ; for assembly entry point
```

```
main.asm - Notepad
File Edit Format View Help

    Data Section

           ORG
                       $3000
                                     ; reserved RAM memory starting address
                                         Memory $3000 to $30FF are for Data
Counter1
           DC.W
                       $4fff
                                     ; initial X register count number
Counter2
           DC.W
                       $0020
                                      ; initial Y register count number
StackSpace
                                      Adjust this number for 1 sec. blink
                                      on your computer. $0040 will
                                      increase delay by twice long.
                      ****************

    Program Section

           ORG
                      $3100
                                     ;Program start address, in RAM
           LDS
                                     ; initialize the stack pointer
pgstart
                      #pgstart
           LDAA
                      #%11110000
                                     ; set PORTB bit 7,6,5,4 as output, 3,2,1,0 as input
           STAA
                      DDRB
                                     ; LED 1,2,3,4 on PORTB bit 4,5,6,7
                                     ; DIP switch 1,2,3,4 on PORTB bit 0,1,2,3.
           BSET
                      PUCR,%00000010
                                     ; enable PORTB pull up/down feature, for the
                                     ; DIP switch 1,2,3,4 on the bits 0,1,2,3.
           BCLR
                      DDRP, %00000011 ; Push Button Switch 1 and 2 at PORTP bit 0 and 1
                                     ; set PORTP bit 0 and 1 as input
           BSET
                      PERP,%00000011
                                     ; enable the pull up/down feature at PORTP bit 0 and 1
                                     ; select pull up feature at PORTP bit 0 and 1 for the
           BCLR
                      PPSP,%00000011
                                     ; Push Button Switch 1 and 2.
           LDAA
                      #%11110000
                                     ; Turn off LED 1,2,3,4 at PORTB bit 4,5,6,7
           STAA
                      PORTB
                                     ; Note: LED numbers and PORTB bit numbers are different
mainLoop
           BSET
                      PORTB,%10000000; Turn off LED 4 at PORTB7
                                     ; Wait for 1 second
           JSR
                      delay1sec
           BCLR
                      PORTB,%10000000 ; Turn on LED 4 at PORTB7
                                     ; Wait for 1 second
           JSR
                      delay1sec
           LDAA
                      PTIP
                                     ; read push button SW1 at PORTP0
           ANDA
                      #%00000001
                                     ; check the bit 0 only
           BEO
                      sw1pushed
sw1notpsh
           BSET
                      PORTB, %00010000; turn OFF LED1 at PORTB4
           BRA
                                     ; loop forever!
                      mainLoop
                      PORTB, %00010000; turn ON LED1 at PORTB4
sw1pushed
           BCLR
           BRA
                      mainLoop
                                     ; loop forever!
```

```
main.asm - Notepad
File Edit Format View Help
* Subroutine Section
; delay1sec subroutine
 Please be sure to include your comments here!
delay1sec
          PSHY
          LDY
               Counter2
                                 ; long delay by
dly1sLoop
          JSR
               delay1ms
                                 ; Y * delay1ms
          DEY
          BNE
               dly1sLoop
          PULY
          RTS
; delay1ms subroutine
; This subroutine cause a few msec. delay
 Input: a 16bit count number in 'Counter1'
; Output: time delay, cpu cycle waisted
; Registers in use: X register, as counter
; Memory locations in use: a 16bit input number in 'Counter1'
; Comments: one can add more NOP instructions to lengthen
          the delay time.
delay1ms
          PSHX
          LDX
               Counter1
                                 ; short delay
dlymsLoop
          NOP
                                 ; X * NOP
          DEX
          BNE
               dlymsLoop
          PULX
          RTS
* Add any more subroutines here
          end
                                  ;last line of a file
```

Stack Memory

HC12 Instruction Set: JSR, BSET, BCLR, ANDA, PSHX, PULX

More Addressing Modes

More Program Flow Chart

Light Blinking Variation / Program Variation

What happen if you run the following program?

Loop:

LDAA #%10000000

STAA PORTB JSR delay1sec

LDAA #%01000000

STAA PORTB JSR delay1sec

LDAA #%00100000

STAA PORTB JSR delay1sec

LDAA #%00010000

STAA PORTB JSR delay1sec

BRA Loop