

Sheetname: CPU

CPU MC68000
CLOCK 16MHZ
DATA AND ADDRESS BUFFERS
HALT AND RESET
DTACK GLUE LOGIC
INTERRUPT

Sheetname: MEMORY

ROM 16kBytes
RAM 1MBytes

Sheetname: FOUR UARTS

TG16C554
4 UARTS.

Arquivo: fourUarts.kicad_sch

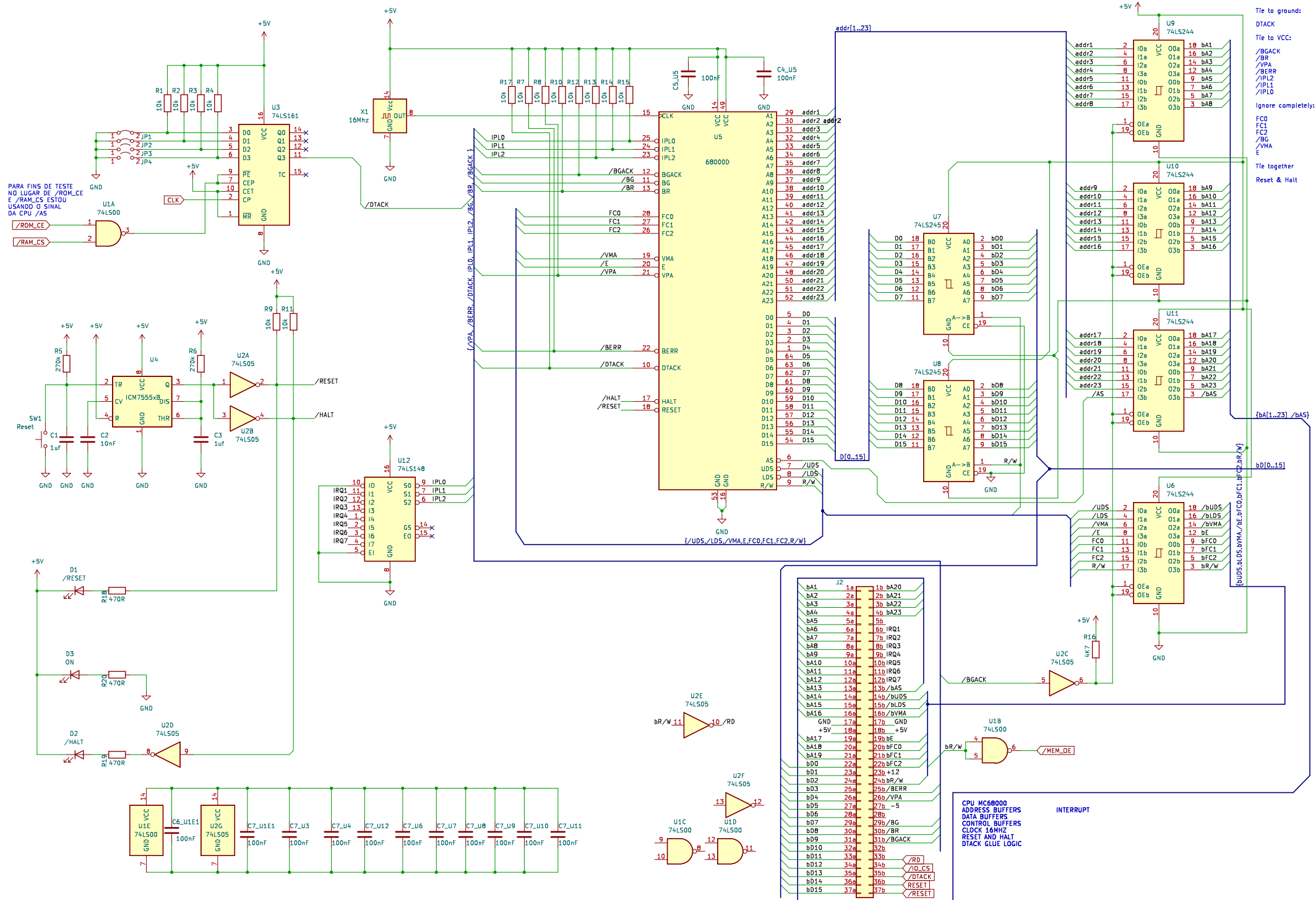
Sheetname: cpuNoBuffer

CPU MC68000
CLOCK 16MHZ
HALT AND RESET
DTACK GLUE LOGIC
INTERRUPT

Arquivo: cpuNoBuffer.kicad_sch

UART CONTROL

Arquivo: uart_control.kicad_sch

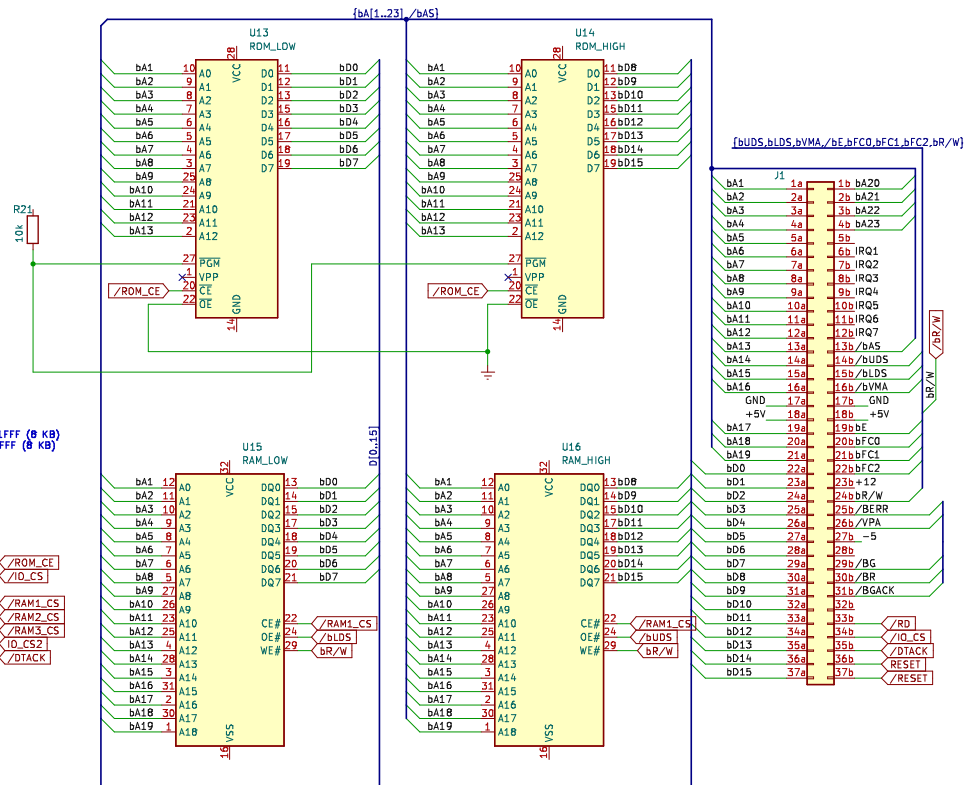
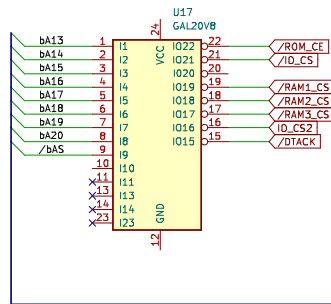
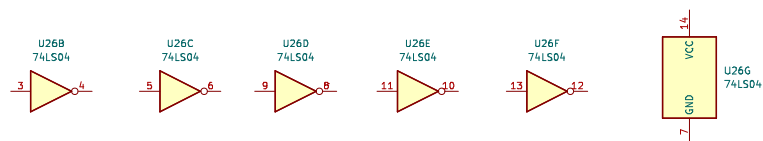


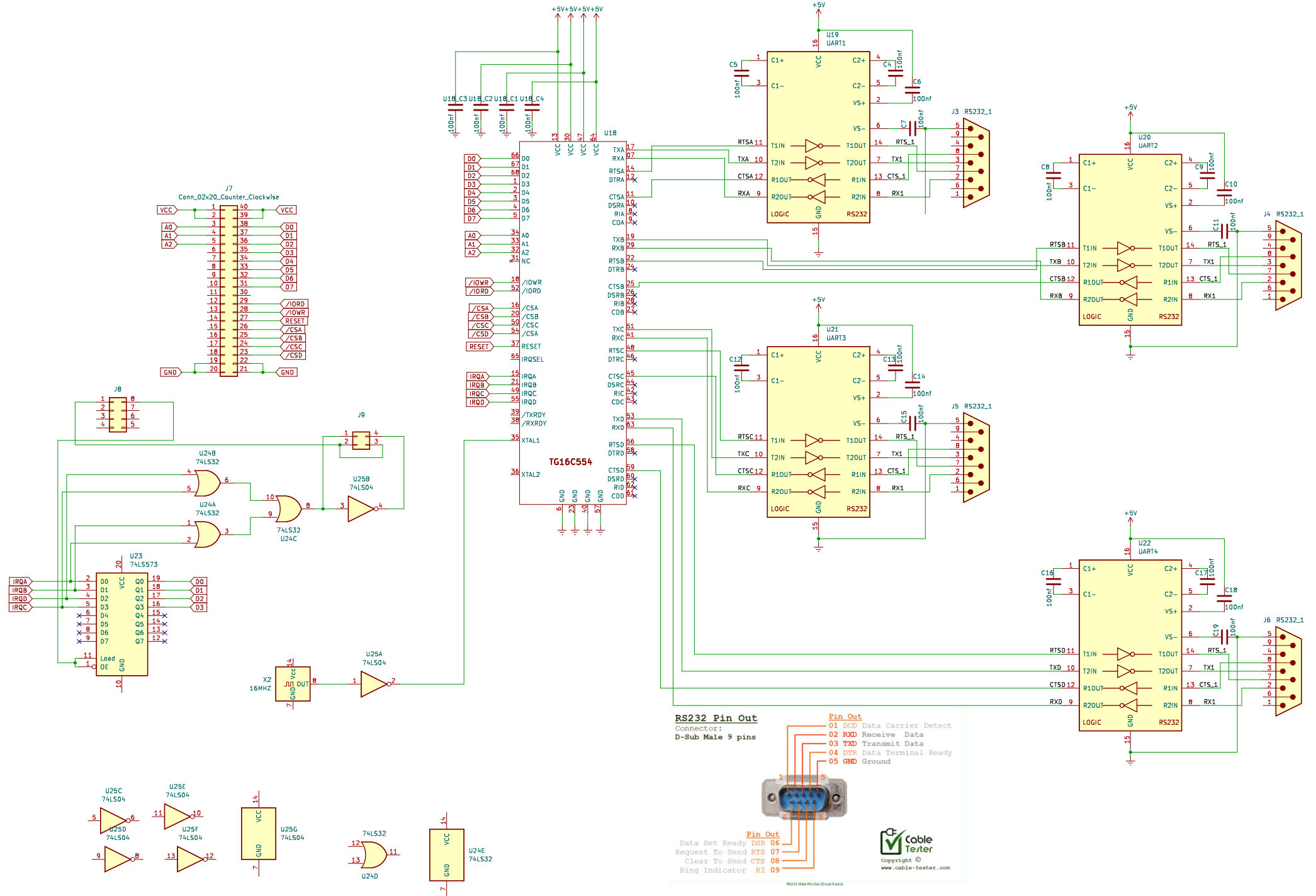
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PINOS DE SAÍDA
IROM_CS PIN 19 : ROM (0x00000-0x01FFF)
IIO_CS PIN 18 : I/O (0x20000-0x03FFF)
IIRAM4_CS PIN 17 : RAM1 (0x004000-0x07FFFF)
IRAM1_CS PIN 16 : RAM2 (0x080000-0x0FFFFFF)
IRAM2_CS PIN 16 : RAM2 (0x100000-0x17FFFF)
IRAM3_CS PIN 16 : RAM2 (0x180000-0x1FFFFFF)

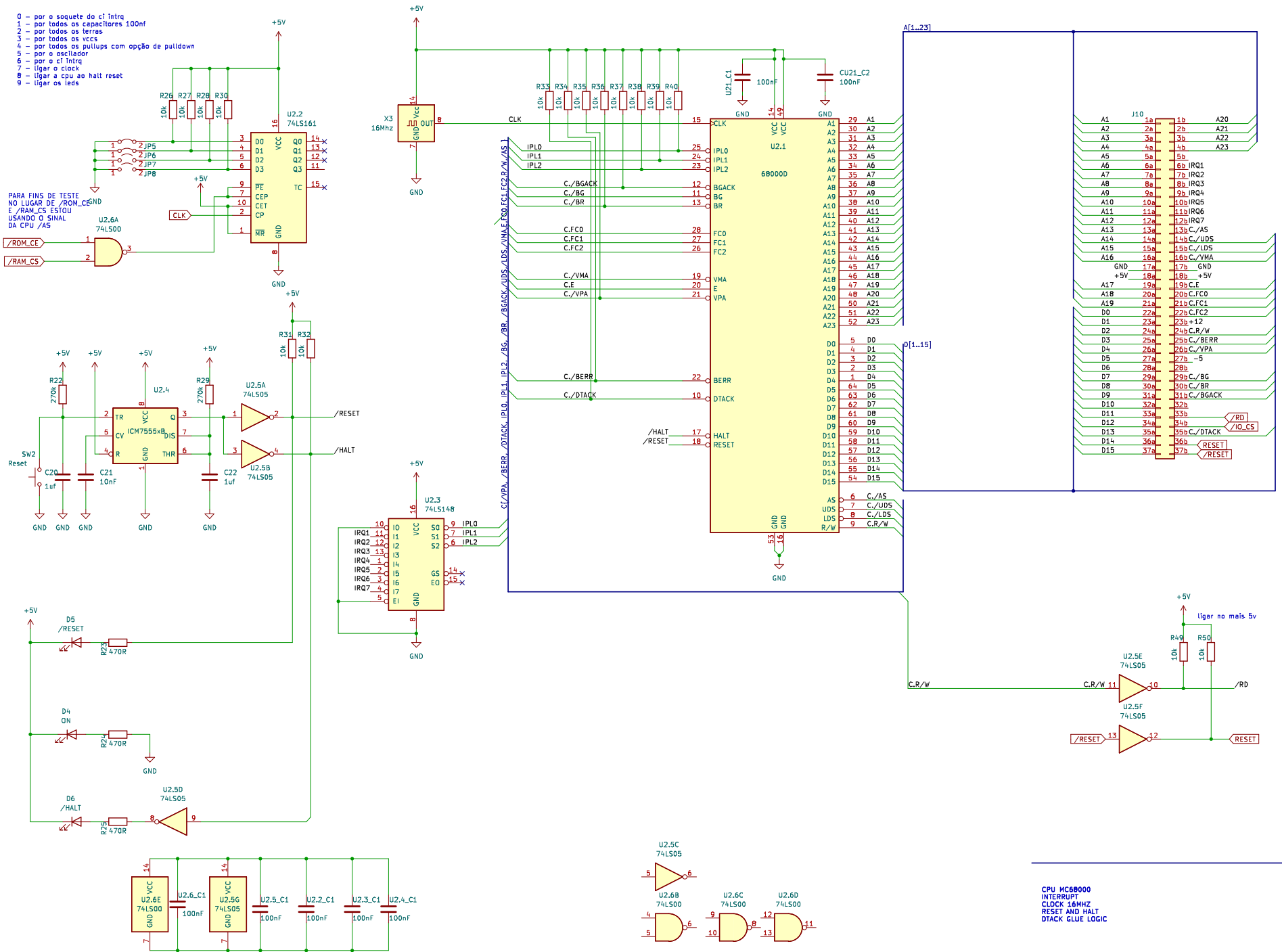
EQUAÇÕES
IIRAM4_CS = IAS & I20 & I19 & I18 & I17 & I16 & I15 & I14 & I13 : 0x00000-0xFFFF (8 KB)
IIO_CS = IAS & I20 & I19 & I18 & I17 & I16 & I15 & I14 & A13 : 0x20000-0x3FFF (8 KB)
IRAM4_CS = IAS & I20 & I19 : 0x004000-0x07FFFF (20-0 e A19=0, exceto R0 em I/O)
IRAM1_CS = IAS & I20 & A19 : 0x080000-0x0FFFFFF (20-0 e A19=1)
IRAM2_CS = IAS & A20 & I19 : 0x100000-0x17FFFF (20-1 e A19=0)
IRAM3_CS = IAS & A20 & A19 : 0x180000-0x1FFFFFF (20-1 e A19=1)

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[illegible]



- 0 - por o soquete do ci Intrq
- 1 - por todos os capacitores 100nF
- 2 - por todos os terras
- 3 - por todos os vccs
- 4 - por todos os pullups com opção de pulldown
- 5 - por o oscilador
- 6 - por o ci Intrq
- 7 - ligar o clock
- 8 - ligar a cpu ao halt reset
- 9 - ligar os leds



CPU MC68000
 INTERRUPT
 CLOCK 16MHZ
 RESET AND HALT
 DTACK GLUE LOGIC

U29
74LS138

| Pin | Signal | Direction |
|-----|----------|-----------|
| 1 | bA9 | Input |
| 2 | bA10 | Input |
| 3 | bA11 | Input |
| 4 | /IO_CS | Output |
| 5 | /IO_CS | Output |
| 8 | GND | Power |
| 12 | UART_CS4 | Output |
| 13 | UART_CS3 | Output |
| 14 | UART_CS2 | Output |
| 15 | UART_CS1 | Output |
| 16 | VCC | Power |

