

POL ROSELLÓ

(585)-203-7957 • pr342@cornell.edu

CURRENT ADDRESS

308 Fairmount Ave Apt 4
Ithaca, NY 14850

PERMANENT ADDRESS

21 Conover Crossing
Fairport, NY 14450

PERSONAL PROFILE

Cornell University ECE and CS double major interested in exploring hard problems in computer science, especially in the fields of machine learning and artificial intelligence.
Born and raised in Barcelona, Spain (U.S. Permanent Resident); attended high school in the United States, living in California, Ohio, and upstate New York.
Native fluency in English, Spanish and Catalan; intermediate fluency in German.

EDUCATION

Cornell University, College of Engineering, Ithaca, NY
Bachelor of Science in Electrical and Computers Engineering (Expected May 2015)
Bachelor of Science in Computer Science (Expected May 2015)
3.9 GPA; 4.0 Major GPA

WORK EXPERIENCE

Undergraduate Researcher January 2014 – present
Batten Research Group, Cornell University

Writing and optimizing algorithms in C/C++ to benchmark a novel high-performance, energy-efficient parallel computing microarchitecture by mapping them to a research ISA.
Contributions acknowledged in two accepted 2014 IEEE MICRO papers authored by the group.

Software Engineering Intern June 2014 – August 2014
Cisco Systems, San Jose

Worked within the Internet of Things Group on the Connected Grid Network Management System, used to deploy and manage ~10 million endpoint IP-based wireless networks for smart grids.
Developed suites in C# to automate the zero-touch deployment, tunnel provisioning, and firmware upgrades of field routers; automated testing of the front-end functionality of the product.

Teaching Assistant (ECE 4750/CS 4420; ECE 3140/CS 3420; CS 2110) August 2013 – present
Cornell University

Currently a Teaching Assistant for the Computer Architecture course at Cornell University. Duties include holding office hours and grading student projects which include a quad-core processor with fully-bypassed cores and a two-way set-associative blocking cache.
Previously was a TA for Embedded Systems and a consultant for Object-Oriented Programming and Data Structures.

Research Assistant October 2012 – May 2013
Molnar Group, Cornell University

Programmed an FPGA and designed a printed circuit board as the testing platform for a very low power, very low noise multi-electrode array (MEA) for electrophysiological neural recordings.

RELEVANT SKILLS

University coursework: Object-Oriented Programming/Data Structures • Embedded Systems • Computer Architecture • Operating Systems • Machine Learning • Design with Microcontrollers • Digital Logic • Signal Processing • Microelectronics • Functional Programming • Evolutionary Algorithms • Discrete Mathematics • Artificial Intelligence • Neuroscience
Experience in: Java • C/C++ • Verilog • Python • C# • OCaml • PCB design • ASM • MATLAB

HONORS AND AWARDS

Cornell College of Engineering Jacobs Scholar
Dean's List every semester
Cornell Adelpic Writing Award Honorable Mention
International Baccalaureate Full Diploma Recipient and AP Scholar with Distinction

PROJECTS

Nanosatellite Student Project Team (Violet)
Ad-hoc wireless synchronization scheme for microcontrollers
Map-learning, line-following robot
Genetic algorithm to optimize defense strategy of a colony of organisms