Dr.gesslerstrasse 11a,
93051, Regensburg.
Prabhasajavvaji27@gmail.com
Linkedin: Prabhasa Javvaji

Technical Competencies :

- Semiconductor Processes
- Quality Management (ISO 17025, Customer audits)
- Failure analysis and reliability
- Crisis Handling
- Automotive Standards (For RF)
- Process automation using Python/VBA, Tableau, MATLAB
- Project management
- Electrical measurement devices
- Signal Integrity troubleshooting
- Technical documentation systems
- Statistical process control (SPC) and root cause analysis methodologies

Extra-Curricular Activities:

- **Bosch Global Software Technologies**

(September 2019 – February 2023)
Volunteered and Hosted several internal events of the organization

- **Amrita School of Engineering**

(February 2016 – February 2019)
Volunteered and served as Head for Mathematics Club.

Languages :

- Mother Tongue: Telugu
- Proficient: English (C2)
- Intermediate : German (A2)

Hobbies :

- Dancing
- Cooking
- Travelling
- Photography
- Space optimization & decluttering

PRABHASA JAVVAJI

Electrical and Microsystems Engineer with expertise in semiconductor packaging, quality management, failure analysis and EMI/EMC testing. Experienced in automating data workflows, driving process optimization, and leading cross-functional quality initiatives. Currently conducting R&D on advanced die attach interconnects for semiconductor reliability. Skilled in bridging development and production through analytical problem-solving and strong collaboration.

Employment History:

Infineon Technologies – Regensburg, Germany
Research Engineer (Master Thesis): **(March 2025 – Present)**

- Led research on Transient liquid phase sintering for stable die attach interconnection with bare Copper surfaces
- Focused on enhancing die attach performance by evaluating the formation of intermetallic phases in sintering pastes or conductive adhesives based on Silver and low-melting metals/alloys.
- Aimed to deliver insights that improve bond stability and reliability, advancing electronics packaging technologies.

Quality Engineer (Working Student): **(February 2024 – January 2025)**

- Developed dashboards using Tableau for Deviation Decision Help data for the Backend Semiconductor Production floor.
- Programmed solutions using Python and VBA to streamline and automate the processing of recurring Excel and PDF datasets.
- Built and managed Confluence knowledge repositories for Backend Preassembly production.
- Expertise in Backend Quality standards and analysis, including FMEA and MRB Process.

Bosch Global Software Technologies– Bengaluru, India
EMC Test Lead: **(September 2020 – February 2023)**

- Led a team in executing rigorous EMC testing (BCI), ensuring compliance with ISO 17025, CISPR 25, ISO 11452-1/2/4 while optimizing coverage, efficiency and resource allocation to meet KPIs and customer demands.
- Ensured accuracy in test documentation and reporting, enabling data-driven decisions and high client satisfaction.
- Provided technical expertise to validate measurements and results, supporting other test leads in cross-project verification, and ensured seamless collaboration to maintain high-quality deliverables.
- Gained expertise in RF root cause analysis by troubleshooting and resolving failures during development ECU testing, analyzing RF interference impacts.

EMC Test Engineer: **(September 2019 – September 2020)**

- Performed EMC/Electrical testing – Bulk Current Injection, Conducted/Radiated Emissions, Radiated Immunity (Antenna Method), and ESD for Automotive Chassis and Active Safety Systems.
- Ensured full compliance with ISO 17025 standards while testing for multiple OEMs.
- Gained expertise in Networking Protocols such as CAN, FlexRay, and Ethernet.

Education:

Master of Technology **(March 2023 – Present)**
Electrical and Microsystems Engineering, Ostbayerische Technische Hochschule, Germany.

Skills: KiCAD, Advanced Packaging of Semiconductors, Optoelectronics, handling of Electrical ins Handling of Electrical instruments.
Project Thesis: Viscosity and Electrical measurements of MR Fluids and MR Bos
(October 2023 – February 2024)

- Funded by DFG, the study addresses industry needs, where I have Investigated viscosity properties of newly developed Boron-based MR fluids using multiple viscometers under varying conditions.
- Measured DC resistance of MR-BOS (Magneto-rheopectic Boronorgano-silicon oxide) with a Picoammeter.

Bachelor of Technology **(July 2015 – May 2019)**
Electrical and Electronics Engineering, Amrita School of Engineering - India

Skills: C Language, AUTOCAD, HTML, LTSpice, LabVIEW, Raspberry Pi, Arduino, Power Electronics, Analog and Digital Systems, Control Systems.
Bachelor Thesis: On board Charger for Electric Vehicle **(September 2018 – February 2019)**

- Designed an on-board Level-1 EV charger prototype capable of charging from standard household AC (110-120V).
- Integrated power electronics (forward converter) for regulation and an Arduino-based display to monitor real-time battery charge status.