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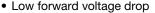
Vishay General Semiconductor

High Current Axial Plastic Rectifier



PRIMARY CHARACTERISTICS								
I _{F(AV)}	6.0 A							
V_{RRM}	50 V, 100 V, 200 V, 400 V, 600 V, 800 V							
I _{FSM}	400 A							
I _R	5.0 μA							
V_{F}	0.9 V, 0.95 V							
T_J max.	150 °C							
Package	P600							
Diode variations	Single die							

FEATURES





· High forward current capability

• High forward surge capability

• Solder dip 275 °C max. 10 s, per JESD 22-B106

 Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>





COMPLIANT

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters, and freewheeling diodes application.

Note

• These devices are not AEC-Q101 qualified.

MECHANICAL DATA

Case: P600, void-free molded epoxy body Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test **Polarity:** Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)										
PARAMETER		SYMBOL	GI750	GI751	GI752	GI754	GI756	GI758	UNIT	
Maximum repetitive	peak reverse voltage	V_{RRM}	50	100	200	400	600	800	V	
Maximum RMS volt	age	V_{RMS}	35	70	140	280	420	560	V	
Maximum DC blocking voltage		V_{DC}	50	100	200	400	600	800	V	
Maximum non-repe	titive peak reverse voltage	V_{RSM}	60	120	240	480	720	1200	V	
Maximum average	T _A =60 °C, PCB mounting (fig. 1)				6	.0				
forward rectified current at	T _L = 60 °C,0.125" (3.18 mm) lead length (fig. 2)	I _{F(AV)}			2	2			Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	400						Α	
Operating junction a	and storage temperature range	T _J , T _{STG}			- 50 tc	+ 150			°C	

ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)														
PARAMETER	TEST (CONDITIONS	SYMBOL	GI750	GI751	GI752	GI754	GI756	GI758	UNIT				
Maximum instantaneous	6.0 A	6.0 A		6.0 A		6.0 A				0.90			0.95	V
forward voltage at	100 A		V _F		1.25					V				
Maximum DC reverse current		T _A = 25 °C		5.0						μΑ				
at rated DC blocking voltage		T _A = 100 °C	IR	1.0					mA					
Typical reverse recovery time	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		t _{rr}	2.5				μs						
Typical junction capacitance	4.0 V, 1	MHz	CJ			15	50			pF				



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THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL GI750 GI751 GI752 GI754 GI756 GI758 UNIT								
Typical thermal resistance	R _{0JA} (1)	20						°C/W	
Typical thermal resistance	R _{0JL} (1)			4	.0			C/VV	

Note

⁽¹⁾ Thermal resistance from junction to ambient and from junction to lead at 0.375" (9.5 mm) lead length, PCB mounted with 1.1" x 1.1" (30 mm x 30 mm) copper pads

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
GI756-E3/54	2.1	54	800	13" diameter paper tape and reel					
GI756-E3/73	2.1	73	300	Ammo pack packaging					

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

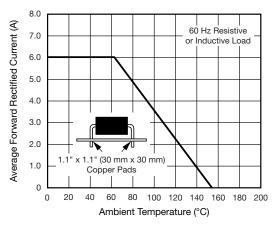


Fig. 1 - Maximum Forward Current Derating Curve

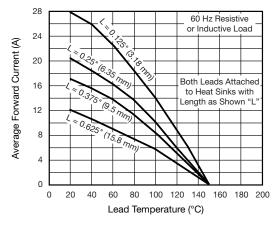


Fig. 2 - Maximum Forward Current Derating Curve

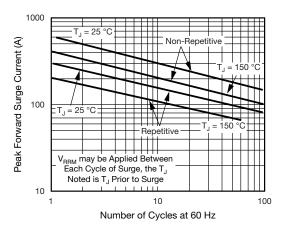


Fig. 3 - Maximum Peak Forward Surge Current

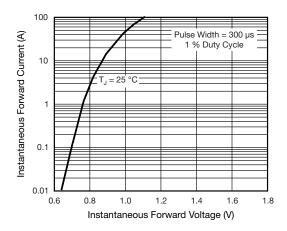


Fig. 4 - Typical Instantaneous Forward Characteristics



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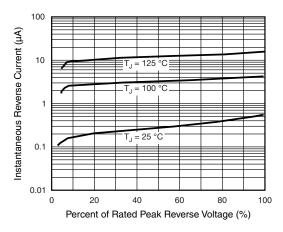


Fig. 5 - Typical Reverse Characteristics

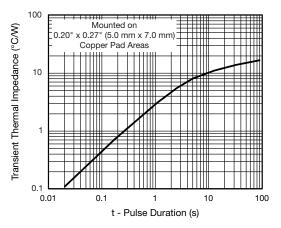
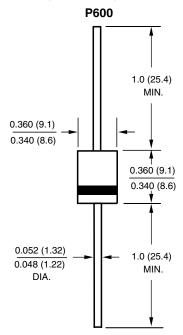


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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Standard Recovery Diodes (Hockey PUK Version), 800 A



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PRODUCT SUMMARY						
I _{F(AV)}	800 A					
Package	DO-200AA					
Circuit configuration	Single Diode					

FEATURES

- Wide current range
- High voltage ratings up to 2400 V
- · High surge current capabilities
- · Diffused junction
- · Hockey PUK version
- · Case style DO-200AA
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- Converters
- · Power supplies
- · Machine tool controls
- · High power drives
- · Medium traction applications

MAJOR RATINGS AND CHARACTERISTICS									
PARAMETER	TEST CONDITIONS	VALUES	UNITS						
		800	A						
I _{F(AV)}	T _{hs}	55	°C						
1		1435	A						
I _F (RMS)	T _{hs}	25	°C						
1	50 Hz	8250	Δ.						
IFSM	60 Hz	8640	A						
l ² t	50 Hz	340	kA ² s						
1-1	60 Hz	311	KA-S						
V _{RRM}	Range	400 to 2400	V						
T _J		- 40 to 190	°C						

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS										
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA						
	04	400	500							
	08	800	900							
VS-SD400CC	12	1200	1300	15						
V3-3D400CC	16	1600	1700	15						
	20	2000	2100							
	24	2400	2500							



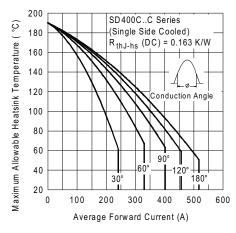
FORWARD CONDUCTION							
PARAMETER	SYMBOL		TEST COND	VALUES	UNITS		
Maximum average forward current	I=	180° conduct	ion, half sine wav	е	800 (425)	Α	
at heatsink temperature	I _{F(AV)}	Double side (single side) coole	ed	55 (85)	°C	
Maximum RMS forward current	I _{F(RMS)}	25 °C heatsin	nk temperature do	uble side cooled	1435		
		t = 10 ms	No voltage		8250		
Maximum peak, one-cycle forward,	1	t = 8.3 ms	reapplied		8640	Α	
non-repetitive surge current	I _{FSM}	t = 10 ms	50 % V _{RRM}		6940		
		t = 8.3 ms	reapplied	Sinusoidal half wave,	7265		
Market 121 for 6 at 1		t = 10 ms No voltage	initial $T_J = T_J$ maximum	340	1		
	l ² t	t = 8.3 ms	reapplied		311	kA ² s	
Maximum I ² t for fusing		t = 10 ms	50 % V _{RRM}		241		
		t = 8.3 ms	reapplied		220		
Maximum I ² √t for fusing	I²√t	t = 0.1 to 10 r	ms, no voltage rea	applied	3400	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x	$I_{F(AV)} < I < \pi \times I_{F(AV)}$	(AV) , $T_J = T_J$ maximum	0.80	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.83	V	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum			0.55	mΩ	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.53	11152	
Maximum forward voltage drop	V _{FM}	I _{pk} = 1930 A, wave	$T_J = T_J$ maximum	n, t _p = 10 ms sinusoidal	1.86	V	

THERMAL AND MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction operating temperature range	TJ		- 40 to 190	°C					
Maximum storage temperature range	T _{Stg}		- 55 to 200						
Maximum thermal resistance,	В	DC operation single side cooled	0.163	K/W					
junction to heatsink	R _{thJ-hs}	DC operation double side cooled	0.073	r/vv					
Mounting force, ± 10 %			4900 (500)	N (kg)					
Approximate weight			70	g					
Case style		See dimensions - link on page 5	DO-2	00AA					

△R _{thJ-hs} CONDUCTION										
CONDUCTION ANGLE	SINUSOIDAL C	ONDUCTION	RECTANGULA	R CONDUCTION	TEST CONDITIONS	UNITS				
CONDUCTION ANGLE	SINGLE SIDE	DOUBLE SIDE	SINGLE SIDE	DOUBLE SIDE	TEST CONDITIONS	UNITS				
180°	0.017	0.018	0.011	0.012						
120°	0.020	0.020	0.020	0.020						
90°	0.025	0.025	0.027	0.027	$T_J = T_J$ maximum	K/W				
60°	0.037	0.036	0.038	0.038						
30°	0.064	0.062	0.065	0.062						

Note

• The table above shows the increment of thermal resistance RthJ-hs when devices operate at different conduction angles than DC



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Fig. 1 - Current Ratings Characteristics

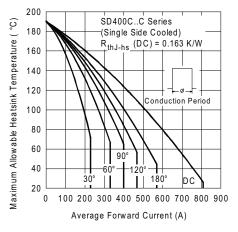


Fig. 2 - Current Ratings Characteristics

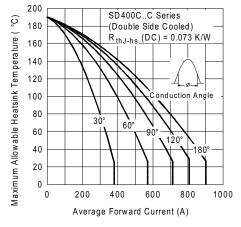


Fig. 3 - Current Ratings Characteristics

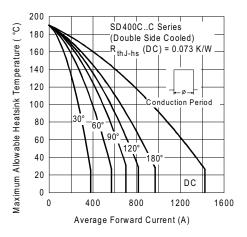


Fig. 4 - Current Ratings Characteristics

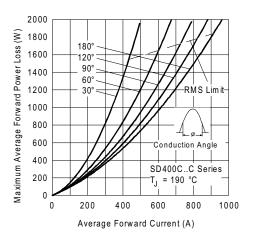


Fig. 5 - Forward Power Loss Characteristics

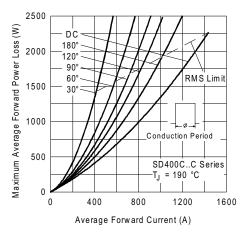


Fig. 6 - Forward Power Loss Characteristics

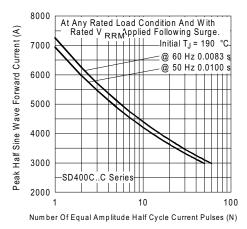


Fig. 7 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

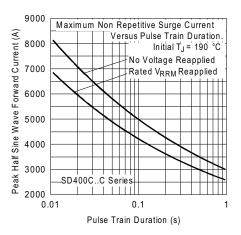


Fig. 8 - Maximum Non-Repetitive Surge Current Single and Double Side Cooled

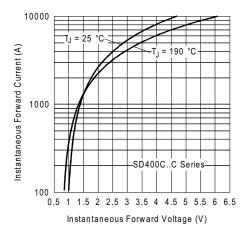


Fig. 9 - Forward Voltage Drop Characteristics

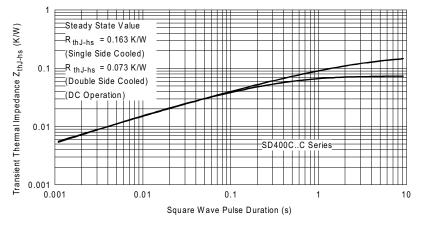
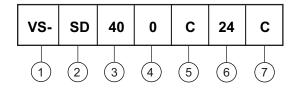


Fig. 10 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Diode

Essential part number

- 0 = Standard recovery

5 - C = Ceramic PUK

- Voltage code x 100 = V_{RRM} (see Voltage Ratings table)

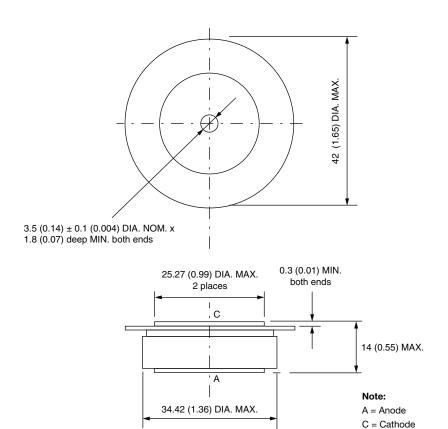
- C = PUK case DO-200AA

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95248	



DO-200AA

DIMENSIONS in millimeters (inches)



Quote between upper and lower pole pieces has to be considered after application of mounting force (see Thermal and Mechanical Specifications)



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VISHAY GENERAL SEMICONDUCTOR

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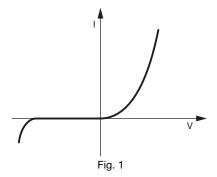
Rectifiers Application Note

Physical Explanation

GENERAL TERMINOLOGY

Semiconductor diodes are used as rectifiers, switches, varactors and voltage stabilizers (see Zener data book).

Semiconductor diodes are two-terminal solid-state devices having asymmetrical voltage-current characteristics. Unless otherwise stated, this means a device has single pn-junction corresponding to the characteristics shown in figure 1.



An application of the voltage current curve is given by

$$I = I_S \left(exp \frac{V}{V_T} - 1 \right)$$

where

I_S = saturation current

$$V_T = \frac{k \times T}{q} = \text{temperature potential}$$

If the diode is forward-biased (anode positive with respect to cathode), its forward current ($I = I_F$) increases rapidly with increasing voltage. That is, its resistance becomes very low.

If the diode is reverse-biased (anode negative with respect to cathode), its reverse current $(-I = I_R)$ is extremely low. This is only valid until the breakdown voltage V_{BR} has been reached. When the reverse voltage is slightly higher than the breakdown voltage, a sharp rise in reverse current results.

Bulk resistance

Resistance of the bulk material between junction and the diode terminals.

Parallel resistance, rp

Diode resistance resulting from HF rectification which acts as a damping resistance to the pre-tuned demodulation circuit.

Differential resistance

See forward resistance, differential

Diode capacitance, C_D

Total capacitance between the diode terminals due to case, junction and parasitic capacitances.

Breakdown voltage, V_{BR}

Reverse voltage at which a small increase in voltage results in a sharp rise of reverse current. It is given in the technical data sheet for a specified current.

Forward voltage, V_F

The voltage across the diode terminals which results from the flow of current in the forward direction.

Forward current, IF

The current flowing through the diode in the direction of lower resistance.

Forward resistance, r_F

The quotient of DC forward voltage across the diode and the corresponding DC forward current.

Forward resistance, differential rf

The differential resistance measured between the terminals of a diode under specified conditions of measurement, i.e., for small-signal AC voltages or currents at a point of forward direction V-I characteristic.

Case capacitance, Ccase

Capacitance of a case without a semiconductor crystal.

Integration time, tav

With certain limitations, absolute maximum ratings given in technical data sheets may be exceeded for a short time. The mean value of current or voltage is decisive over a specified time interval termed integration time. These mean values over time interval, tav, should not exceed the absolute maximum ratings.

Average rectified output current, IFAV

The average value of the forward current when using the diode as a rectifier. The maximum allowable average rectified output current depends on the peak value of the applied reverse voltage during the time interval at which no current is flowing. In the absolute maximum ratings, one or both of the following are given:

- The maximum permissible average rectified output current for zero diode voltage (reverse).
- The maximum permissible average rectified output current for the maximum value of V_{RRM} during the time interval at which no current is flowing.

 $I_{\mbox{\scriptsize FAV}}$ decreases with an increasing value of the reverse voltage during the interval of no current flow.

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Vishay General Semiconductor

Physical Explanation

Rectification efficiency, η_r

The ratio of the DC load voltage to the peak input voltage of an RF rectifier.

Series resistance, r_s

The total value of resistance representing the bulk, contact and lead resistance of a diode given in the equivalent circuit diagram of variable capacitance diodes.

Junction capacitance, CJ

Capacitance due to a pn junction of a diode which decreases with increasing reverse voltage.

Reverse voltage, V_R

The voltage drop which results from the flow of reverse current (through the semiconductor diode).

Reverse current, I_R (leakage current)

The current which flows when reverse bias is applied to a semiconductor junction.

Reverse resistance, R_R

The quotient of the DC reverse voltage across a diode and the corresponding DC reverse current.

Reverse resistance, differential, rr

The differential resistance measured between the terminals of a diode under specified condition of measurement i.e., for small-signal (AC) voltage or currents at a point of reverse-voltage direction V-I characteristic.

Peak forward current, IFRM

The maximum forward current with sine-wave operation, $f \ge 25$ Hz, or pulse operation, $f \ge 25$ Hz, having a duty cycle $t_{\rm D}/T \le 0.5$.

Peak reverse voltage, V_{RRM}

The maximum reverse voltage having an operating frequency $f \ge 25$ Hz for sine-wave as well as pulse operation.

Peak surge forward current, I_{FSM}

The maximum permissible surge current in a forward direction having a specified waveform with a short specified time interval (i.e., 10 ms) unless otherwise specified. It is not an operating value. During frequent repetitions, there is a possibility of change in the device's characteristic.

Peak surge reverse voltage, V_{RSM}

The maximum permissible surge voltage applied in a reverse direction. It is not an operating value. During frequent z repetitions, there is a possibility of change in the device's characteristic.

Power dissipation, Pv

0

Δ. \cap

⋖

◄ An electrical power converted into heat. Unless otherwise specified, this value is given in the data sheets under absolute maximum ratings, with TA = 25 °C at a specified distance from the case (both ends).

Switching on Characteristic

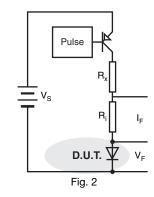
Forward recovery time, tfr

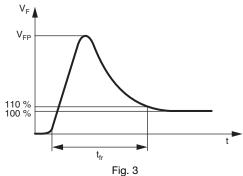
The time required for the voltage to reach a specified value (normally 110 % of the steady state forward voltage drop), after instantaneous switching from zero or a specified reverse voltage to a specified forward biased condition (forward current).

This recovery time is especially noticeable when higher currents are to be switched within a short time. The reason is that the forward resistance during the turn-on time could be higher than the DC current (inductive behavior). This can result in the destruction of a diode because of high instantaneous power loss if constant current control is used.

Turn on transient peak voltage, V_{fp}

The voltage peak (overshoot) after instantaneous switching from zero or a specified reverse voltage to a specified forward biased condition (forward current). The forward recovery is very important especially when higher forward currents must be switched on within a very short time (switching on losses).





Switching off Characteristic, Inductive Load

Reverse recovery time, t_{rr}

The time required for the current to reach a specified reverse current, i_R (normally 0.25 % of I_{RM}), after switching from a specified forward current IF to a specified reverse biased condition (reverse voltage V_{Batt}) with a specified slope

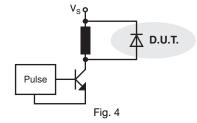
Revision: 16-Aug-11 Document Number: 84064 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

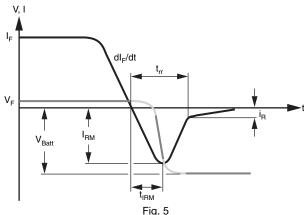
Physical Explanation

Peak reverse recovery current, IRM

The peak reverse current after switching from a specified forward current I_F to a specified reverse biased condition (reverse voltage V_B) with a specified switching slope dI_F/dt.

The reverse recovery is very important especially when switching from higher currents to high reverse voltage within a very short time (switching off losses).



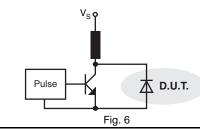


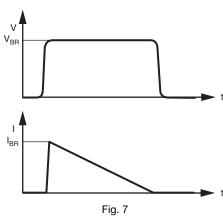
Reverse avalanche energy, E_R

 $\mathsf{E}_\mathsf{R} = \frac{1}{2} \times \mathsf{L} \times \mathsf{I}^2$

is dissipated within the rectifier. Under this condition the diode is in a reverse avalanche mode with a reverse current at the beginning which is equal to the current that was flowing through the inductance just before it was switched U off.

☐ The reverse energy capability depends on the reverse O current and the junction temperature prior to the avalanche mode.





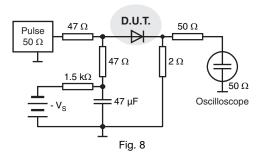
Switching off Characteristic, Instantaneous Switching

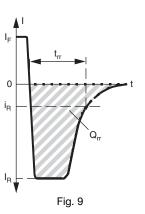
Reverse recovery time, t_{rr}

The time required for the current to reach a specified reverse current, i_R (normally 0.25 A), after instantaneous switching from a specified forward current I_F (normally 0.5 A) to a specified reverse current I_R (normally 1.0 A).

Reverse recovery charge, Q_{rr}

The charged stored within the diode when instantaneous switched from a specified forward current I_F (normally 0.5 A) to a specified reverse current I_R (normally 1.0 A).





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PLICATION

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C3D08065I Silicon Carbide Schottky Diode Z-REC® RECTIFIER

V_{RRM} = 650 V $I_{F}(T_{c}=130^{\circ}C)$ = 8 A Q_{c} = 21 nC

Features

- 650-Volt Schottky Rectifier
- Ceramic Package Provides 2.5kV Isolation
- Zero Reverse Recovery Current
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Positive Temperature Coefficient on V_F

Benefits

- Electrically Isolated Package
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

Package







TO-220 Isolated



Applications

- HVAC
- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter Stages
- AC/DC converters

Part Number	Package	Marking
C3D08065I	Isolated TO-220-2	C3D08065I

Maximum Ratings ($T_c = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	650	V		
V _{RSM}	Surge Peak Reverse Voltage	650	V		
V _{DC}	DC Blocking Voltage	650	V		
I _F	Continuous Forward Current	16.5 8 7.5	А	T _c =25°C T _c =130°C T _c =135°C	Fig. 3
I _{FRM}	Repetitive Peak Forward Surge Current	29 19	А	T_c =25°C, t_p =10 ms, Half Sine Wave T_c =110°C, t_p =10 ms, Half Sine Wave	
I _{FSM}	Non-Repetitive Peak Forward Surge Current	69 55	А	T_c =25°C, t_p =10 ms, Half Sine Wave T_c =110°C, t_p =10 ms, Half Sine Wave	
I _{F,Max}	Non-Repetitive Peak Forward Surge Current	650 530	А	T_c =25°C, t_p = 10 μ s, Pulse T_c =110°C, t_p = 10 μ s, Pulse	
P _{tot}	Power Dissipation	53.6 23.2	W	T _c =25°C T _c =110°C	Fig. 4
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C		
	TO-220 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 2.1	1.8 2.4	V	$I_F = 8 \text{ A } T_J = 25^{\circ}\text{C}$ $I_F = 8 \text{ A } T_J = 175^{\circ}\text{C}$	Fig. 1
I_R	Reverse Current	10 12	51 204	μΑ	$V_R = 650 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 650 \text{ V } T_J = 175^{\circ}\text{C}$	Fig. 2
Q _c	Total Capacitive Charge	20		nC	$V_R = 400 \text{ V, } I_F = 8A$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	Fig. 5
С	Total Capacitance	395 37 32		pF	$V_R = 0 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 200 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$ $V_R = 400 \text{ V, } T_J = 25^{\circ}\text{C, } f = 1 \text{ MHz}$	Fig. 6
E _c	Capacitance Stored Energy	3.0		μJ	V _R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	Parameter	Тур.	Unit	Note
$R_{_{ heta JC}}$	Package Thermal Resistance from Junction to Case	2.8	°C/W	Fig. 8

Typical Performance

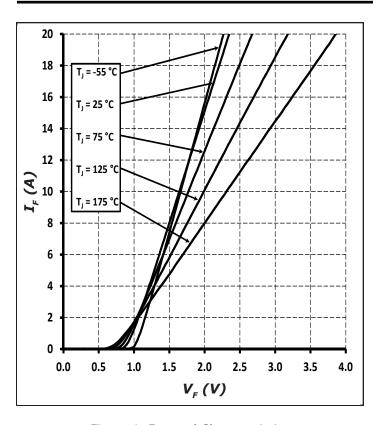


Figure 1. Forward Characteristics

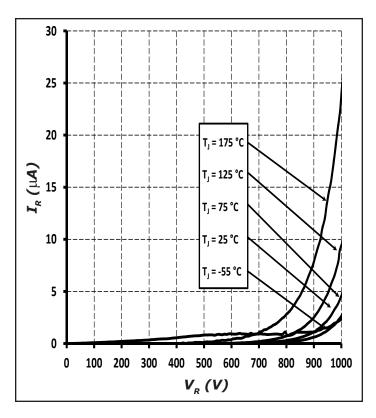
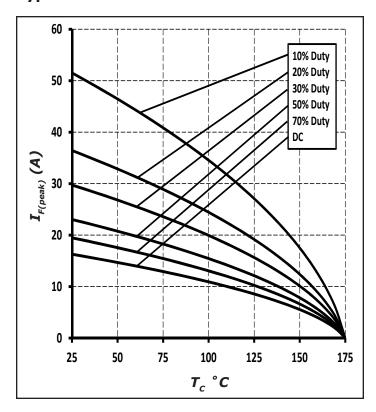


Figure 2. Reverse Characteristics



Typical Performance



 T_c ° C

Figure 3. Current Derating

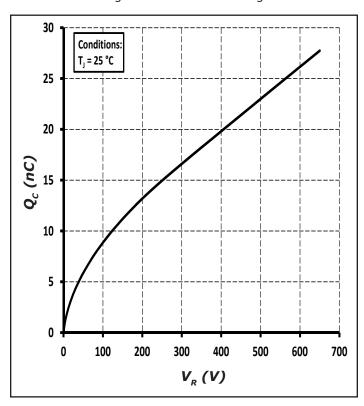


Figure 5. Total Capacitance Charge vs. Reverse Voltage $\,$

Figure 4. Power Derating

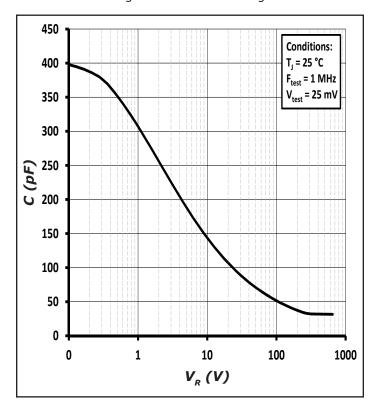


Figure 6. Capacitance vs. Reverse Voltage



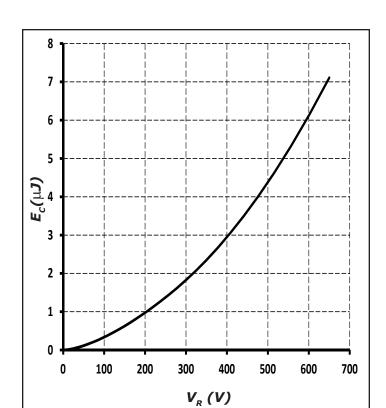


Figure 7. Capacitance Stored Energy

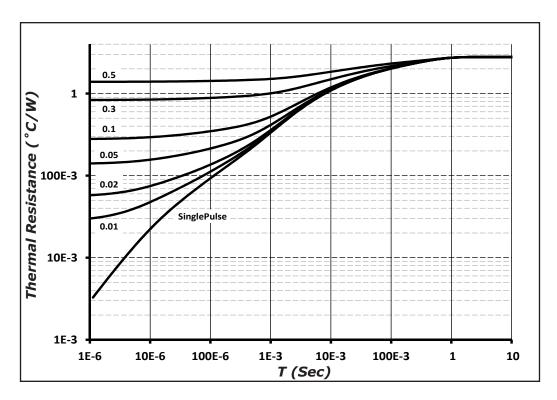
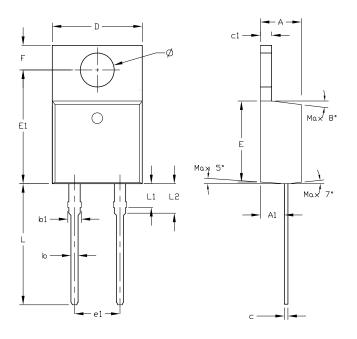


Figure 9. Transient Thermal Impedance



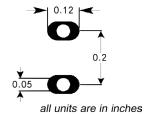
Package Dimensions

Package TO-220-2



Symbol	Dimension i	n Millimeters	Dimension in Inches	
Syllibol	Min		Min	Max
Α	4.420	4.720	0.174	0.186
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.360	0.460	0.014	0.018
c1	1.170	1.370	0.046	0.054
D	9.960	10.250	0.392	0.404
Ш	8.990	9.290	0.354	0.366
E1	12.550	12.850	0.494	0.506
e1	4.980	5.180	0.196	0.204
F	2.590	2.890	0.102	0.114
L	13.080	13.480	0.515	0.531
L1	2.470	2.870	0.097	0.113
L2	3.200	3.600	0.126	0.142
Ø	3.790	3.890	0.149	0.153
θ1	Max 8°			
θ2	Max 7°			
θ3	Max 5°			
Τ	Max 0.0205 Max 0.52			0.52

Recommended Solder Pad Layout



TO-220-2

Part Number	Package	Marking
C3D08065I	Isolated TO-220-2	C3D08065I

Note: Recommended soldering profiles can be found in the applications note here: http://www.wolfspeed.com/power_app_notes/soldering





Diode Model

$$\begin{array}{c|c} - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & & \\ - & & \\ \hline - & &$$

$$Vf_T = V_T + If * R_T$$

$$V_T = 0.95 + (T_J * -1.2*10^{-3})$$

$$R_T = 0.054 + (T_J * 5.5*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Ecology section of our website at http://www.wolfspeed.com/power/tools-and-support/product-ecology.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into
the human body nor in applications in which failure of the product could lead to death, personal injury or property
damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines,
cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control
systems, or air traffic control systems.

Related Links

- Cree SiC Schottky diode portfolio: http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes
- Schottky diode Spice models: http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2
- SiC MOSFET and diode reference designs: http://go.pardot.com/l/101562/2015-07-31/349i