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Sri Jayachamarajendra College of Engineering (SJCE), Mysore – 570 006
An Autonomous Institute Affiliated to
Visvesvaraya Technological University, Belgaum



“An Embedded Cheque Processing System”

Seminar report submitted in partial fulfillment of curriculum prescribed for
the award of the degree of

BACHELOR OF ENGINEERING IN COMPUTER SCIENCE AND ENGINEERING

by

Ganesh S.
(4JC09CS044)

Under the Guidance of

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DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
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CERTIFICATE

This is to certify that the seminar titled “**An Embedded Cheque Processing System**” is presented by **Ganesh S.** in partial fulfillment of the award of the degree of **Bachelor of Engineering in Computer Science and Engineering of Visvesvaraya Technological University, Belgaum during the year 2012-13.** It is certified that all corrections / suggestions indicated during seminar presentation have been incorporated in the report. The seminar report has been approved as it satisfies the academic requirements in respect of seminar presentation prescribed for the Bachelor of Engineering degree.

Guide

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