

Lecture 1: Introduction - W2: 8 19 May

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1.1 General Review

A **function problem** is a map $f : \Sigma^* \rightarrow \Sigma^*$ where Σ^* is a string. These could be rather complicated, such as taking as input a start and end vertex and outputting a shortest path (these are strings under the appropriate encoding).

A **decision problem** is a function problem where the range is the boolean values $\{yes, no\}$. And **language** associated with a decision problem is the set of all strings that map to *yes* under f i.e. $\{x \in \Sigma^* : f(x) = yes\}$.

1.2 Turing Machine

You know what these are. They are Turing's attempt to simulate human computers. Formally a Turing Machine (TM) M is a tuple

$$M = (Q, \Sigma, \Gamma, \delta, q_0, q_{acc}, q_{reg})$$

where Q is the set of states, Σ are the alphabet, Γ is the tape symbols (these are a superset of Σ), q_0 is the set of start states, q_{acc} is the set of accepting states, and q_{reg} is the set of rejecting states.

$t_M(x)$ is the **running time** of a TM M on input $x \in \Sigma^*$. It represents the number of steps M takes on x before halting $t_M(x) = \infty$ if M does not halt on x . If there exists a function $t : \mathbb{N} \rightarrow \mathbb{N}$ such that $t_M(x) \leq t(|x|)$ then M is t -bounded. *This TM only needs to be "good" asymptotically, finitely many issues can be put in a look up table and fixed in constant time.*

The **space** of a TM M is the total number of work tape cells touched by M during the computation of x . Again, if there exists a function $s : \mathbb{N} \rightarrow \mathbb{N}$ with the necessary properties, then M is s -bounded.

k -tape TM consists of k infinite tapes. Of these, one is the input and one is the output leaving $k - 2$ work tapes.

Theorem 1.1 *A k -tape TM can be efficiently simulated by a one-tape TM with only a quadratic time slowdown.*

Proof: Allocate k parallel track on the one-tape TM. Delineate each track with the special tape symbol $*$. For each step of the k -tape TM first sweep the tape on the one-tape TM to find the track to read. ■

Remark: this quadratic time slowdown for a one-tape TM simulation is unavoidable! Surprisingly there is only a log linear slow if we were to use a two-tape TM simulation i.e. if the k -tape TM M is t -bounded then simulating M using a 2-tape TM takes $O(t \log t)$ time.

Theorem 1.2 *A k -tape TM M over tape alphabet $\Gamma = \{0, 1\}$ can be simulated by a k -tape TM M' over tape alphabet $\Gamma' = \{0, 1, \#, -\}$ with at most $O(\log |\Gamma|)$ factor slowdown (both use input alphabet $\Sigma = \{0, 1\}$) i.e. if M is t -bounded then M' is $t \log |\Gamma|$ bounded.*

The key to proving this theorem is to note that it is possible to encode each symbol in Γ by a number. There are $\lceil \log |\Gamma| \rceil$ numbers needed which results in the $\log |\Gamma|$ factor slowdown.

1.2.1 Universal Turing Machines

A **Universal TM** (UTM) is a TM U which takes as input a description of a TM M as a string and another string $w \in \Sigma^*$ and simulates M on w . The existence of UTM foretold the creation of the general purpose computer and hints at their limitations (e.g. cannot detect self replicating programs). We try to simulate a t -bounded TM M using U in $O(t \log t)$ time as follows:

Theorem 1.3 *There is a 2-tape UTM U that given $\langle M, w \rangle$, where M is a k -tape TM and w is an input to M , will simulate M on w with at most a log linear slowdown.*

Proof: We will mainly focus on the difficulty of simulating a k -tape TM using a 2-tape TM (other issues involving the number of tape symbols and states of the k -tape TM can be solved by adding a constant factor).

The gist of the proof is to use one tape to hold the k tapes as k tracks. Then at some special position 0, we will keep all k symbols currently being scanned for M . For each transition step we will move each track so that the new tape cell to be scanned is placed at position 0. We can't move the whole track since that would be too expensive so we will only move a portion of the tape using the second tape to help.

Partitioning the first tape proceeds as follows: divide the chunks to the right of the 0 position into zones R_0, R_1, \dots (read left to right) where zone R_i has 2^{i+1} cells. Similarly, divide the space to the left of 0 into zones L_0, L_1, \dots (read right to left) where zone L_i has size 2^{i+1} . As follows, where P_0 is position 0, $L_{i,j}$ is the j -th cell of L_i , similarly for $R_{i,j}$:

$$\dots, L_{1,3}, L_{1,2}, L_{1,1}, L_{1,0}, L_{0,1}, L_{0,0}, P_0, R_{0,0}, R_{0,1}, R_{1,0}, R_{1,1}, R_{1,2}, R_{1,3}, \dots$$

Next, introduce a new *special blank* symbol different from the tape symbols of M . For zone Z_i , if Z_i has all special blank cells then it is empty, if Z_i has 2^i special blank symbols then it is half full, and if Z_i has no special blank symbols then it is full.

In a preprocessing step for U , initialize each zone i , for $i = 0, \dots, \log t$ to be half full. Throughout the simulation we maintain the following invariant:

1. Position 0 of U 's tape contains non-"special blank" in each track (this is the k -tuple of symbols currently scanned by M).
2. For each i , $0 \leq i \leq \log t$, either both L_i and R_i are half-full, or exactly one of them is full while the other one is empty.

Next we demonstrate the procedure for moving a track of the tape to the left (this is equivalent to moving one of the heads of M to the right)

1. Scan the zones R_0, R_1, \dots until you find one which is non-empty. Suppose this zone is R_{i_0} .
2. Shift the contents of position 0 to L_0 , L_0 to L_1 , and so on until L_{i_0-1} to L_{i_0} . Since R_0, \dots, R_{i_0-1} are all empty, L_0, \dots, L_{i_0-1} are all full so will now half fill L_1, \dots, L_{i_0} .
3. Take the first (left-most) non-special-blank cell in R_{i_0} and put it in position 0. Move the remaining non-special-blank cell of R_{i_0} down to half fill the zones R_0, R_1, \dots .

4. Observe that R_0, \dots, R_{i_0-1} and L_0, \dots, L_{i_0-1} are all half-full and L_{i_0} is full or half full depending on if R_{i_0} is empty or half full.

Finally we perform amortized analysis of the running time. The key observation from the above example is that after finding and shifting R_{i_0} , the next at least $2^{i_0} - 1$ will have to look no farther than R_{i_0-1} (all R_0, \dots, R_{i_0-1} are half full). Thus the total amount of shifting we need to do is:

$$\sum_{i=0}^{\log t} \frac{t}{2^i} \cdot O(2^i) \leq O(t \log t)$$

This is only the time needed for one track, but we said that all the extra time for the tracks and the symbols can be hidden behind a constant. ■

1.3 Complexity Classes

If n is the input size with function $f : \mathbb{N} \rightarrow \mathbb{N}$

- $\text{Time}(f(n)) = \{L : L \text{ is decided by a TM in at most } f(n) \text{ steps}\}$
- $\text{Space}(f(n)) = \{L : L \text{ is decided by a TM by touching at most } f(n) \text{ worktape cells}\}$

Complexity classes are typically defined as collections of languages over the binary alphabet with the tape symbols made arbitrary. Also any arbitrary (fixed) number of tapes are allowed. Examples include:

- $\text{EXP} = \bigcup_{k \geq 1} \text{Time}(2^{n^k})$
- $\text{L} = \text{Space}(\log n)$

In general, a complexity class is a collection of languages decidable within a given amount of computational resources.

1.4 Linear Speedup Theorem

Theorem 1.4 (*Linear Time Speedup*) *If a Language L is decided by some TM M in time $t(n)$, then for any $\epsilon > 0$, there is a TM M' that decides L in time at most $n + \epsilon n + \epsilon t(n) + 2$.*

Proof: The idea is to encode k -tuples of symbols of the tape alphabet of M as one tape symbol of M' . The original input of size n compresses to an input to M' of size n/k . When we simulate M on M' , every k steps of M will translate to a six steps in M' . The gist of the move procedure is to move the head in M' : L, R, R, L to read $3k$ symbols altogether (start at the initial cell; L to move to the cell on the left; R to get back to the initial cell; R again to move to the cell on the right; L to get back to the initial cell). Then we figure out what these $3k$ symbols would look like after k time steps on M . Using two more moves we can update the initial cells with the new content. This procedure saves us a factor of $1/k$. The total time used is:

- n - compress input to M of size n to input to M' of size n/k .

- $n/k + 2$ - moved back to the left-most symbol of the input to M' .
- $6t(n)/k$ - simulate M on M' .

Thus we choose $k = 6/\epsilon$ to get the desired bound. ■

Theorem 1.5 (*Linear Space Speedup*) If a language L is decided by some TM M in space $s(n)$, then for any $\epsilon > 0$ there is an TM M' that decides L in space at most $\epsilon n + \epsilon s(n) + 2$.

As a result all constant factors can be ignored in the discussion of space and time complexity when the bound is greater than n .

1.5 Reduction

A language L_1 is reducible to L_2 , denoted $L_1 \leq L_2$ if there is an *efficiently* computable function $f : \Sigma^* \rightarrow \Sigma^*$ such that

$$\begin{aligned} x \in L_1 &\implies f(x) \in L_2 \\ x \notin L_1 &\implies f(x) \notin L_2 \end{aligned}$$

You should think of L_2 has the harder problem. Pay close attention to the word *efficiently*. For not it means polynomial time.

1.6 Completeness

In complexity class C , language L is *C-complete* if

1. $L \in C$
2. Every language in L is reducible to L . Note that reducibility is with respect to the complexity class. There is no sense in doing poly-time reductions if we are in the class \mathbb{L} for example.

A language L is *C-hard* if every language in C is reducible to L (though L might not be in C).

Extended Church-Turing Thesis: Everything *efficiently* computable on a physical computer is *efficiently* computable on a Turing Machine.

1.7 Time Hierarchy Theorem

The following shows that giving a TM more time strictly increases the class of languages that it can decide. First a **time-constructible** function $f : \mathbb{N} \rightarrow \mathbb{N}$ is one where there exists a TM such that given the input 1^n , writes down $1^{f(n)}$ in $O(f(n))$ time. Can you think of functions which satisfies this property? Actually most functions that you think of satisfies this property. In the following only consider time-constructible running times. **Proper** complexity functions is a similar notion but is a bit nicer since on input of size n we are allowed time $O(n + f(n))$ and $O(f(n))$ space. Thus $\log n$ is proper but not time-constructible.

Theorem 1.6 If f, g are time-constructible functions satisfying $f(n) \log f(n) = o(g(n))$, then

$$\mathbf{DTIME}(f(n)) \subsetneq \mathbf{DTIME}(g(n)) \quad (1.1)$$

a.k.a for any proper $t(n), T(n) \geq n$ s.t.

$$T(n) \geq \omega(t(n) \log t(n))$$

we have

$$\mathbf{Time}(t(n)) \subsetneq \mathbf{Time}(T(n))$$

Proof: The gist is to find a language D which is in $\mathbf{Time}(T(n))$ but not in $\mathbf{Time}(t(n))$ use diagonalization to show this second part. We consider the encoding $\langle M \rangle$ of TM M . We also allow "padded encodings $\langle M \rangle 01^m$ for all $m \geq 0$ so that M has encodings of arbitrarily large size. Put all TM, $M_1, M_2, \dots, M_i, \dots$, that run in time at most $t(n)$ down rows of the table (consider clocked TM here only since we don't know how to tell when a TM stops on a certain input) and encodings $\langle M_1 \rangle, \dots, \langle M_j \rangle, \dots$ across the columns. The contents of cell i, j is the result of running M_i on input $\langle M_j \rangle$. Add necessary timeout $t(n)$ so that M_i stops on all inputs. Define the diagonal language D as follows:

$$D = \{ \langle M_i \rangle : M_i \text{ does not accept } \langle M_i \rangle \text{ in } \leq t(n) \text{ steps} \}$$

Observe that $D \notin \mathbf{Time}(t(n))$ by contradiction. Suppose that $D \in \mathbf{Time}(t)$ then there exists a TM M which decides D . Since we have enumerated all TM in time $t(n)$, $M = M_j$ for some $j \geq 1$. By running M_j on $\langle M_j \rangle$ we reach a contradiction (either M_j accepts $\langle M_j \rangle$ so it doesn't ($\langle M_j \rangle \notin D$) or M_j rejects $\langle M_j \rangle$ so it doesn't ($\langle M_j \rangle \in D$). Next note that $D \in \mathbf{Time}(T(n))$ by the UTM theorem since we can simulate $D \in \mathbf{Time}(O(t(n) \log t(n)))$ (including some constant factor lost due to the structure of the input machine). Since $T \gg \omega(t \log t)$, $D \in \mathbf{Time}(T(n))$. ■

1.8 Space Hierarchy Theorem

Similar idea as the above. First $f : \mathbb{N} \rightarrow \mathbb{N}$ is a **space-constructible** function if there is a TM M that, given any n -bit input, constructs $f(n)$ in space $O(f(n))$. The proof is essentially the same as the above except that we do not have a factor of $\log f(n)$ since the UTM for space-bounded computations has only a constant time overhead.

Theorem 1.7 If f, g are space-constructible functions satisfying $f(n) = o(g(n))$, then

$$\mathbf{SPACE}(f(n)) \subsetneq \mathbf{SPACE}(g(n)) \quad (1.2)$$

For any proper $s(n), S(n) \geq \log n$ s.t. $S(n) \geq \omega(s(n))$ then

$$\mathbf{Space}(s) \subsetneq \mathbf{Space}(S)$$

Theorem 1.8 (Gap Theorem) There exist non-proper $t(n)$ such that

$$\mathbf{Time}(t) = \mathbf{Time}(2^t)$$

(there is a special definition for this function).

Remark: if your function is weird, expect that something weird will happen.

1.9 Nondeterministic Time Hierarchy Theorem

1.9.1 Class NP

You know this, special since they are poly-time verifiable.

$$\text{NP} = \{L : \exists V \text{ verifier s.t. } \forall x, x \in L \iff \exists y, |y| \leq \text{poly}(|x|), V(x, y) \text{ accepts}\}$$

Much like Time and Space defined previously, we define NTime and NSpace as follows:

$$\begin{aligned} \text{NTime}(f(n)) &= \{L : \text{some multi-tape NTM decides } L \text{ in time at most } f(n)\} \\ \text{NSpace}(f(n)) &= \{L : \text{some multi-tape NTM decides } L \text{ in space at most } f(n)\} \end{aligned}$$

Theorem 1.9 *If f, g are time-constructible functions satisfying $f(n+1) = o(g(n))$, then*

$$\text{NTIME}(f(n)) \subsetneq \text{NTIME}(g(n)) \quad (1.3)$$

Proof:

■

1.10 Ladner's Theorem: Existence of NP-intermediate problems

Theorem 1.10 *Suppose $P \neq \text{NP}$. Then $\exists L \in \text{NP}$ such that $L \notin P$ and L is not NP-complete.*

Proof: More diagonalization, but more special this time! Suppose that $P \neq \text{NP}$ then $\text{SAT} \notin P$. Consider a padded variant of SAT called:

$$\text{SAT}_H = \{\phi 01^{n^{H(n)}} : \phi \in \text{SAT}, |\phi| = n\}$$

(read through this definition carefully each element of SAT_H is specifically defined by $H(n)$).

First note that $\exists H(n)$ computable in P such that

1. $\text{SAT}_H \in P \implies H(n) \leq O(1)$; read $H(n)$ is basically a constant.
2. $\text{SAT}_H \notin P \implies \forall c \exists n_0 \forall n \geq n_0 : H(n) > c$; read $H(n)$ is greater than every constant.

In particular we will refer to the following $H(n)$. for $n \in \mathbb{N}$ let $H(n)$ be the least $i < \log \log n$ such that TM M_i decides SAT_H on all inputs lengths $m \leq \log n$, in time $O(m^i)$; if no such TM M_i exists, then define $H(n) = \log \log n$. Observe that $H(n)$ is defined recursively. Try coming up with a $O(n^3)$ algorithm for $H(n)$. Why does this algorithm show that $\text{SAT}_H \in \text{NP}$?

Next we claim that $\text{NP} \neq P \implies \text{SAT}_H \notin P$. **Proof:** By contrapositive: assume that $\text{SAT}_H \in P$. Then we can use your algorithm for SAT_H to solve SAT! Take input ϕ for SAT. Constructing the padded version (input to SAT_H) takes poly-time since $H(n)$ is bounded by a constant (see above). Then run your poly-time SAT_H algorithm. This is a poly-time algorithm for SAT! ■

Finally we claim that $\text{NP} \neq \text{P} \implies \text{SAT}_H \notin \text{NPC}$. **Proof:** Again by contradiction: suppose that $|\phi| = n$. If you use the second then the first claim in a somewhat straight-forward way (not forgetting the definition of SAT_H), the rest of the proof should be apparent. First suppose that $\text{SAT} \leq \text{SAT}_H$. Then there exists a polynomial reduction function f bounded above by n^d for a constant $d > 0$. Thus f maps a SAT -instance ϕ of size n to a SAT_H -instance $\psi 01^{m^{H(m)}}$ where $|\psi| = m$. Since $|f(\phi)| \leq n^d$, we get that $m^{H(m)} \leq n^d$, so

$$m \leq n^{d/H(m)}$$

We have assumed that $\text{SAT}_H \notin \text{P}$. Further by the second item in the previous claim we have that $H(n) > 2d$ for large enough n . Thus for all large enough m we have $m \leq n^{1/2}$. Recall $|\psi| = m$ and $|\phi| = n$ so deciding $\phi \in \text{SAT}$ is the same as deciding if $\psi \in \text{SAT}$ but ψ is square-root the size of ϕ . By building a recursive algorithm, we can exploit this trait to obtain a poly-time algorithm to solve SAT . ■

The remainder of the theorem is recursive (o.O, yeah weird). After each step you can square root the input, after repeated trails you get down far enough to just brute force. So if $\text{SAT}_H \in \text{P}$ or $\text{SAT}_H \in \text{NP}$ we end up with a polynomial time algorithm for SAT . That is the structure of the proof.

It remains to define $H(n)$ (recursively) as follows:

$$H(n) = \begin{cases} \min_{i < \log \log n} & \text{such that } M_i \text{ decides } \text{SAT}_H \\ \log \log n & \text{otherwise} \end{cases}$$

■

The whole thing is a bit wonky, beware!

We don't know any problems which are not in P and not in NP (obviously, since that would solve the crazy problem), but we do have some candidates: such as graph isomorphism (recent result!) and integer factorization. Schaefer showed that for a set of special CPS problems the dichotomy exists. Consider:

1. Size two clauses
2. Horn clauses
3. Dual Horn
4. XOR equations
5. Trivial clauses (satisfy all zero or all one assignments)

Theorem 1.11 (*Schaefer's Dichotomy*) *If CSPs use clauses of type (i) only, for $1 \leq i \leq 5$, then $\text{CSP} - \text{SAT} \in \text{P}$. Otherwise $\text{CSP} - \text{SAT}$ is NP-complete (the numbering is from above). E.g. $2 - \text{SAT}$ is poly-time solvable.*

Here is an example of Schaefer's Dichotomy: we will mix Dual Horn clauses ($x_1 \vee x_2 \vee x_3$) and non-Dual Horn clauses of size two to get a CSP which is NP-complete. To show this, we will reduce 3-Colorability (3 - COL to this problem. Take an instance of 3 - COL.

1.11 Reducibility and NP-Completeness

1.12 SAT

Theorem 1.12 *Cook-Levin Theorem*

1. SAT is NP-complete.
2. 3SAT is NP-complete.

Proof:

■

Lemma 1.13 SAT is NP-hard.

Proof:

■

1.13 TM v.s. Circuit

For the TM, the algorithm M is the *same* for any length $|w|$. Circuits C_n is a fixed algorithm which only works for a *fixed* length input. For different lengths the circuits might potentially be quite different. Circuits are more "natural" than TMs.

Example 1.14 Given $w \in \{0,1\}^*$, decide if $w \neq 0^*$ (not all zero string). For a TM: input w scan the tape. Circuit C_4 : complete three of height 2 with or at each node.

Define the complexity class **SIZE**(s) for a function $s : \mathbb{N} \rightarrow \mathbb{N}$ to be the set of all Boolean function families $f = \{f_n\}_{n \geq 0}$ for which there exists a circuit family $\{C_n\}_{n \geq 0}$ such that C_n computes f_n , and for all sufficiently large n , $|C_n| \leq s(n)$.

Generally when deciding with circuits you need to slice the language depending on the input size. To decide L with circuits, you need a family of circuits $C = \{C_n\}$ where C_n decides L_n . We *want* circuits to be small i.e. use only a few number of logic gates.

Circuit Minimization Task:

1. given a truth table of $f : \{0,1\}^n \rightarrow \{0,1\}$ you want the smallest circuit for f (try not encoding the entire 2^n look-up table).
2. Given a circuit $C_n : \{0,1\}^n \rightarrow \{0,1\}$ find the smallest equivalent circuit.

Unfortunately both problems can only be solved by exhaustive search (currently). We also cannot categorize these problems in our hardness classes NP, NP-complete etc.

Efficient Algorithms: for uniform model (TM - different size input same machine) it is the class P , while for non-uniform models (circuits - different size inputs different machine) we want the class of poly-size which is a family $\{C_n\}$ such that $C_n \leq O(n^d)$ for some constant d .

Theorem 1.15 $P \subseteq \text{PolySize}$.

Lemma 1.16 For any proper $t : \mathbb{N} \rightarrow \mathbb{N}$, $\text{Time}(t) \subseteq \text{Size}(t \cdot \log t) \subset \text{PolySize}$

Proof: First show the weaker inclusion $\text{Time}(t) \subseteq \text{Size}(t^2)$. As follows: first consider only a finite chunk of the tape of size $2t(n)$ this is where the interesting things are going to happen. Then you want to do something similar to the SAT proof, that is you want to specify the configurations and only look at the local changes. There is at most $t(n)$ configurations and every configuration is of length $2t(n)$. You only need to connect a constant number of cells in one configure to a cell of the next so you get the $O(t^2)$ running time.

To get $\text{Size}(t \log t)$ we need to introduce **oblivious** TM. These are TM that only cares about the size of the inputs; on inputs of the same size the movement of the TM is *exactly the same*! Thus, instead of putting a gadget for each cell of every configuration, you only need to put them where the TM head is scanning. As it turns out all languages in $\text{Time}(t)$ can be decided in time $O(t \log t)$ by an oblivious time TM (how do we make such an oblivious machine? That's for you to find out!).

Using this oblivious TM we can simplify the grid to $O(t)$. ■

There are problems which are in $\text{SIZE}(\text{poly})$ (that is each circuit runs in poly-time with respect to the input size) but are not in P. Consider the following: consider the language **Halt**. This language consists of all TM descriptions which halt on the empty tape input. The associated boolean function $f_n(x_1, \dots, x_n) = 1$ iff TM M_n halts on the empty tape. Since f_n is a constant function (range is 0 or 1), the family of f_n is computable by linear-size Boolean circuits. However this family is *not* computable by any algorithm since the Halting problem is undecidable. Note: even though this linear-size Boolean circuit exists, we *cannot* find it.

However, it is not known the relationship between NP and $\text{SIZE}(\text{poly})$. It is believed that $\text{NP} > \text{SIZE}(\text{poly})$ since the converse would be a startling result due to the following claim:

Claim 1.17 *If $\text{NP} = \text{P}$, then $\text{NP} \subseteq \text{SIZE}(\text{poly})$.*

Here is a potentially useful instance of a *P-complete* problem. Consider the **Circuit Value Problem (CVP)**: given (the encoding of) a boolean circuit C and a binary string x , decide if the value of $C(x) = 1$ i.e. evaluate the circuit.

Theorem 1.18 *CVP is P-complete under log-space reductions (observe that the reduction needs to be weaker than the class itself).*

Proof: We need to show that $\text{CVP} \in \text{P}$ and that CVP is P-hard i.e. $\forall L \in \text{P}, L \leq_{\log\text{space}} \text{CVP}$. Remark that an element of CVP is $\langle C, w \rangle$ which is a circuit C and an input w . $\text{CVP} \in \text{P}$ is simple so a topological sort on the associated digraph (number nodes so the arrow satisfies the index order) and evaluate. To show that CVP is P-hard, we first construct the circuit C associated with $L \in \text{P}$; this can be done in log-space as shown in the $\text{P} \subseteq \text{SIZE}(\text{poly})$ proof. L has a poly-time decider. Take the input x of the decider and form the input $\langle C, x \rangle$ to CVP. ■

Theorem 1.19 *Circuit – SAT is NP-Complete.*

Proof: We will be using the verifier definition. Recall $\text{P} \subseteq \text{SIZE}(\text{poly})$, that is poly-time TM can be simulated on poly-size circuits. Take a verifier $V(x, y)$ and make an associated poly-size circuit $C(x_1, \dots, x_n, y_1, \dots, y_{n^d})$. For each input x form circuit $C_x(y) = C(x, y_1, \dots, y_{n^d})$ by fixing x_1, \dots, x_n to be x . Then let the reduction take $x \mapsto C_x$. ■

The two facts are necessary to get the circuit space bounds. We are not using diagonalization since the model (circuit complexity) is non-uniform. Here mostly combinatorial arguments work.

Two related problems in EXP and NEXP have to do with **succinct-Circuits**. Let $C(x_1, \dots, x_n)$ be a boolean circuit with size exponential in n . C can be described **succinctly** if every bit i of the encoding of C can

be obtained by evaluating some small circuit (polynomial size) D on input i . Think of $D(y_1, \dots, y_m)$ is a polynomial sized compression of C . To use the circuit C , you would still need to uncompress it by running every string of length m and concatenating the results, taking exponential time. *Not all exponential-sized circuits can be compressed. In fact most exponential-sized circuits on n inputs do not have associated circuits D of size $\text{poly}(n)$.*

Theorem 1.20 *Succinct-CVP is EXP-complete under logspace reductions. Where succinct-CVP is defined as: given a circuit $D(y_1, \dots, y_n)$ that is a succinct description of a circuit $C(x_1, \dots, x_n)$ of size at most 2^n , and an input $a \in \{0, 1\}^n$, compute the value $C(a)$.*