Verilog Demonstration

Downloading the Icarus Verilog

Link: https://bleyer.org/icarus/



ICARUS VERILOG

Icarus Verilog for Windows

Icarus Verilog is a free compiler implementation for the IEEE-1364 Verilog hardware description language. Icarus is maintained by Stephen Williams and it is released under the GNU GPL license.

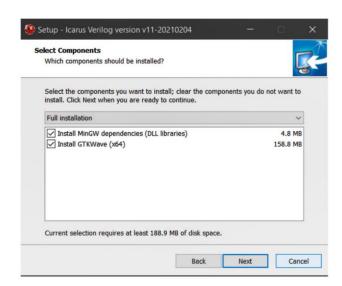
In this page you will find easy to install Icarus Verilog packages compiled with the MinGW toolchain for the Windows environment. GTKWave for Win32 is also included in the latest releases. The installers have been created with Jordan Rusell's Inno Setup free installer utility.

Download

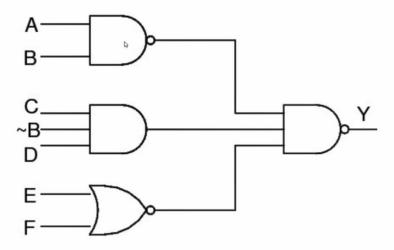
You can find Icarus Verilog sources and binaries for most platforms at the Icarus site FTP. The sources available here have been compressed with 7-zip.

Installation Icarus Verilog

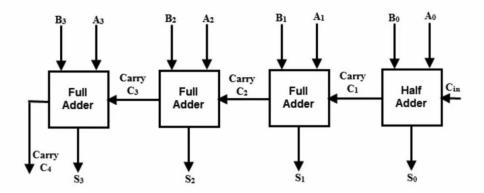
- Proceed with the Executable Setup File
- Select 'Full Installation' in the 'Select Components' section
- Tick Checkbox 'Add executable folder(s) to the user PATH'
- 4. Install



Program 1 - Gate Circuit



Program 2 - 4- Bit Adder



GTK Wave

