

# Prajnashankari M N

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My Portfolio- <https://prajnaportfolio.netlify.app/> | GitHub- <https://github.com/prajnashankari>

Passionate tech enthusiast with a knack for problem-solving, eager to contribute to innovative tech solutions and build meaningful impact.

## Education

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- **Bachelor of Engineering (B.E)- Computer Science and Engineering (Data Science)** Dec 2021- Aug 2025  
Vivekananda College of Engineering and Technology, Puttur **CGPA: 9.45 (Upto 6<sup>th</sup> sem)**
- **PUC- PCMB** Jun 2019- May 2021  
Vittal Pre-University College, Vitla **Percentage: 95.67**
- **SSLC** Jun 2015- May 2019  
Vittal High School, Vitla **Percentage: 95.2**

## Projects/ Experience

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- **SoleSense (Feet and Footwear Website)** [GitHub- SoleSense](#)  
Developed a web application that analyzes foot images using machine learning to suggest appropriate footwear.  
Technologies: Python, OpenCV, Flask, HTML, CSS, JavaScript
- **My Portfolio** [GitHub- Portfolio](#)  
Created a personal website to showcase projects, skills, and contact information with a clean responsive design.  
Technologies: HTML, CSS, JavaScript
- **WedPlanner Pro**  
Built a wedding planning system featuring CRUD operations for event and task management using DBMS.  
Technologies: SQL, HTML, CSS
- **AgriInsights** [AgriInsights](#)  
Designed a web application that retrieves and displays historical agricultural data for districts using an API.  
Technologies: API integration, JavaScript, HTML, CSS
- **Vision Vanguard ChatBotter**  
Developed a basic chatbot capable of handling simple conversations during a hackathon.  
Technologies: Python, Basic NLP

## Technical Skills

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- **Programming Languages:** C (Intermediate), Python (Intermediate), Java (Beginner)
- **Web Development:** HTML, CSS, SQL, MongoDB, JavaScript

## Licenses & Certifications

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- **Python 101 for Data Science** 2023  
IBM Developer Skills Network
- **Postman Classroom Program** 2023  
Centre for Open-source Software and Hardware, NITK Surathkal with Postman

## Leadership & Involvement

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- Branch Figure CSE (DS)- CoRE
- IEEE VCET Student Branch Registration Head