

HARINI VASUDEVAN

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OBJECTIVE

A position where there is a need for an individual with high motivation, willingness to learn and accept responsibilities

TECHNICAL COMPETENCIES

- Basics of C & C++
- NI Multisim
- Step7Microwin
- MATLAB
- Xilinx
- MS Office and Ubuntu
- MPLAB
- Proteus Design Suite
- Machine learning

EDUCATION

Bachelor of Engineering

Electronics and Communication, 89.6%

Anna University, 2010-2014

Master of Engineering

Applied Electronics, 94.3%

Anna University 2015-2017

LANGUAGES

English, Fluent; Tamil, First language

EXPERIENCE

Period: 06/2014 – 06/2015; Organization: Excel Electronics, Coimbatore, India

- **Embedded Developer:** The role of the position is to research, develop and write the embedded software as per the requirements of the client
- Successfully completed five projects in the period of six months

Period: 01/2018- 05/2018; Organization: The University of Texas at Tyler, USA

- **Research Assistant:** Engaged in the research which is sponsored by National Health Funds, USA using machine learning.
- **Teaching Assistant:** Assisted the undergraduate students in the microprocessor lab sessions.

PROJECTS / THESIS

- Designing and planning of RF network in GSM technology at Bharat Sanchar Nigam limited (BSNL)
- Feedback System for MV Panel Arrangement at Roots Industries India Limited, Coimbatore
- The FPGA implementation of steganography using CHAOS algorithm

RESPONSIBILITIES AND POSITIONS

Student Secretary of 'INFOREA 2K13', an international technical symposium; Event organizer for 'DAYA'13', an event for 3000+ orphanage student's talent expo; President of the Interact club of satellite; Secretary of the Rotaract club of INFO; Treasurer of the Indian Student Association at the University of Texas at Tyler, USA

ACHIEVEMENTS

- Won third prize in "GRAD 180" thesis competition at the University of Texas at Tyler, USA
- Secured 20th rank in Anna University among 8224 students in the Electronics and Communication Engineering stream
- Awarded as "Star of INFO", the best outgoing student award
- Won Second prize in presenting a paper on the title "Self-rechargeable mobile phones using silicon nanowires and macro fiber composites (MFC)"

PUBLICATIONS

- An international journal of engineering science and computing (IJESC), on the title 'FPGA implementation of secret data sharing through image by using LWT and LSB steganography', Vol 7, No. 6, July 2017
- An international journal of engineering science and computing (IJESC), on the title 'Secret data sharing through image by using LWT and LSB steganography', Vol 6, No. 12, December 2016

EXTRA CURRICULAR ACTIVITIES

- Actively participated in a one-year Leadership Training Programme "AS I EVOLVE"
- Actively participated in the Rotary Youth Leadership Awards (RYLA), conducted by Regional RYLA committee Region VII
- Academic Intern at Adithya Institute of Technology. The role of position is to develop e-course materials, teaching assistance and lab assistance