

**Class:** Final Year (Computer Science and Engineering)

**Year:** 2021-22                      **Semester:** 1

**Course:** High Performance Computing Lab

### Practical No. 7

**Exam Seat No:**2018BTECS00100

1. Exam Seat Number - Prakash Singh

**Problem Statement 1:** Setup the environment requirements, for execution of CUDA C programs.

**Screenshot #:**

```
prax@praxx-ideapad:~$ wget https://developer.download.nvidia.com/compute/cuda/repos/ubuntu1804/x86_64/cuda-ubuntu1804.pin
--2021-11-22 17:00:19-- https://developer.download.nvidia.com/compute/cuda/repos/ubuntu1804/x86_64/cuda-ubuntu1804.pin
Resolving developer.download.nvidia.com (developer.download.nvidia.com)... 152.199.39.144
Connecting to developer.download.nvidia.com (developer.download.nvidia.com)|152.199.39.144|:443... connected.
HTTP request sent, awaiting response... 200 OK
Length: 190 [application/octet-stream]
Saving to: 'cuda-ubuntu1804.pin'

cuda-ubuntu1804.pin 100%[=====>] 190 --.-KB/s in 0s

2021-11-22 17:00:19 (2.54 MB/s) - 'cuda-ubuntu1804.pin' saved [190/190]
```

```
prax@praxx-ideapad:~$ nvcc -V
nvcc: NVIDIA (R) Cuda compiler driver
Copyright (c) 2005-2021 NVIDIA Corporation
Built on Mon_Sep_13_19:13:29_PDT_2021
Cuda compilation tools, release 11.5, V11.5.50
Build cuda_11.5.r11.5/compiler.30411180_0
prax@praxx-ideapad:~$ S
```

**Information #:** Installed CUDA 11.5 on ubuntu 18.04 LTS

**Problem Statement 2:** Execute the attached Program 1, and understand the output.

**Screenshot #:**

```
prax@praxx-ideapad:~/Desktop/HPC/7$ ./a.out
There is 1 device supporting CUDA

Device 0: "NVIDIA GeForce MX150"
  Major revision number:      6
  Minor revision number:      1
  Total amount of global memory: 2099904512 bytes
  Total amount of constant memory: 65536 bytes
  Total amount of shared memory per block: 49152 bytes
  Total number of registers available per block: 65536
  Warp size: 32
  Multiprocessor count: 3
  Maximum number of threads per block: 1024
  Maximum sizes of each dimension of a block: 1024 x 1024 x 64
  Maximum sizes of each dimension of a grid: 2147483647 x 65535 x 65535
  Maximum memory pitch: 2147483647 bytes
  Texture alignment: 512 bytes
  Clock rate: 1531500 kilohertz
```

**Information #:** The program gives device information (NVIDIA GeForce MX150) with global memory of 2GB.

**Problem Statement 3:** Write a CUDA C program to perform the addition of two vectors of arbitrary size (Dynamic Array).

**Screenshot #:**

```
1  #include<stdio.h>
2  #include<cuda.h>
3
4  __global__ void arradd(int *x,int *y, int *z)    //kernel definition
5  {
6      int id=blockIdx.x;
7      /* blockIdx.x gives the respective block id which starts from 0 */
8      z[id]=x[id]+y[id];
9  }
10
11 int main()
12 {
13     int a[6];
14     int b[6];
15     int c[6];
16     int *d,*e,*f;
17     int i;
18     printf("\n Enter six elements of first array\n");
19     for(i=0;i<6;i++)
20     {
21         scanf("%d",&a[i]);
22     }
23     printf("\n Enter six elements of second array\n");
24     for(i=0;i<6;i++)
25     {
26         scanf("%d",&b[i]);
27     }
28
29     /* cudaMalloc() allocates memory from Global memory on GPU */
30     cudaMalloc((void **)&d,6*sizeof(int));
31     cudaMalloc((void **)&e,6*sizeof(int));
32     cudaMalloc((void **)&f,6*sizeof(int));
33
34     /* cudaMemcpy() copies the contents from destination to source. Here destination is GPU(d,e) and source is CPU(a,b) */
35     cudaMemcpy(d,a,6*sizeof(int),cudaMemcpyHostToDevice);
36     cudaMemcpy(e,b,6*sizeof(int),cudaMemcpyHostToDevice);
37
38     /* call to kernel. Here 6 is number of blocks, 1 is the number of threads per block and d,e,f are the arguments */
39     arradd<<6,1>>>(d,e,f);
40
41     /* Here we are copying content from GPU(Device) to CPU(Host) */
42     cudaMemcpy(c,f,6*sizeof(int),cudaMemcpyDeviceToHost);
43
44     printf("\nSum of two arrays:\n ");
45     for(i=0;i<6;i++)
46     {
47         printf("%d\t",c[i]);
48     }
49
50     /* Free the memory allocated to pointers d,e,f */
51     cudaFree(d);
52     cudaFree(e);
53     cudaFree(f);
54
55     return 0;
56 }
57
```

```
prax@praxx-ideapad:~/Desktop/HPC/7$ nvcc vadd.cu
prax@praxx-ideapad:~/Desktop/HPC/7$ ./a.out
```

```
Enter six elements of first array
1 2 2 2 2 2

Enter six elements of second array
1 2 2 2 2 2
```

```
Sum of two arrays:
2      4      4      4      4      4
prax@praxx-ideapad:~/Desktop/HPC/7$
```

**Github Link:**<https://github.com/prakx1/HPC-LAB/tree/master/7>

**Note: (Remove this part)**

1. Upload only .pdf file on WCE Moodle.
2. Rename .pdf file with ExamSeatNumber\_P#
3. Submit Document on moodle and code on Github in public repository.