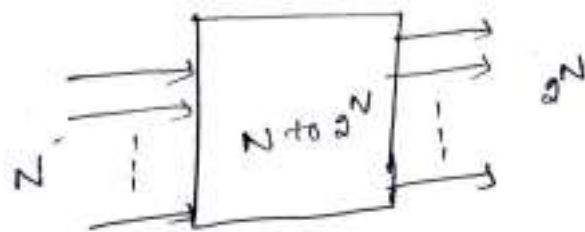


Decoder:

In digital electronics, a Combinational Logic circuit that Converts an N -bit binary input code into M output Channels in such a way that only one output channel is activated for each one of the possible combinations of inputs is known as a decoder.

→ Decoder which Converts N input lines into a maximum of 2^N output lines.

Block diagram:



2 to 4 decoder:



Truth table

A	B	y_3	y_2	y_1	y_0
0	0	0	0	0	1
0	1	0	0	1	0
1	0	0	1	0	0
1	1	1	0	0	0