

C-DAC's Advanced Computing Training School

Common Campus Placement Programme





Basic Information

Course : PG - DAC, Aug 19

Address : A/P Narande, Kumbhar lane., Kolhapur, Maharashtra



PG - DAC Marks

| S.NO. | Module | Maximum Marks (Theory) | Obtained Marks |
|-------|---|------------------------|----------------|
| 1 | OOPs with C++ programming | 40 | 20 |
| 2 | Algorithm & Data Structures | 40 | 21 |
| 3 | Database Technologies | 40 | 21 |
| 4 | Java Technologies-I(Core Java) | 40 | 27 |
| 5 | Microsoft .NET Technologies | 40 | 20 |
| 6 | Operating System Concepts | 40 | 21 |
| 7 | Advanced Software Development Methodologies | 40 | 16 |
| 8 | Advanced Web Programming | 40 | 24 |
| 9 | MEAN Stack | 40 | 30 |
| 10 | Java Technologies-II(Web Based Java) | 40 | 19 |
| | Total | 400 | 219 |

Academic Details

| Level | Stream | Institute | Board/University | Passing Year | Degree % | Division |
|-------|---------------------------------|--|--|--------------|----------|----------|
| BTech | Electronics & Telecommunication | K.E. Societys Rajarambapu Institute of Technology,Rajaramnagar,Isl ampur-415414 | Shivaji University, Kolhapur, Maharashtra | 2019 | 62.1 % | I |
| XII | Science | Shri. A.N.Nandrekar Junior College, jaysinghpur | Maharashtra | 2015 | 73.84 % | I |
| X | General | Shri.M.G.Shaha Vidyamandir Bahubali,Kumbhoj | Maharashtra | 2012 | 76.18 % | I |

Academic Projects

Title : Portal for Local Bus Transport

Platform: J2EE Duration: 1 Month

Description : Portal for Local Bus Transport, it aims at computerizing the entire process of bus management in cities to help travel management to both users and companies. This software manages different types of data management procedures in city bus transport routes, bus stops, bus details, user information with their pass details. Admin can

add, update/delete bus routes, can view user pass details. User can search for buses at particular time for a particular

routes and can also apply for monthly pass.

Title : Dynamic Power Factor Improvement Device

Platform : Embedded C Duration : 6 Months

Description : Power factor improvement plays a crucial role in the efficient system operation and electricity consumption costs reductions, so we had integrated hardware, software and real time applications into generalized device to improve power factor and we developed device tat will improve power factor up to 0.9978 as well as consume less power

and provides less disturbance in system.

Personal Information

Date of Birth: 30/09/1996Gender : MaleNationality: indiaPassport : Available

Foreign Languages : none Languages Known : Marathi, HIndi, English

I hereby declare that the information given above is true to the best of my Information knowledge belief.

Date : Signature :

P_DI_08