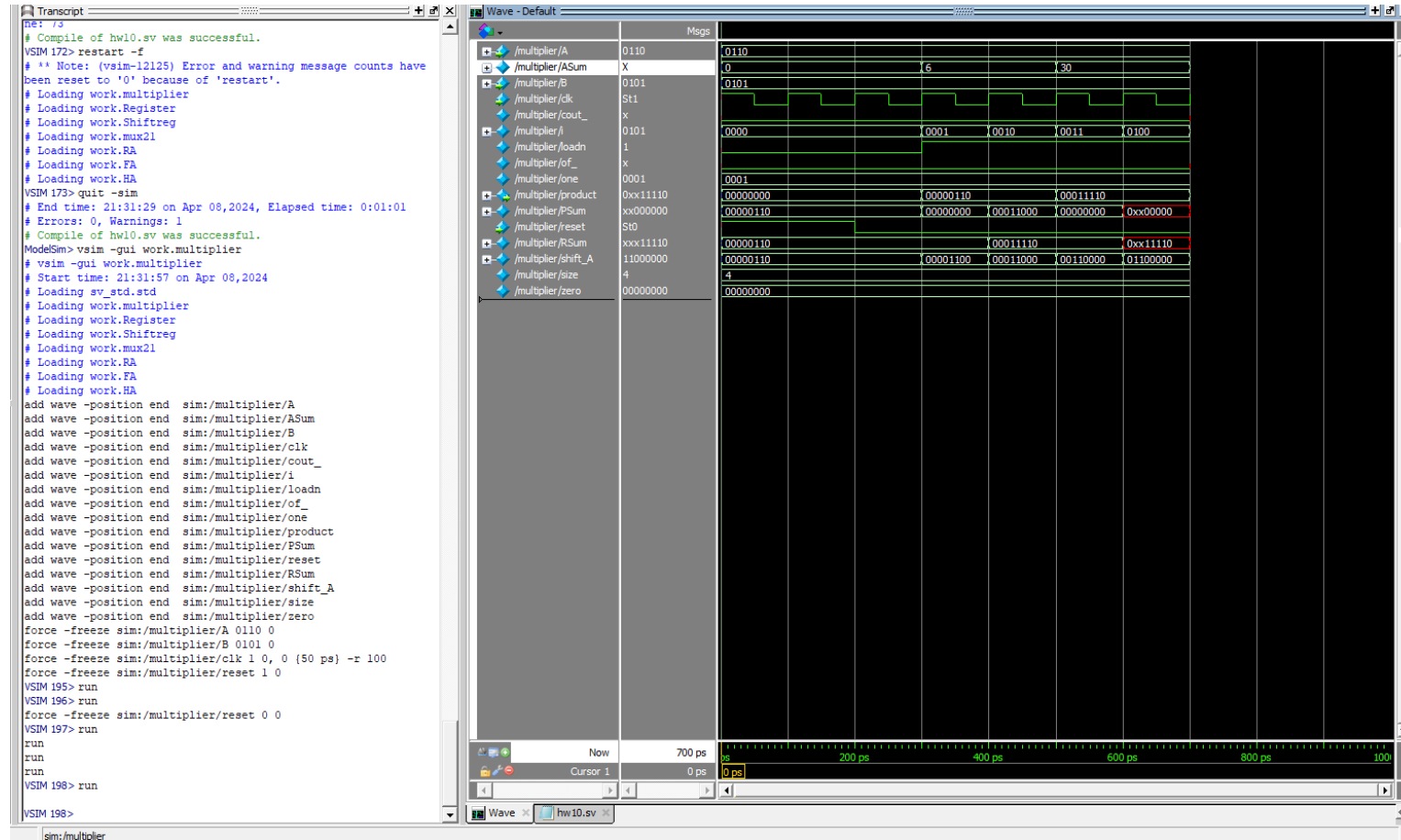


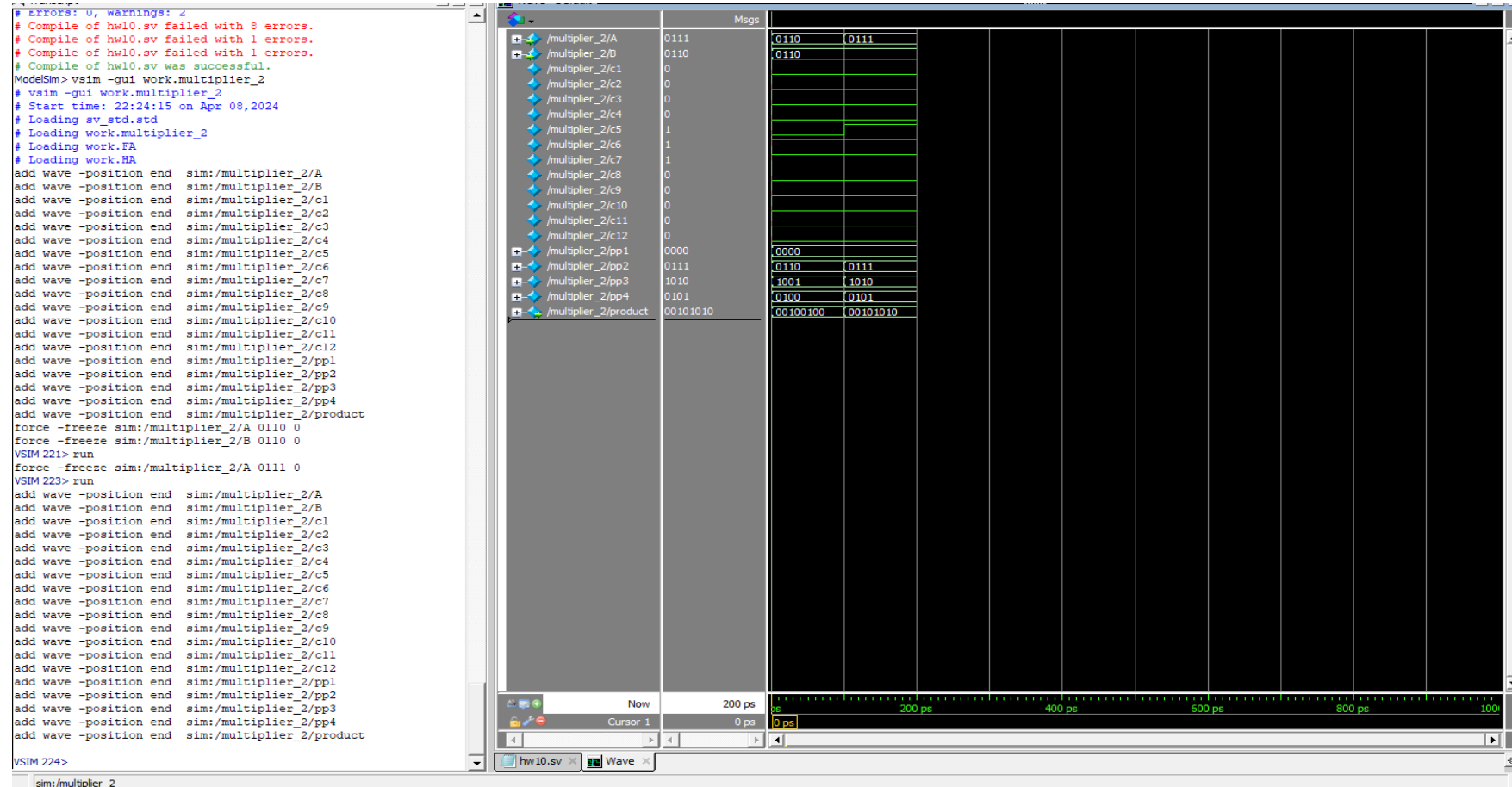
# Homework 10

Pranav Nadimpalli

# Problem 1 Successfully multiply 6 and 5 with no simulation errors

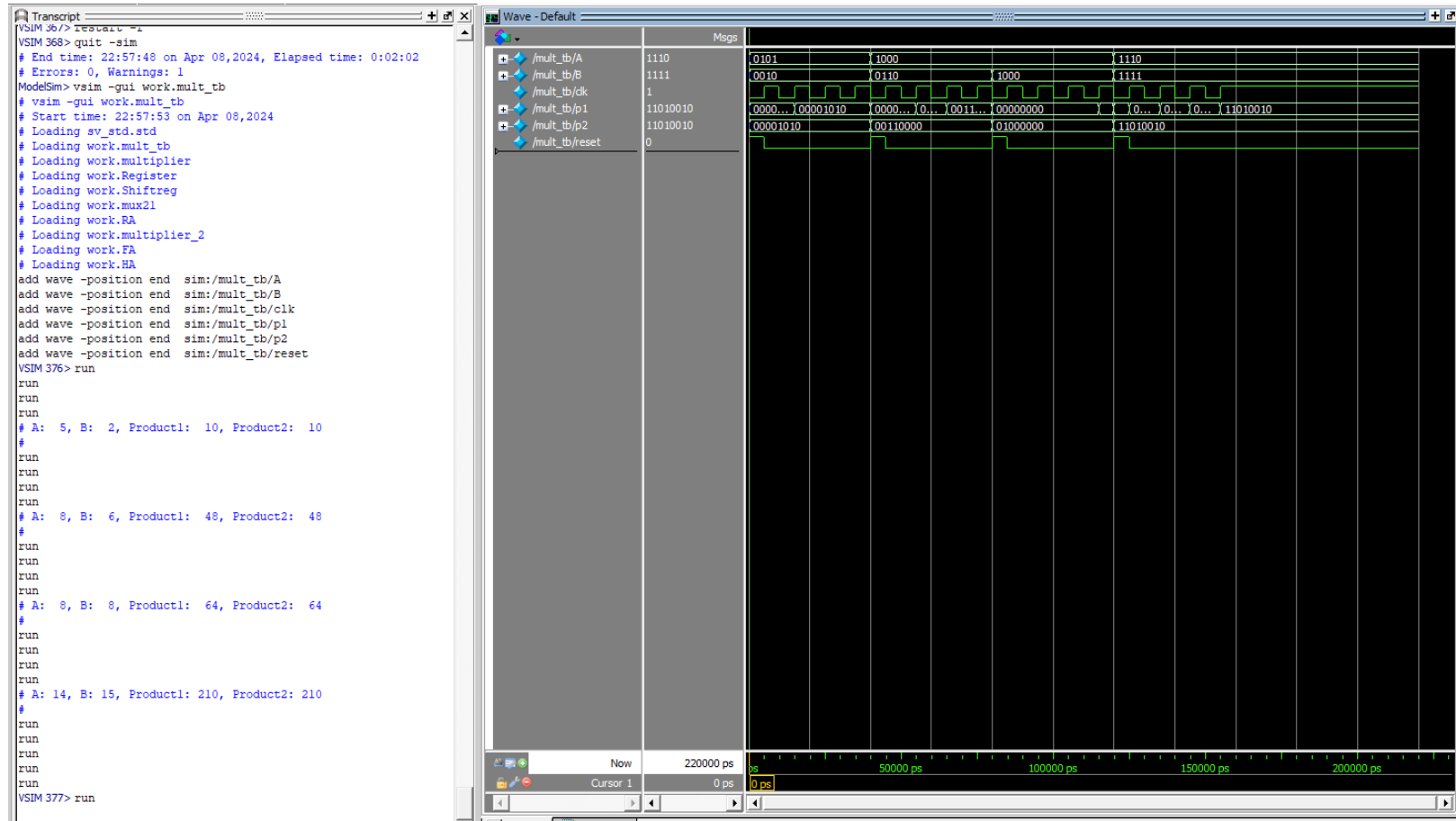


# Problem 2; successful multiplication with no port errors

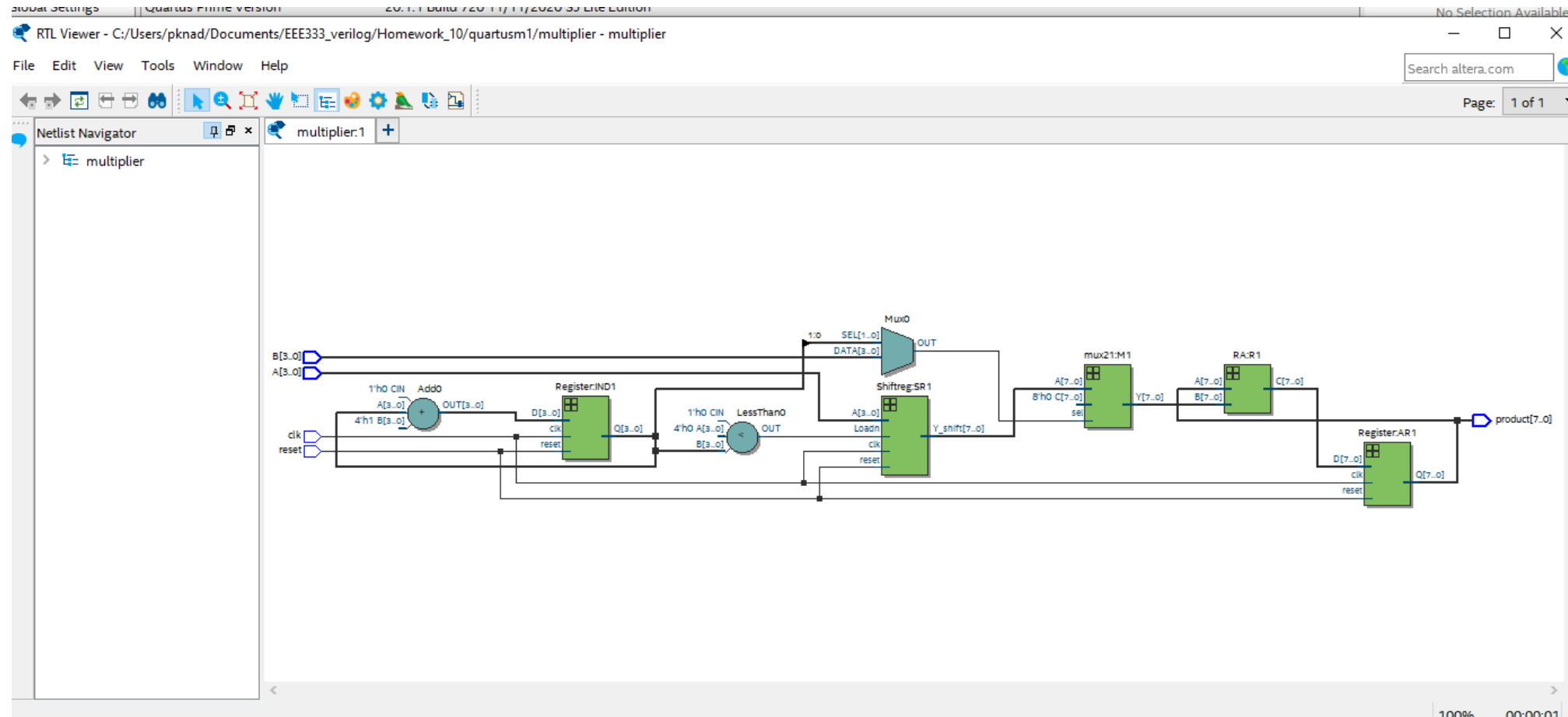


# Problem 3: Both Multipliers give same answer

important to note that product 1 is only correct after three clock cycles



# Problem 4 Clocked Multiplier RTL



# Problem 4 comb multiplier RTL

