

Birla Institute of Technology and Science, Pilani, Goa Campus



Smart Lighting System

GROUP 4 – PROBLEM 13

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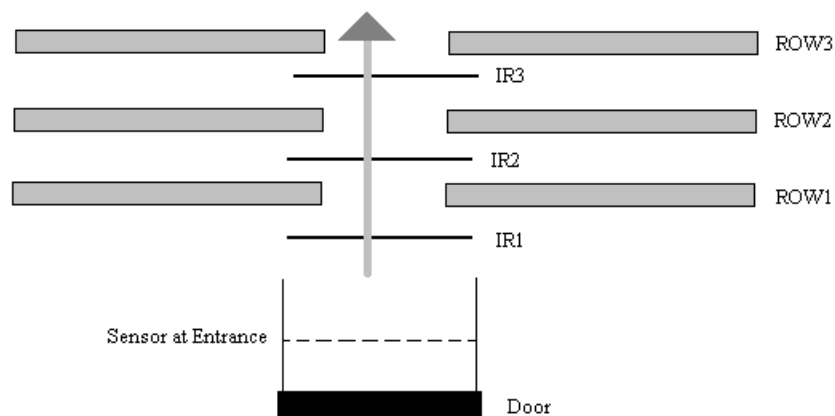
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PROBLEM STATEMENT

Smart Lighting System

Description: This is a lighting system for a conference room. As the seats get filled the light should be turned on. The rows are filled from row1 onwards. There are 4 lights per row. As each row begins to get filled the lights get turned on. As each rows empties completely the light gets turned off. You can assume there are atleast 5 rows. Entry to the auditorium is restricted to a certain point of time. Exit can be at any point of time.

System Details:



ASSUMPTIONS

- Only one person enters or leaves at a given point of time
- People occupy the first seat that is available to them (Row 2 is only occupied if seats in Row 1 are completely filled)
- 10 seats per row have been assumed
- People do not switch rows

HARDWARE DEVICES

CHIP NUMBER	CHIP	QUANTITY REQUIRED	USE
8086	Microprocessor	1	Central Processing Unit
6116	RAM 2K	2	Random access memory which contains DS,SS
2732	ROM 4K	2	Read only memory which contains entire code (CS)
74LS373	8 Bit Latch	3	To latch address bus
74LS245	8 Bit Buffer	2	To buffer data bus (bidirectional)
74LS138	3:8 Decoder	1	Used for select signals
8255	Programmable Peripheral Interface	1	Input and Output ports
8284	Clock Timer	1	For stable clock signal
LED	Common Cathode Configuration	20	For lighting
PIR SENSOR	555-2087	6	Detect motion
RJ 1V CA220	DC Relay	5	Amplification for lights in each row
AND, OR, NOT	Gates	8 Or, 2 Not, 1 And	Logic Circuit

JUSTIFICATION

- 2732 – 2 nos. Smallest ROM chip available is 4K in Proteus and as we need to have even and odd bank and ROM is required at reset address which is at FFFF0H and 00000H - where there is the IVT
- 6116 – 2 nos. Smallest RAM chip available is 2 K and we need odd and even bank. We need RAM for stack and temporary storage of data

MAPPING

Memory Organization:

The system uses 4KB of RAM and 8KB of ROM. RAM consists of two 2K chips and ROM consists of 4K chips. They are organized into odd and even bank to facilitate both byte and word size data transfers.

Read Only Memory (2732): Starting Address: 00000h, Ending Address: 01FFFh

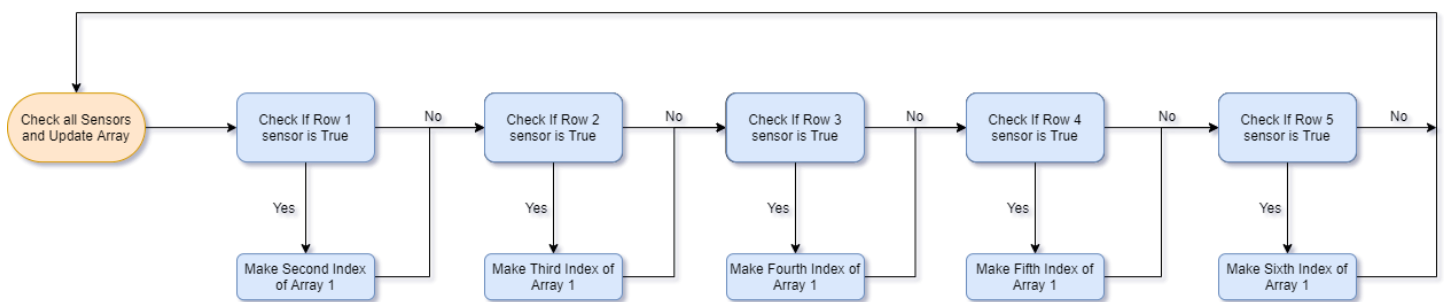
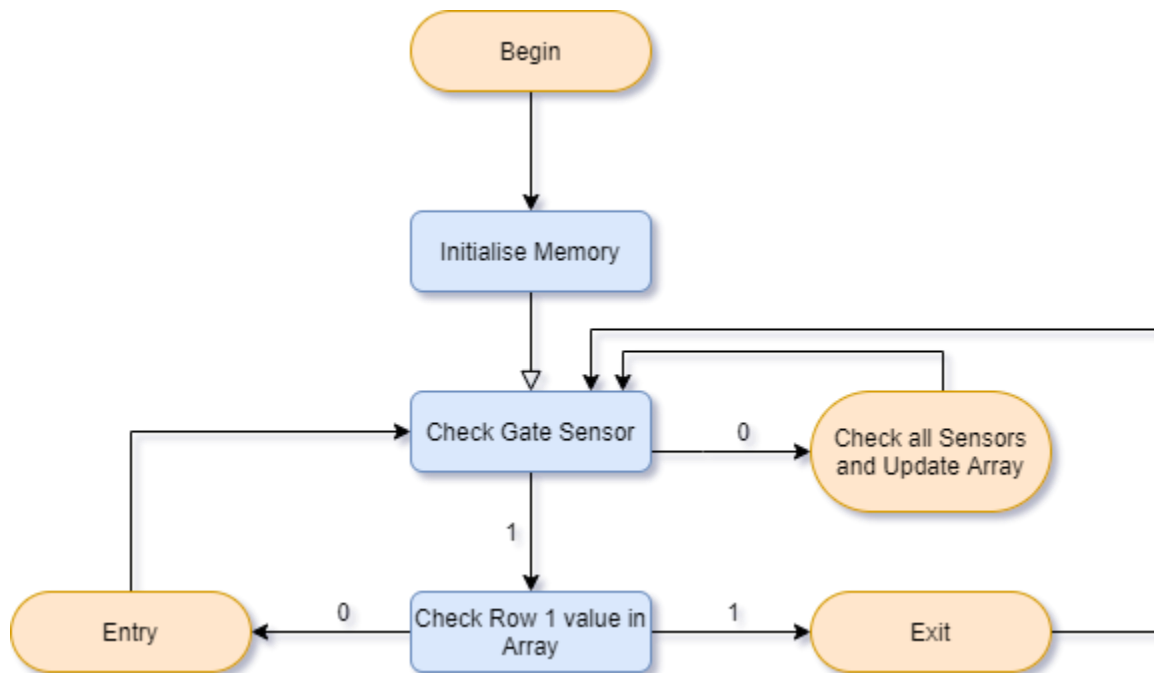
Random Access Memory (6116): Starting Address: 02000h, Ending Address: 02FFFh

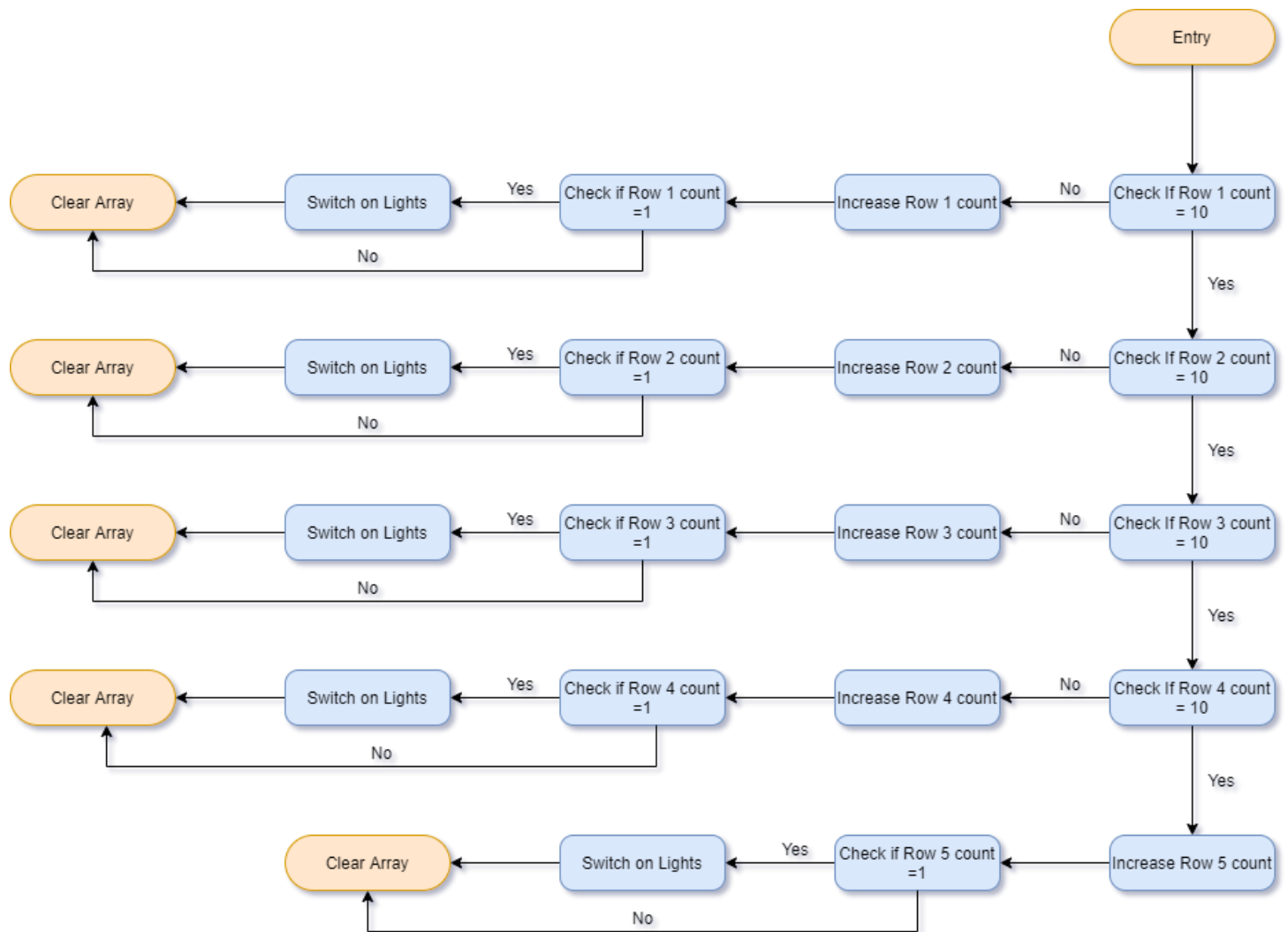
CHIP	A19	A18	A17	A16	A15	A14	A13	A12	A11	A10	A9	A8	A7	A6	A5	A4	A3	A2	A1	A0
ROM :FROM	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ROM :TO	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1
RAM :FROM	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RAM :TO	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	1

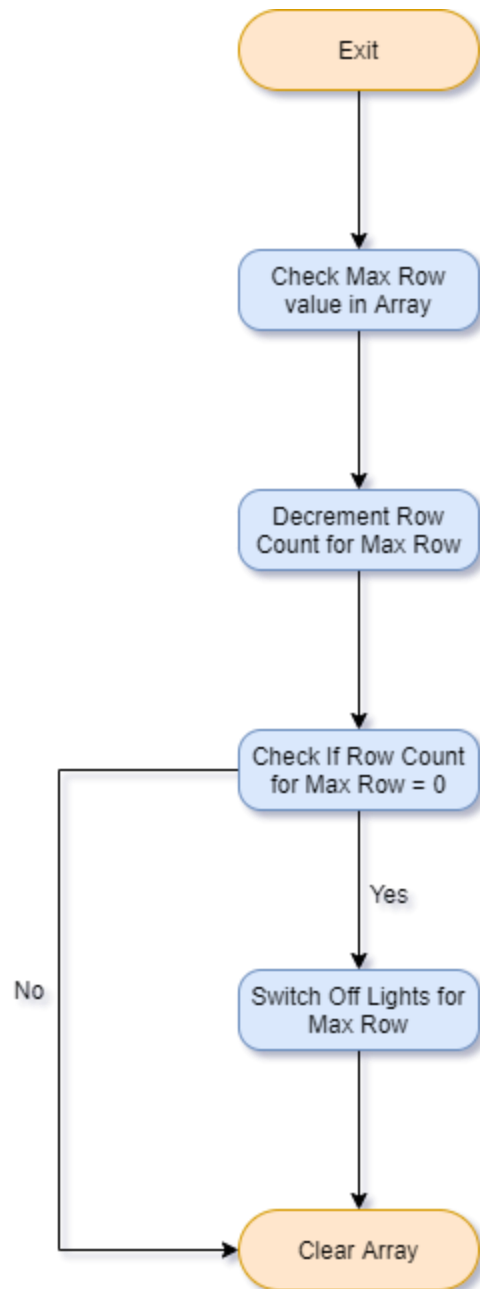
I/O Mapping:

8255-0 Port	Address	Usage
Port A	00000H	Input from Sensors
Port B	00002H	Not Used
Port C	00004H	Output to LEDs
CWR	00006H	Control Register

FLOWCHART







CODE

;MAIN PROGRAM

COUNTER DW 00H

RCOUNTER DB 00H,00H,00H,00H,00H,00H ;Maintains an array of which sensors have been activated till gate sensor is activated

LSTATUS DB 0 ;Maintains a status of lights

MAXROW DB 00H ;Max # of the row that was pressed during exit

SEATS DB 00H,00H,00H,00H,00H,00H ;Individual Row Count for all rows

;8255-0

PORTA0 EQU 00H

PORTB0 EQU 02H

PORTC0 EQU 04H

COMMAND_ADDRESS0 EQU 06H

JMP ST1

DB 1001 DUP(0)

ST1:

; INITIALIZE DS, ES,SS TO START OF RAM

MOV AX,02000H

MOV DS,AX

MOV ES,AX

MOV SS,AX

MOV SP,02FFEH

;intialise port a as input & b& c as output

mov al,00110110b

out 0eh,al

mov al,4

out 08h,al

mov al,0

out 08h,al

mov al,90h

out 06h,al

MOV SEATS,00H

MOV SEATS+1,00H

MOV SEATS+2,00H

MOV SEATS+3,00H

MOV SEATS+4,00H

MOV RCOUNTER,00H

MOV RCOUNTER+1,00H

MOV RCOUNTER+2,00H

MOV RCOUNTER+3,00H

MOV RCOUNTER+4,00H

MOV RCOUNTER+5,00H

MOV LSTATUS,00H

MOV MAXROW,00H

;;CHECK FOR ENTRY THROUGH GATE

X1: IN AL,00H

AND AL,80H

CMP AL,80H

JNE X2

JMP X7 ; X7 is the sequence where gate emits code 1 aka it is interrupted

;; this is the code for the Check all sensors and update array part of code

;;CHECK FOR Sensor Interrupt IN ROW1

X2: IN AL,00H

AND AL,40H

CMP AL,40H

JNE X3

MOV CX, 0D000h ;Add delay because if delay is not added, the loop will go on too fast and increase count to an out of bounds value

W3:

NOP

NOP

NOP

NOP

NOP

LOOP W3

ADD RCOUNTER+1,1

CMP RCOUNTER+1,0

JE X1

CMP RCOUNTER+2,0

JNE X1

MOV MAXROW,1

;;CHECK FOR Sensor Interrupt IN ROW2

X3: IN AL,00H

AND AL,20H

CMP AL,20H

JNE X4

MOV CX, 0D000h

W4:

NOP

NOP

NOP

NOP

NOP

LOOP W4

ADD RCOUNTER+2,1

CMP RCOUNTER+2,0

JE X1

CMP RCOUNTER+3,0

JNE X1

MOV MAXROW,2

;;CHECK FOR Sensor Interrupt IN ROW3

X4: IN AL,00H

AND AL,10H

CMP AL,10H

JNZ X5

MOV CX, 0D000h

W5:

NOP

NOP

NOP

NOP

NOP

LOOP W5

ADD RCOUNTER+3,1

CMP RCOUNTER+3,0

JE X1

CMP RCOUNTER+4,0

JNE X1

MOV MAXROW,3

;;CHECK FOR Sensor Interrupt IN ROW4

X5: IN AL,00H

AND AL,08H

CMP AL,08H

JNE X6

```
MOV CX, 0D000h
```

```
W6:
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
LOOP W6
```

```
ADD RCOUNTER+4,1
```

```
CMP RCOUNTER+4,0
```

```
JE X1
```

```
CMP RCOUNTER+5,0
```

```
JNE X1
```

```
MOV MAXROW,4
```

```
;;CHECK FOR Sensor Interrupt IN ROW5
```

```
X6: IN AL,00H
```

```
AND AL,04H
```

```
CMP AL,04H
```

```
JNE X1
```

```
; X1 is the sequence that checks the gate
```

```
MOV CX, 0D000h
```

```
W7:
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
    NOP
```

```
LOOP W7
```

```
ADD RCOUNTER+5,1
```

```
CMP RCOUNTER+5,0
```

JE X1

MOV MAXROW,5

JMP X1

;; Check row 1 array value

X7: MOV RCOUNTER,1

CMP RCOUNTER+1,1

JE Y1 ; Y1 is the sequence for exit

JMP Z1 ; Z1 is the sequence for entry

;; Entry Sequence

;; Check if Row1 count is 10

Z1: CMP SEATS,10

JNE Z2

;; Check if Row2 count is 10

Z3: CMP SEATS+1,10

JNE Z4

;; Check if Row3 count is 10

Z5: CMP SEATS+2,10

JNE Z6

;; Check if Row4 count is 10

Z7: CMP SEATS+3,10

JNE Z8

;; Increment Row 5

Z9: SUB RCOUNTER+1,1


```

SUB RCOUNTER+2,1
SUB RCOUNTER+3,1
SUB RCOUNTER+4,1
ADD SEATS+4,1
CMP SEATS+4,0
JLE C2                                ;C2 is the sequence that clears the array RCOUNTER's gate value

```

```

MOV AL,LSTATUS                        ;Load current status of lights into al so they dont get changed
MOV BL,00001000b                     ;Make sure the light in 5th row is on by or with current status
OR AL,BL
OUT 04H, AL                           ;Output now condition to port C
MOV LSTATUS,AL                       ;Update current status of lights

```

```

JMP C2

```

```

Z2: SUB RCOUNTER+1,1
ADD SEATS,1
CMP SEATS,0
JLE C2                                ;C2 is the sequence that clears the array RCOUNTER's gate value

```

```

MOV AL,LSTATUS                        ;Load current status of lights into al so they dont get changed
MOV BL,10000000b                     ;Make sure the light in 1st row is on by or with current status
OR AL,BL
OUT 04H, AL                           ;Output now condition to port C
MOV LSTATUS,AL                       ;Update current status of lights

```

```

JMP C2

```

```

Z4: SUB RCOUNTER+1,1
SUB RCOUNTER+2,1
ADD SEATS+1,1
CMP SEATS+1,0
JLE C2                                ;C2 is the sequence that clears the array RCOUNTER's gate value

```

```

MOV AL,LSTATUS           ;Load current status of lights into al so they dont get changed
MOV BL,01000000b         ;Make sure the light in 2nd row is on by or with current status
OR AL,BL
OUT 04H, AL               ;Output now condition to port C
MOV LSTATUS,AL           ;Update current status of lights

```

```

JMP C2

```

```

Z6:  SUB RCOUNTER+1,1
      SUB RCOUNTER+2,1
      SUB RCOUNTER+3,1
      ADD SEATS+2,1
      CMP SEATS+2,0
      JLE C2               ;C2 is the sequence that clears the array RCOUNTER's gate value

```

```

MOV AL,LSTATUS           ;Load current status of lights into al so they dont get changed
MOV BL,00100000b         ;Make sure the light in 3rd row is on by or with current status
OR AL,BL
OUT 04H, AL               ;Output now condition to port C
MOV LSTATUS,AL           ;Update current status of lights

```

```

JMP C2

```

```

Z8: SUB RCOUNTER+1,1
      SUB RCOUNTER+2,1
      SUB RCOUNTER+3,1
      SUB RCOUNTER+4,1
      ADD SEATS+3,1
      CMP SEATS+3,0
      JLE C2               ;C2 is the sequence that clears the array RCOUNTER's gate value

```

```

MOV AL,LSTATUS           ;Load current status of lights into al so they dont get changed
MOV BL,00010000b         ;Make sure the light in 4th row is on by or with current status
OR AL,BL

```

OUT 04H, AL ;Output now condition to port C

MOV LSTATUS,AL ;Update current status of lights

JMP C2

;; Clear Array

C1: MOV RCOUNTER,0

MOV RCOUNTER+1,00h

MOV RCOUNTER+2,00h

MOV RCOUNTER+3,00h

MOV RCOUNTER+4,00h

MOV RCOUNTER+5,00h

MOV MAXROW,00h

MOV CX, 0D000h

W2:

NOP

NOP

NOP

NOP

NOP

LOOP W2

JMP X1

C2: MOV RCOUNTER,0

MOV MAXROW,00h

MOV CX, 0D000h

W1:

NOP

NOP

NOP

NOP

NOP

LOOP W1

JMP X1

;;Exit Sequence

;; Decrement the row count for max row value

Y1: CMP MAXROW,1

;Check MaxRow Value

JNE Y2

SUB SEATS,1

;Subtract Row Count of MaxRow

CMP SEATS,0

;Check If the count has become 0

JNE C1

MOV AL,LSTATUS

;Load Current state of Lights in AL

MOV BL,01111111b

;conserve all values except row LEDs

AND AL,BL

OUT 04H,AL

;Output to port C

MOV LSTATUS,AL

;Update status of Lights

JMP C1

Y2: CMP MAXROW,2

JNE Y3

SUB SEATS+1,1

CMP SEATS+1,0

JNE C1

MOV AL,LSTATUS

MOV BL,10111111b

AND AL,BL

OUT 04H,AL

MOV LSTATUS,AL

JMP C1

Y3: CMP MAXROW,3

JNE Y4

SUB SEATS+2,1

CMP SEATS+2,0

JNE C1

MOV AL,LSTATUS

MOV BL,11011111b

AND AL,BL

OUT 04H,AL

MOV LSTATUS,AL

JMP C1

Y4: CMP MAXROW,4

JNE Y5

SUB SEATS+3,1

CMP SEATS+3,0

JNE C1

MOV AL,LSTATUS

MOV BL,11101111b

AND AL,BL

OUT 04H,AL

MOV LSTATUS,AL

JMP C1

Y5: SUB SEATS+4,1

CMP SEATS+4,0

JNE C1

MOV AL,LSTATUS

MOV BL,11110111b

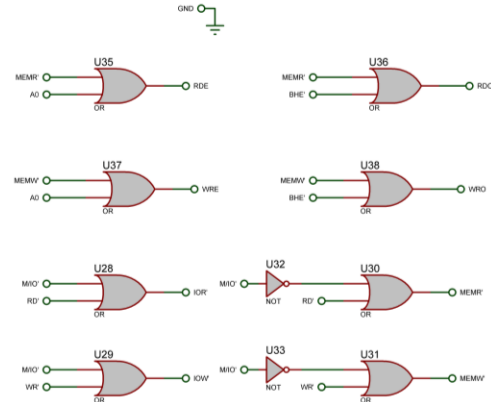
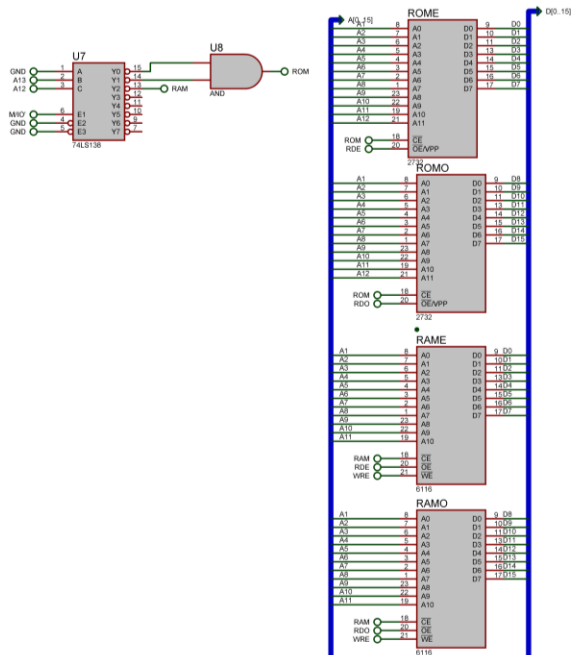
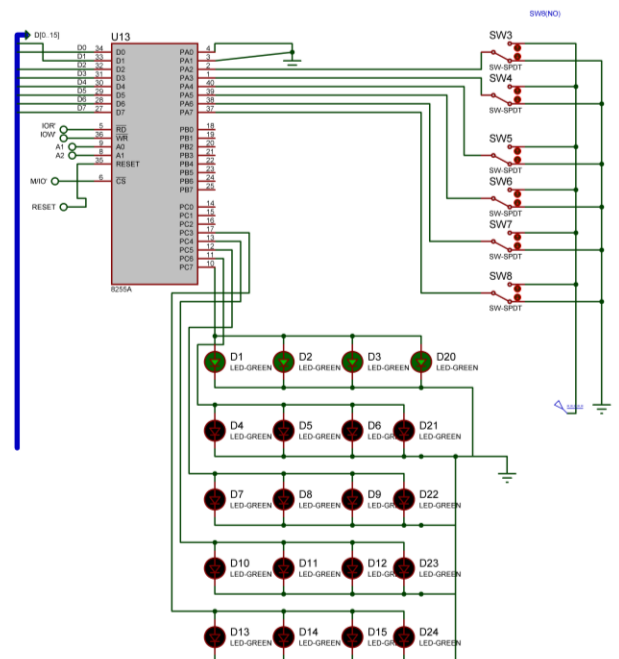
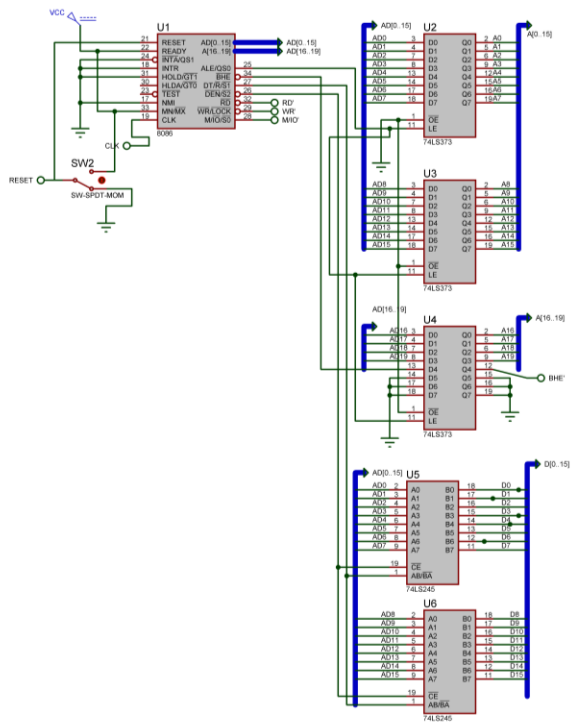
AND AL,BL

OUT 04H,AL

MOV LSTATUS,AL

JMP C1

The diagram illustrates a digital logic circuit for a 16x16 matrix keypad. The central component is an 8086 microprocessor, which is connected to a 74LS373 register and a 74LS374 register. The 8086 is also connected to an 8255A PPI (Programmable Peripheral Interface) and a 74LS373 register. The 8255A is connected to a 16x16 matrix keypad. The keypad is organized into 16 rows and 16 columns. The circuit includes various logic gates (AND, OR, NOT) and a 16x16 matrix keypad layout. The diagram is labeled with component values, pin numbers, and logic symbols.



FIRMWARE

Implemented using emu8086

Implemented on Proteus 8

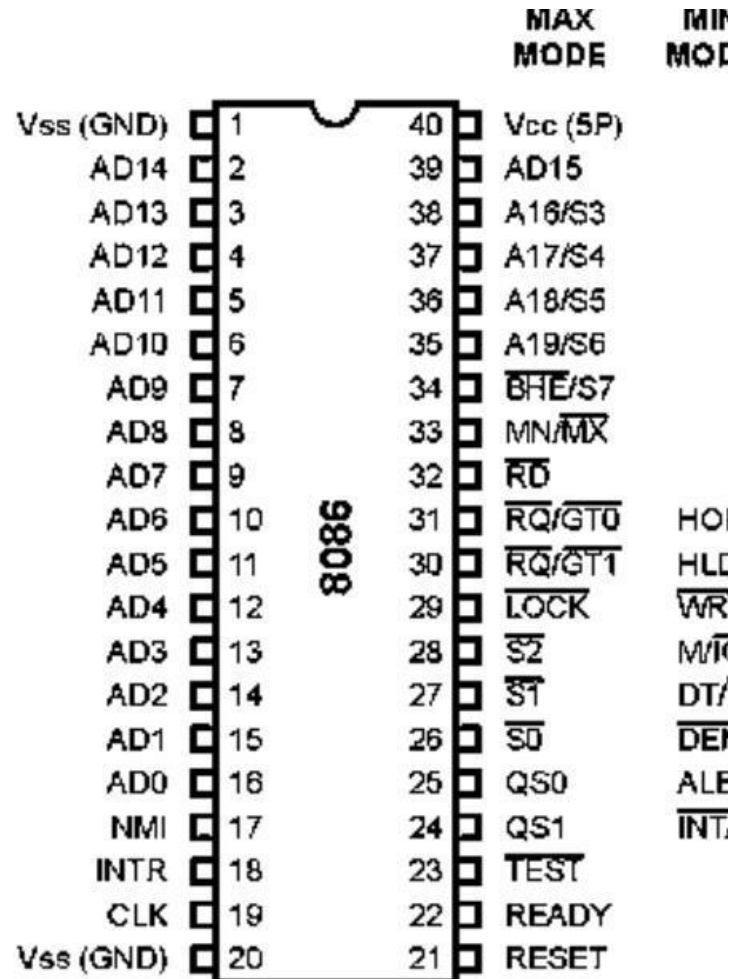
The Full Diagram and Each Component's Individual diagram has been uploaded as a separate file

There is also a walkthrough video on YouTube made by us that explains the Code, it's logic and it's working

It can be found at:

<https://youtu.be/lshg31mhzko>

REFERENCES



PIR SENSOR

The PIR (Passive Infra-Red) Sensor is a pyroelectric device that detects motion by sensing changes in the infrared (radiant heat) levels emitted by surrounding objects. This motion can be detected by checking for a sudden change in the surrounding IR pattern. When motion is detected the PIR sensor outputs a high signal on its output pin. This logic signal can be read by a microcontroller or used to drive an external load. PDF Documentation Attached

VARIATIONS IN PROTEUS IMPLEMENTATION

- 8284 is not shown explicitly as Proteus allows setting of time from within the 8086 module
- Since sensors are not available in Proteus, Switches have been used to simulate the working of sensors
- 2732 used as 2716 is not available in Proteus
- DC Relays were not added as LEDs glow on very low voltage and are not needed in Proteus

List Of Attachments

- Hardware Design – Hardware.pdf
- EMU8086 ASM File – final.asm
- Binary File – final.bin
- Manual for PIR 555-28027
- Proteus File – SmartLighting.pdrspj