- Analytical models for parallel computing are used to analyze and predict the performance of parallel algorithms and systems.
- This models provide insights into the scalability, efficiency, and resource utilization of parallel programs running on parallel computing architectures.
- Following are few analytical model commonly used:
 - Execution time models
 - Speecup models
 - Work span models
 - Queuing models
 - Communication models

• Execution time:

A sequential algorithm is usually evaluated in terms of its execution time, expressed as a function of the size of its input.

The execution of a parallel program depends not only on the input size but also on the number of processing elements used, and their relative computation and inter-process communication speeds.

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Sources of overhead:

Inter-process interaction Idling
Excess computation

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The overhead incurred by a parallel program are encapsulated in a single expression referred to as the overhead function.

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 T_o

$$T_0 = pT_p - T_s$$

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Speed is the measure that captures the relative benefit of solving a problem in parallel. It is defined as the ratio of the time taken to solve a problem on a single processing element to the time required to solve the same problem on a parallel computer with 'p' identical processing elements.

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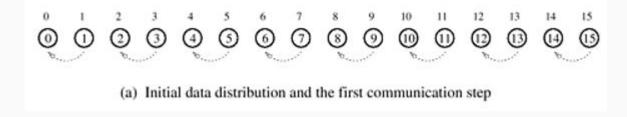
$$S = \frac{T_S}{T_p}$$

Speedup Example:

Adding 'n' number using 'n' processing elements.

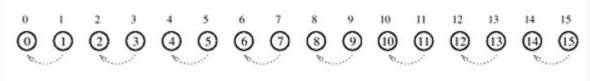
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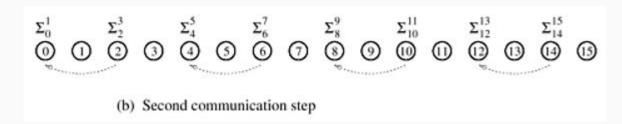
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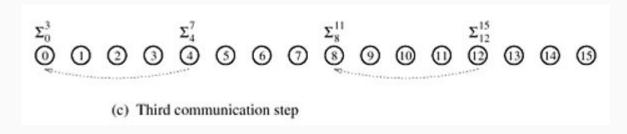


(a) Initial data distribution and the first communication step

Speedup Example:

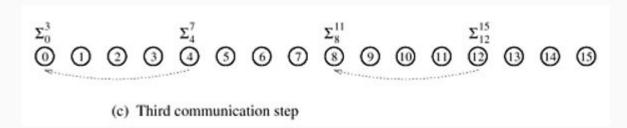
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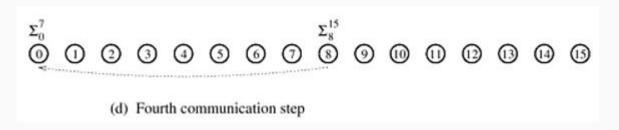




Speedup Example:

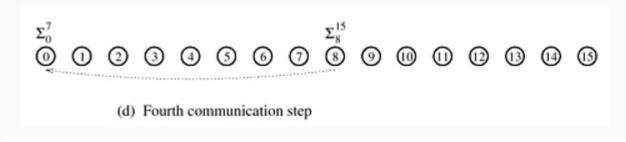
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Speedup Example:

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(e) Accumulation of the sum at processing element 0 after the final communication

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Speed up

$$S = \frac{T_S}{T_p} = O(\frac{n}{\log_2 n})$$

Speedup - Amdhal's Law:

In computer architecture Amdhal's law is a formula which gives theoretical speedup in latency of the execution task of a fixed workload that can be expected of a system whose resources are improved.

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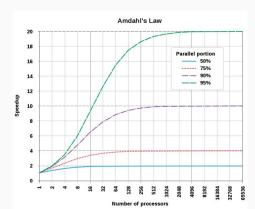
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Caches

Exploratory decomposition

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A problem of size 'W' | Problem is memory bound 64kb cache on one processor
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Single processor system:

Problem size = W |

Memory access time (cache hit 80%):

 $2 \times 0.8 + 100 \times 0.2 = 21.6$ ns

Processing rate:

1000 / 21.6 = 46.3 MFLOPS

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Two processor system:

Problem size = W / 2

Memory access time (cache hit 90%):

 $2 \times 0.9 + 100 \times 0.08 + 400 \times 0.02 = 17.8 \text{ ns}$

Processing rate (2 procs simultaneously):

(2 x 1000) / 17.8 = 112.36 MFLOPS

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Only an ideal parallel system containing 'p' processors can deliver a speedup equal to 'p'. In practice, ideal behaviour is not achieved because while executing a parallel program, the processing elements cannot devote 100% of their time to the computation of the algorithm.

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$$E = \frac{S}{p} \ or \ \frac{T_S}{pT_p}$$

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$$E = \frac{T_S}{C}$$

- Very often, programs are designed and tested for smaller problems on fewer processing elements.
- The real problems are intended to solve are much larger, and the machines contain larger number of processing elements.
- Code development is simplified by using scaled-down versions of the machine and the problem, their performance and correctness is much more difficult to establish based on scaled down systems.
- We have to apply difference techniques for evaluating the scalability of the parallel programs using analytical tools.

"One of the best programming skills you can have is knowing when to walk away for a while."

Questions?

Thanks!

Contact:

Vineet More

vineet.more.bfab@gmail.com

[Make sure to use HPCAP23 as the subject line for your queries]

LinkedIn Handle:

https://www.linkedin.com/in/vineet-more-c-programmer/

