High Performance Computing (HPC) MCQs [set-14]

326. If there is 4X4 Mesh Topology message passing cycles will require
complete all to all reduction.
A. 4
B. 6
C. 8
D. 16 Answer: C
327. Following issue(s) is/are the true about sorting techniques with parallel
computing.
A. large sequence is the issue
B. where to store output sequence is the issue
C. small sequence is the issue
D. none of the above Answer: B
328. Partitioning on series done after
A. local arrangement
B. processess assignments
C. global arrangement
D. none of the above Answer: C
329. In Parallel DFS processes has following roles.(Select multiple choices if
applicable)
A. donor
B. active
C. idle
D. passive Answer: A

330. Suppose there are 16 elements in a series then how many phases will be		
required to sort the series using parallel odd-even bubble sort?		
A. 8		
B. 4		
C. 5		
D. 15 Answer: D		
Allowell D		
331. Which are different sources of Overheads in Parallel Programs?		
A. interprocess interactions		
B. process idling		
C. all mentioned options		
D. excess computation Answer: C		
332. The ratio of the time taken to solve a problem on a parallel processors to the		
time required to solve the same problem on a single processor with p identical		
processing elements.		
A. the ratio of the time taken to solve a problem on a single processor to the time required to		
solve the same problem on a parallel computer with p identical processing elements.		
B. the ratio of the time taken to solve a problem on a single processor to the time required to		
solve the same problem on a parallel computer with p identical processing elements		
C. the ratio of number of multiple processors to size of data		
D. none of the above Answer: B		
333. CUDA helps do execute code in parallel mode using		
A. cpu		
B. gpu		
C. rom		
D. cash memory		
Answer: B		
334. In thread-function execution scenario thread is a		
A. work		
B. worker		
C. task		

D. none of the above swer: B	
5. In GPU Following statements are true	
A. grid contains block	
B. block contains threads	
C. all the mentioned options.	
D. sm stands for streaming multiprocessor swer: C	
6. Computer system of a parallel computer is capable of	
A. decentralized computing	
3. parallel computing	
C. centralized computing	
D. all of these swer: A	
7. In which application system Distributed systems can run well?	
A. hpc	
3. distrubuted framework	
C. hrc	
D. none of the above swer: A	
8. A pipeline is like?	
A. an automobile assembly line	
3. house pipeline	
C. both a and b	
D. a gas line swer: A	
9. Pipeline implements ?	
A. fetch instruction	
3. decode instruction	
C. fetch operand	
D. all of above swer: D	

execution of another instruction is called ?
A. super-scaling
B. pipe-lining
C. parallel computation
D. none of these
Answer: B
341. VLIW stands for ?
A. very long instruction word
B. very long instruction width
C. very large instruction word
D. very long instruction width
Answer: A
342. Which one is not a limitation of a distributed memory parallel system?
A. higher communication time
B. cache coherency
C. synchronization overheads
D. none of the above
Answer: B
343. Which of these steps can create conflict among the processors?
A. synchronized computation of local variables
B. concurrent write
C. concurrent read
D. none of the above
Answer: B
344. Which one is not a characteristic of NUMA multiprocessors?
A. it allows shared memory computing
B. memory units are placed in physically different location
C. all memory units are mapped to one common virtual global memory
D. processors access their independent local memories Answer: D
345. Which of these is not a source of overhead in parallel computing?

A. non-uniform load distribution	
B. less local memory requirement in distributed computing	
C. synchronization among threads in shared memory computing	
D. none of the above Answer: B	
346. Systems that do not have parallel processing capabilities are?	
A. sisd	
B. simd	
C. mimd	
D. all of the above Answer: A	
347. How does the number of transistors per chip increase according to Moore 's	
aw?	
A. quadratically	
B. linearly	
C. cubicly	
D. exponentially Answer: D	
348. Parallel processing may occur?	
A. in the instruction stream	
B. in the data stream	
C. both[a] and [b]	
D. none of the above Answer: C	
349. To which class of systems does the von Neumann computer belong?	
A. simd (single instruction multiple data)	
B. mimd (multiple instruction multiple data)	
C. misd (multiple instruction single data)	
D. sisd (single instruction single data) Answer: D	
350. Fine-grain threading is considered as a threading?	
A. instruction-level	
B. loop level	

C. task-level

D. function-level

Answer: A