High Performance Computing (HPC) MCQs [set-7]

151. every node has to know when	n to communicate that is
A. call the procedure	
B. call for broadcast	
C. call for communication	
D. call the congestion Answer: A	
152. the procedure is disturbed an	
A. synchronization	
B. communication	CO.
C. both	40,
D. none Answer: A	Mate coll.
153. Renaming relative to the sou A. xor	rce is the source.
B. xnor	
C. and	
D. nand	
Answer: A	
154. Task dependency graph is	
A. directed	
B. undirected	
C. directed acyclic	
D. undirected acyclic Answer: C	
155. In task dependency graph lo	ngest directed path between any pair of start and

finish node is called as -----

A. total work

- B. critical path
- C. task path
- D. task length

Answer: B

156. which of the following is not a granularity type

- A. course grain
- B. large grain
- C. medium grain
- D. fine grain

Answer: B

157. which of the following is a an example of data decomposition

- A. matrix multiplication
- B. merge sort
- C. quick sort
- D. 15 puzzal

Answer: A

158. which problems can be handled by recursive decomposition

- A. backtracking
- B. greedy method
- C. divide and conquer problem
- D. branch and bound

Answer: C

159. In this decomposition problem decomposition goes hand in hand with its execution

- A. data decomposition
- B. recursive decomposition
- C. explorative decomposition
- D. speculative decomposition

Answer: C

160. which of the following is not an example of explorative decomposition

- A. n queens problem
- B. 15 puzzal problem

	one way task ver: B
C.	regular task
В.	static task
Α.	dynamic task
64.	Intask are defined before starting the execution of the algorithm
Ansv	ver: D
D.	ordered statistics
C.	finding cycle in a graph
В.	finding deadlock in an operating system
	finding prerequisite of a task
l 63.	Which of the following is not an application of topological sorting?
	ver: D
	zero degree
	any degree
	minimum degree
	In most of the cases, topological sort starts from a node which has maximum degree
	directed acyclic graphs ver: D
	undirected acyclic graphs
	directed cyclic graphs
	undirected cyclic graphs
	Topological sort can be applied to which of the following graphs?
ANSV	ver: D
	quick sort

partitioning

- A. block
- B. cyclic
- C. block cyclic

D. CHUIK	
Answer: D	_
166. blocking optimization is used to improve temmporal locality for reduce	
A. hit miss	
B. misses	
C. hit rate	
D. cache misses	
Answer: B	-
167. CUDA thought that 'unifying theme' of every form of parallelism is	
A. cda thread	
B. pta thread	
C. cuda thread	
D. cud thread	
Answer: C	-
168. Topological sort of a Directed Acyclic graph is?	
A. always unique	
B. always not unique	
C. sometimes unique and sometimes not unique	
D. always unique if graph has even number of vertices Answer: C	
ATISWEL. C	-
169. threads being block altogether and being executed in the sets of 32 threads	
called a	
A. thread block	
B. 32 thread	
C. 32 block	
D. unit block Answer: A	
170. True or False: The threads in a thread block are distributed across SM units	
so that each thread is executed by one SM unit.	
A. true	
B. false	
Answer: A	_

171. When the topological sort of a graph is unique?

- A. when there exists a hamiltonian path in the graph
- B. in the presence of multiple nodes with indegree 0
- C. in the presence of single node with indegree 0
- D. in the presence of single node with outdegree 0

Answer: A

172. What is a high performance multi-core processor that can be used to accelerate a wide variety of applications using parallel computing.

- A. cpu
- B. dsp
- C. gpu
- D. clu

Answer: C

173. A good mapping does not depends on which following factor

- A. knowledge of task sizes
- B. the size of data associated with tasks
- C. characteristics of inter-task interactions
- D. task overhead

Answer: D

174. CUDA is a parallel computing platform and programming model

- A. true
- B. false

Answer: A

175. Which of the following is not a form of parallelism supported by CUDA

- A. vector parallelism floating point computations are executed in parallel on wide vector units
- B. thread level task parallelism different threads execute a different tasks
- C. block and grid level parallelism different blocks or grids execute different tasks
- D. data parallelism different threads and blocks process different parts of data in memory

Answer: A