

DBandwidth efficiency

Bi =
$$\frac{1}{24000}$$
 Bandwidth = $B = \frac{24000}{3}$ Bandwidth = $B = \frac{24000}{3}$ = $\frac{24000}{8000}$ = $\frac{24000}{8000}$ = $\frac{3}{2}$ bits per second per cycle of bandwidth.

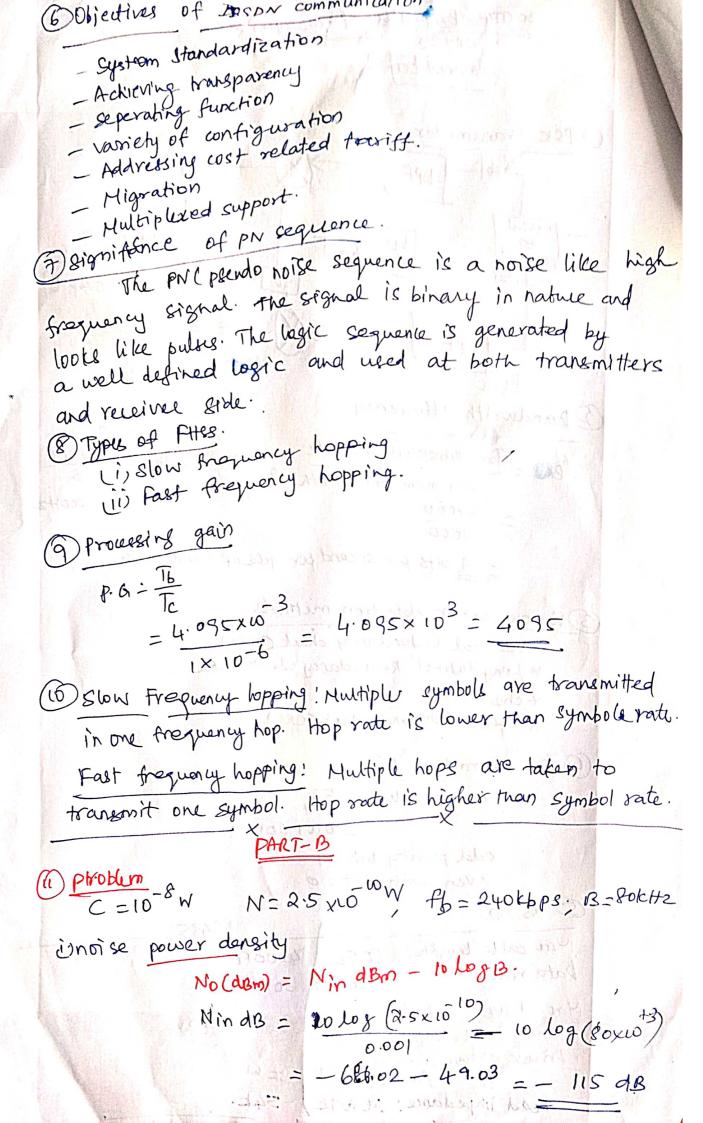
- 3 various error detection methods
 - * Vertical Redundancy check (VRC)
 - * Longitudinal Redundancy check (LRC
 - * check sum
 - * Cyclic Redublancy clerk (CRC).
 - @ For character S'

ASCII Value = 53

= 0101 0011

odd parity bit = 1 even parity bit = 0.

Priver output? \$25V + 60 Hod impedance: 3k to 7k 54k.



Start guard pattern

Stop guard pattern. - centre guard pattern - Longists of 2 long bars the first left hand digit - identified how the Upc symbol is used.

