

Summary

Design Name	ALU
Fitting Status	Design Rule Checking Failed
Software Version	J.36
Device Used	XA95144XL-15-CS144
Date	3-14-2021, 1:17PM

RESOURCES SUMMARY

Macrocells Used	Pterms Used	Registers Used	Pins Used	Function Block Inputs Used
0/144 (0%)	0/720 (0%)	0/144 (0%)	0/0 (0%)	0/432 (0%)

PIN RESOURCES

Signal Type	Required	Mapped	Pin Type	Used	Total
Input	132	0	I/O	0	0
Output	66	0	GCK/IO	0	0
Bidirectional	0	0	GTS/IO	0	0
GCK	0	0	GSR/IO	0	0
GTS	0	0			
GSR	0	0			

GLOBAL RESOURCES

Global clock net(s) used	0
Global output enable net(s) used	0
Global set/reset net(s) used	0

POWER DATA

Macrocells in high performance mode (MCHP)	0
Macrocells in low power mode (MCLP)	0
Total macrocells used (MC)	0

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