Summary

Design Name	CU
Fitting Status	Successful
Software Version	J.36
Device Used	XA9536XL-15-VQ44
Date	3-14-2021, 1:40PM

RESOURCES SUMMARY

Macrocells Used	Pterms Used	Registers Used	Pins Used	Function Block Inputs Used
4/36 (12%)	5/180 (3%)	0/36 (0%)	10/34 (30%)	7/108 (7%)

PIN RESOURCES

Signal Type	Required	Mapped	Pin Type	Used	Total
Input	6	6	I/O	10	28
Output	4	4	GCK/IO	0	3
Bidirectional	0	0	GTS/IO	0	2
GCK	0	0	GSR/IO	0	1
GTS	0	0			
GSR	0	0			

GLOBAL RESOURCES

Global clock net(s) used	0
Global output enable net(s) used	0
Global set/rest net(s) used	0

POWER DATA

Macrocells in high performance mode (MCHP)	0
Macrocells in low power mode (MCLP)	4
Total macrocells used (MC)	4

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