Design:	D:\4th Semester\CS203 lab\Mini-Project\mini-project\tic_tac_toe.ncd
Preferences:	tic_tac_toe.pcf
Part:	5vlx110tff1136-1
Data version:	PREVIEW,v1.0,08-30-06

Power summary:	I(mA)	P(mW)
Total estimated power consumption:		818
Vccint 1.00V:	489	489
Vccaux 2.50V:	130	325
Vcco25 2.50V:	2	4
Inputs:	0	0
Logic:	0	0
Outputs:		
Vcco25	0	0
Signals:	0	0
Quiescent Vccint 1.00V:	489	489
Quiescent Vccaux 2.50V:	130	325
Quiescent Vcco25 2.50V:	2	4

Thermal summary:	
Estimated junction temperature:	33C
Ambient temp:	25C
Case temp:	33C
Theta J-A range:	10 - 10C/W

Analysis completed: Mon Mar 22 20:11:01 2021