```
Release 9.2i - Bitgen J.36
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Loading device for application Rf_Device from file '5vlx110t.nph' in environment
D:\4th Semester\CS250.
    "tic_tac_toe" is an NCD, version 3.1, device xc5vlx110t, package ff1136,
speed -1
Opened constraints file tic_tac_toe.pcf.

Mon Mar 22 20:06:35 2021

D:\4th Semester\CS250\bin\nt\bitgen.exe -intstyle ise -w -g DebugBitstream:No -g Binary:no -g CRC:Enable -g
ConfigRate:2 -g CclkPin:PullUp -g M0Pin:PullUp -g M1Pin:PullUp -g M2Pin:PullUp -g ProgPin:PullUp -g
DonePin:PullUp -g InitPin:Pullup -g CsPin:Pullup -g DinPin:Pullup -g BusyPin:Pullup -g RdWrPin:Pullup -g
TckPin:PullUp -g TdiPin:PullUp -g TdoPin:PullUp -g TmsPin:PullUp -g UnusedPin:PullDown -g UserID:0xFFFFFFFF -g
RetainConfigStatus:Yes -g ConfigFallback:Enable -g SelectMAPAbort:Enable -g BPI_page_size:1 -g
OverTempPowerDown:Disable -g DCIUpdateMode:AsRequired -g StartUpClk:CClk -g DONE_cycle:4 -g GTS_cycle:5 -g
GWE_cycle:6 -g LCK_cycle:NoWait -g Match_cycle:Auto -g Security:None -g DonePipe:No -g DriveDone:No -g
Encrypt:No tic_tac_toe.ncd
```

Summary of Bitgen Options:

Option Name	Current Setting
Compress	(Not Specified)*
Readback	(Not Specified)*
CRC	Enable**
DebugBitstream	No**
ConfigRate	2**
StartupClk	Cclk**
CclkPin	Pullup**
DonePin	Pullup**
HswapenPin	Pullup*
M0Pin	Pullup**
M1Pin	Pullup**
M2Pin	Pullup**
ProgPin	Pullup**
InitPin	Pullup**
CsPin	Pullup**
DinPin	Pullup**
BusyPin	Pullup**
RdWrPin	Pullup**
TckPin	Pullup**
TdiPin	Pullup**
TdoPin	Pullup**

TmsPin	Pullup**
UnusedPin	Pulldown**
GWE_cycle	6**
GTS_cycle	5**
OverTempPowerDown	Disable**
LCK_cycle	NoWait**
Match_cycle	Auto**
DONE_cycle	4**
Persist	No*
DriveDone	No**
DonePipe	No**
Security	None**
UserID	0xFFFFFFFF**
ActivateGclk	No*
ActiveReconfig	No*
Encrypt	No**
Key0	pick*
StartCBC	pick*
KeyFile	(Not Specified)*
DCIUpdateMode	AsRequired**
ConfigFallback	Enable**
SelectMAPAbort	Enable**
RetainConfigStatus	Yes**
BPI_page_size	1**
BPI_1st_read_cycle	1*
IEEE1532	No*
Binary	No**

```
  *  Default setting.
 ** The specified setting matches the default setting.

Running DRC.
DRC detected 0 errors and 0 warnings.
Creating bit map...
Saving bit stream in "tic_tac_toe.bit".
Bitstream generation is complete.
```