Praneeth Narayanamurthy

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Education

B.Tech., Electrical and Electronics Engineering, National Institute of Technology Karnataka, 2014.
Thesis: *Efficient-Estimation of Lightning Parameters using Genetic Algorithms.*

Ph.D., Electrical Engineering, Iowa State University, 2016 – 2020 (expected).

Research Interests

Signal Processing, Machine Learning, Matrix Factorization, Big Data, Algorithms

Work Experience

Research Assistant: Jan. 2016 – Present, *Iowa State University, Ames.*
I design and analyze provable, online algorithms for matrix factorization problems. Specifically, I have worked on Robust Principal Component Analsyis (RPCA), Matrix Completion (MC) and Robust Matrix Completion (RMC) algorithms. In our work we show that by exploiting mild statistical properties of time-series data, we are able to (i) obtain increased "robustenss" for RPCA, (ii) complete matrices whose set of missing entries are not probabilistic in nature for MC, and (iii) provide the first complete online, provable algorithm for RMC. I am currently working on structured Phase Retrieval, Computer Vision applications such as object tracking and detection in videos, and Neural Networks.

Project Assistant: July 2014 – Dec. 2015, *Indian Institute of Science, Bangalore.*
I was part of the Indian Government project of developing Text-to-Speech systems for 11 regional Indian Languages. Specifically, I worked on (i) developing post-processing algorithms to enhance the naturalness of synthesized speech; and (ii) studying resampling techniques to reduce time and space complexity for low-footprint devices.

Publications and Pre-Prints

Pre-Prints

1. [Phaseless Low Rank Matrix Recovery and Subspace Tracking](#)
   Seyedehsara Nayer, **Praneeth Naryanamurthy** *and Namrata Vaswani,*
   `manuscript` (Feb. 2019).

2. [Subspace Tracking from Missing and Corrupted Data](#),
   Praneeth Narayanamurthy, *Vahid Daneshpajooh and Namrata Vaswani*
   `under review, IEEE Transactions on Signal Processing` (Oct. 2018)

3. Nearly Optimal Robust Subspace Tracking,
   Praneeth Naryanamurthy *and Namrata Vaswani*,
   manuscript (Mar. 2018).

4. Finite Sample Guarantees for PCA in non-isotropic and Data-Dependent Noise,
   Namrata Vaswani and **Praneeth Narayanamurthy**,
   manuscript (May 2017).

Journals and Highly Selective Conference Papers

1. Nearly Optimal Robust Subspace Tracking,
   Praneeth Naryanamurthy *and Namrata Vaswani*,
   International Conference on Machine Learning (ICML),
   Long talk (Top 8.6% of papers) 2018.

2. Provable Dynamic Robust PCA or Robust Subspace Tracking,
   Praneeth Narayanamurthy *and Namrata Vaswani*,
   to appear, IEEE Transactions on Information Theory, 2018.

3. Robust PCA, Subspace Learning, and Tracking,
   Namrata Vaswani, Thierry Bouwmans, Sajid Javed and **Praneeth Narayanamurthy**,
   IEEE Signal Processing Magazine (July 2018).

4. Static and Dynamic Robust PCA and Matrix Completion: A review,
   Namrata Vaswani, and **Praneeth Narayanamurthy**,
   Proceedings of IEEE (Aug. 2018).

Conference and Workshops

1. Provable Subspace Tracking with Missing Entries,
   Praneeth Narayanamurthy, *Vahid Daneshpajooh, and Namrata Vaswani*,
   IEEE International Symposium on Information Theory (ISIT), 2019.

2. Provable Memory-Efficient Online Robust Matrix Completion,
   Praneeth Narayanamurthy, *Vahid Daneshpajooh, and Namrata Vaswani*,
   IEEE International Conference on Acousitcs Speech and Signal Processing (ICASSP), 2019

3. Provable Dynamic Robust PCA or Robust Subspace Tracking,
   Praneeth Narayanamurthy *and Namrata Vaswani*,
   IEEE International Symposium on Information Theory (ISIT), 2018.

4. Nearly Optimal Robust Subspace Tracking: A Unified Approach,
   Praneeth Narayanamurthy *and Namrata Vaswani*,
   IEEE Data Science Workshop (DSW), 2018.

5. PCA in Sparse Data-Dependent Noise,
   Namrata Vaswani and **Praneeth Narayanamurthy**,
   IEEE International Symposium on Information Theory (ISIT), 2018.

6. A Fast and Memory-Efficient Algorithm for Robust PCA (MERoP),
   Praneeth Narayanamurthy *and Namrata Vaswani*,
   IEEE International Conference on Acousitcs Speech and Signal Processing (ICASSP), 2018

7. Robust PCA and Robust Subspace Tracking: A comparative Evaluation,
   Sajid Javed, **Praneeth Narayanamurthy***, Namrata Vaswani and Thierry Bouwmans,*
   `IEEE Statistical Signal Processing Workshop (SSP), 2018.`

8. Finite Sample Guarantees for PCA in non-isotropic and Data-Dependent Noise,
   Namrata Vaswani and **Praneeth Narayanamurthy**,
   `Allerton Conference on Communication, Control, and Computing, 2017`

9. Provably correct Robust Subspace Tracking: A Correlated-PCA-based Approach,
   Brian Lois, Namrata Vaswani and **Praneeth Narayanamurthy**, `NIPS workshop on LHDS, 2016.`

10. Efficient Resampling of speech signals in Shift-Invariant Spaces,
    Gutta Sreedevi, **Praneeth Narayanamurthy***, and Chandra Sekhar Seelamantula,*
    `IEEE National Conference on Communications (NCC) 2016.`

11. Dictionary-Learning based Post-Filter for HMM-based Speech Synthesis,
    Praneeth Narayanamurthy *and Chandra Sekhar Seelamantula,*
    `IEEE Region 10 Conference (TENCON) 2015.`

Professional Service

I review articles for IEEE Transactions on Signal Processing, IEEE Transactions on Networking, IEEE Journal of Selected Topics in Signal Processing, IEEE Signal Processing and Wireless Communications.

Graduate Courses

Electrical Engineering: Probability and Random Processes, Convex Optimization, Detection and Estimation Theory, Principles of Data Science, Deep Machine Learning, Statistical Machine Learning

Computer Science: Design and Analysis of Algorithms, Machine Learning

Mathematics: Linear Algebra, Numerical Analysis-II

Skills

Proficient: MATLAB, LaTeX

Intermediate: Python, C++, Bash, Tensorflow, Git

Beginner: Julia, Scheme, Perl

Honors and Awards

Recepient of ICML travel grant – 2018.

Finalist of National GE Edison Challenge – 2013

Indian National Mathematical Olympiad Awardee – 2009

Certificate of Excellence from Central Board of Secondary Education for securing 100% grade in Mathematics and Sanskrit in 10th standard – 2008

Talks

1. *MEDRoP: Memory Efficient Dynamic Robust PCA*
   Microsoft Research India, Bangalore
   December 2017

2. *MEDRoP: Memory Efficient Dynamic Robust PCA*
   ECE Department, Indian Institute of Science, Bangalore
   December 2017

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