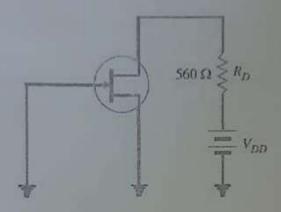
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Q4 (a) What are the types of possible configurations in B.IT. Derive the relation between 0, 8 [1] CO2 8L1

Q5 (a) With a neat sketch, explain the operation of n-channel enhancement type MOSFET. [3] CO2 BL2 Q5 (b) For the JFET in Figure, VG5 (off) = - 4V and IDSS = 12 mA. Determine the minimum value of VDD required to put the device in the constant-current region of operation.



:::::: 18/01/2023 ::::::M

## BIRLA INSTITUTE OF TECHNOLOGY, MESRA, RANCHI (MID SEMESTER EXAMINATION)

CLASS: BTECH

BRANCH: CSE/ECE/EEE/AI & ML

SEMESTER: 1

SESSION: MO/2022

## SUBJECT: EC101 BASICS OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME: 2 HOURS

FULL MARKS: 25

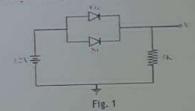
PTO

## INSTRUCTIONS:

1. The question paper contains 5 questions each of 5 marks and total 25 marks.

2. Attempt all questions.

- 3. The missing data, if any, may be assumed suitably.4. Tables/Data handbook/Graph paper etc., if applicable, will be supplied to the candidates
- Q1 (a) Describe the operation of Zener Diode based voltage regulator with a suitable circuit [3] CO1 8L2
- Q1 (b) In the following Fig.1, If Germanium (Ge) diode connection (direction) is reversed. [2] CO1 BL4 the value of output voltage (Vo) changes by how much volts? (Assume that the Ge diode has large breakdown voltage).



- Q2 (a) Draw the circuit of diode based Half Wave Rectifier (HWR) and Derive the expressions [3] CO1 8L1
- for average and RMS voltage for HWR. Q2 (b) A full wave rectifier produces a rms voltage of 10 V from a 50 Hz line source and feeds a resistive load of 1100  $\Omega$ . If the filter uses a capacitor of C = 50  $\mu$ F, calculate the output dc voltage and ripple voltage.
- BL2
- Q3 (a) Explain Base-width Modulation Effect in BJT using suitable diagram. [3] CO2
  Q3 (b) Find the Quiescent-point voltage and current values for voltage divider configuration [2] CO2 shown (Fig2)

