

<b>Topics for MST/Quiz2: December 2020</b>
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| 1. Computer Registers   |
| 2. Common Bus System  |
| 3. Design of Timing and Control Unit  |
| 4. Instruction Format   |
| 5. Instruction Codes  |
| 6. Types of Instructions: Memory Reference, I/O Reference, Register Reference |
| 7. Instruction Cycle: Fetch/Decode/Execute Phase                              |
| 8. Timing Diagrams: Fetch Phase   |
| 9. Interrupt Cycle  |
| 10. ARM Core data flow model  |
| 11. CPSR instruction Format   |
| 12. ARM instruction set: Branching /Logical/Arithmetic/Shift Instructions     |

Logical Questions may be asked from all these topics
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<b>For MST_ Quiz 2: The syllabus is up S. No: 12</b>
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