

Chandan Prasad

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SKILLS

C/C++, C#, Python, Rust, Go
Computational Geometry
Performance Optimization

TypeScript/Javascript, Julia, Java
High Performance Computing
Docker, Kubernetes, GCP

HTML/CSS, ReactJS, Svelte, SQL
Distributed Systems

EXPERIENCE

Symbotic Inc. | *Wilmington, MA*

Jul 2023 - Present

Principal Software Engineer - Algorithms

- Design and develop real-time distributed software systems that schedule and coordinate tasks for fleets of autonomous mobile robots in warehouse environments - most warehouse environments using this system have in excess of 1000 bots deployed.
- Architect highly available, scalable software solutions for end-to-end autonomous supply chain operations serving major retail and wholesale customers.
- Research and productize solutions for NP-Hard optimization problems using combinatorial optimization techniques to improve fleet efficiency.
- Involved in cross-team effort to develop a simulator to model full system behavior to simplify and enable more rigorous testing before deploying changes to production while also providing a simpler setup for developers to use for debugging.
- Implement asynchronous and multithreaded software for real-time robot fleet coordination across Windows and Linux platforms.

6 River Systems / Shopify | *Westford, MA*

Apr 2022 - May 2023

Senior Software Engineer - Performance Optimization & Algorithms

- Improved the performance of the robotic warehouse automation system employed at 6 River Systems
- Used profiling and other tools to find hot-spots in the application with an aim to improve the performance of the system
- Improved the tooling around development, profiling and testing that improved developer productivity
- Overhauled an UI application that provided a deeper visibility into the internals of the core scheduling robot picking algorithms - this was very useful for answering support questions from internal and external customers.
- Evaluated the possibility of moving the scheduling service over to the Rust programming language as a part of a hack-days project. Used a minimal prototype of the core optimization algorithm to validate the gains, the limited prototype improved performance by about 10x and the memory usage reduced by around 3X.
- Evaluated replacing the core data-structure used for storing the in-memory objects with a C++ library wrapped with N-API to improve performance.

Dassault Systemes | *Waltham, MA*

Feb 2017 - Mar 2022

SIMULIA Developer Manager

- Improved the performance and scalability of the DigitalRock discretizer by utilizing input information to enhance initial decomposition, this provided up to a 10% improvement compared to prior releases. The discretizer is a piece of software that takes input geometry and case definitions and performs, booleans, geometry validation and cleanup and prepares the model for simulation.
- Added support to balance the load amongst the various discretization processes in a parallel run. This is required since the initial decomposition does not accurately capture the distribution of computational load in typical production case, more over the load cannot be determined a-priori, a dynamic load balancing scheme based on work stealing was developed to overcome this problem.
- Added the ability to re-decompose the domain in parallel to reduce fragmentation of the grid across various processes. This operation is intended to minimize the boundary of the domain owned by a process, which in turn reduces the volume of network calls and improves overall scalability.

Exa GmbH | *Stuttgart, Germany*

Dec 2015 - Feb 2017

Senior Software Engineer

- Augmented the file IO features in the PowerFLOW Discretizer to support distributing input file data to multiple processors and writing output files from data received from multiple processors.
- Created wrappers and additional utility methods around the MPI library to simplify the development of the parallel discretizer while providing greater visibility into the system to assist with debugging and profiling.
- Added shared memory support for the discretizer using the POSIX spec to amortize the cost of storing input information across all processors. This reduced the memory load on a node and improved scalability of the PowerFLOW Discretizer.
- Added support to read images, convert them to 3D geometry using the marching cubes algorithm and use the geometry in downstream discretizations and simulations. This is used as a part of Exa DigitalROCK offering, these efforts along with the parallelization reduced turnaround time for large simulations from days and weeks to hours.

Exa Corporation | *Burlington MA, US*

Jul 2014 - Dec 2015

Senior Software Engineer

- Improved the handling of 'toroidal' geometry (eg. Treating opposing faces of a cube as abutting) in the discretizer.
- Improved integration testing by creating a new test suite and streamlined the process to create new test cases.

- Added initial support for parallel discretizations in the next generation discretizer.

Exa Corporation | Burlington MA, US

Jul 2013 - Jul 2014

Software Engineer

- Improved the robustness of the discretizer used in PowerFLOW for complex geometries by integrating an updated geometry kernel
- Developed and maintained high performance support libraries used in the discretizer and in other applications at Exa Corp.
- Improved memory efficiency of the discretizer by migrating some low level components to using paged allocation.

Georgia Institute of Technology – Aerothermodynamics Research and Technology Lab (ARTLab) | Atlanta GA, US

Jan 2011 - May 2013

Graduate Research Assistant

- Extended the Normal Ray Refinement (NRR) methodology to 3D Bodies in NASCART-GT (a cartesian grid based Navier-Stokes solver for hypersonic flows), reducing computational cost of viscous simulations by 30-60% with minimal loss in accuracy
- Streamlined the programming procedures in the lab by introducing IDE's integrated with source versioning systems, documentation systems, profiling and performance analysis tools, reducing the time for feature additions, bug fixes and increasing collaboration.
- Developing a solver based on the cartesian grid methodology using C++ (ported features from the Fortran based codebase) - this effort led to a 15x performance increase during grid generation

SBM Jain College of Engineering | Bangalore, India

Sep 2009 - Jul 2010

Research Associate

- Conducted an experimental study of the machinability characteristics of Aluminum Silicon to improve the quality of automotive parts.
- Managed staffing of the research team, budgets and Inventory. Also managed communications with other research institutes to reduce duplication of work and to improve overall value for the research funds.

EDUCATION

Georgia Institute of Technology | Atlanta, GA

Jan 2011 - May 2013

Master of Science in Aerospace Engineering

Visvesvaraya Technological University | Bangalore, India

Jun 2006 - Jul 2010

Bachelor of Engineering, Mechanical Engineering

OTHER PROJECTS

AHS Student Design Competition - Fuselage Aerodynamics Lead | Atlanta GA, US

Jan 2012 - Aug 2012

Georgia Institute of Technology, Atlanta - Graduate Team

- Analyzed flows over a variety of fuselage geometries and optimized the shape of the fuselage to exceed design requirements
- Conducted design studies to determine the effect of various types of landing gear on the performance of the aircraft

SAE Aerodesign Competition 2010 - WEST - Team Captain | Van Nuys CA, US

Jan 2010 - Mar 2010

Team Drone - Aeromodeling Group at SBM Jain College of Engineering

- Developed MATLAB code to conduct multi-variable optimization of design parameters for Micro Air Vehicle (MAV) configuration
- Designed the MAV using SolidWorks and analyzed the design in CFD in order to reduce manufacturing and performance uncertainties
- Presented technical report at the finals, securing first place in presentation & third overall among 45 universities

National Research Development Completion, Innovate India 2010 | Bangalore, India

Nov 2010

Team Drone - Aeromodeling Group at SBM Jain College of Engineering

- Presented an easy to deploy Unmanned Aerial Vehicle (UAV) designed for agricultural and private property surveillance purposes, secured first place at the national level

Bishop Cotton Boys School | Bangalore, India

Jun 2003 - Feb 2005

Model Rocket Project - EBENDER

- Designed and fabricated model rocket using 'off-the-shelf' parts to test the performance of an indigenously designed rocket motor that was developed by us.
- Worked in collaboration with Indian Space Research Organization (ISRO) for final testing and launch logistics
- Received nation wide recognition and also mentioned in the LIMCA book of records (2005) for this project

AWARDS AND ACHIEVEMENTS

- Awarded the Maiden Cup for the best character influence and leadership qualities amongst the graduating class - 2005
- Given the Golden crest, the highest honor given to any Student/Alumnus of Bishop Cotton Boys School for achievements beyond the regular curriculum, Bangalore - 2005

PUBLICATIONS

Robert A, Kobolov V I, Ruffin S M, Manmohan Chandan P, Zaki M, 'Implementation and Evaluation of Normal Ray Refinement Technique in Adaptive Cartesian Framework', 42nd AIAA Fluid Dynamics Conference and Exhibit, 2012