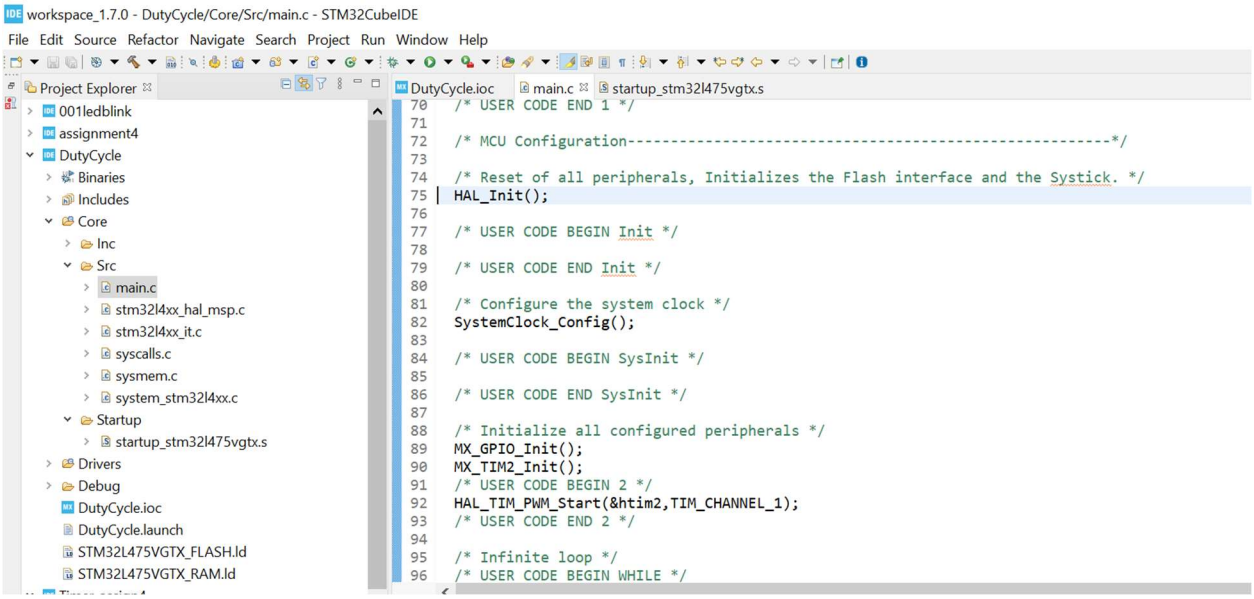
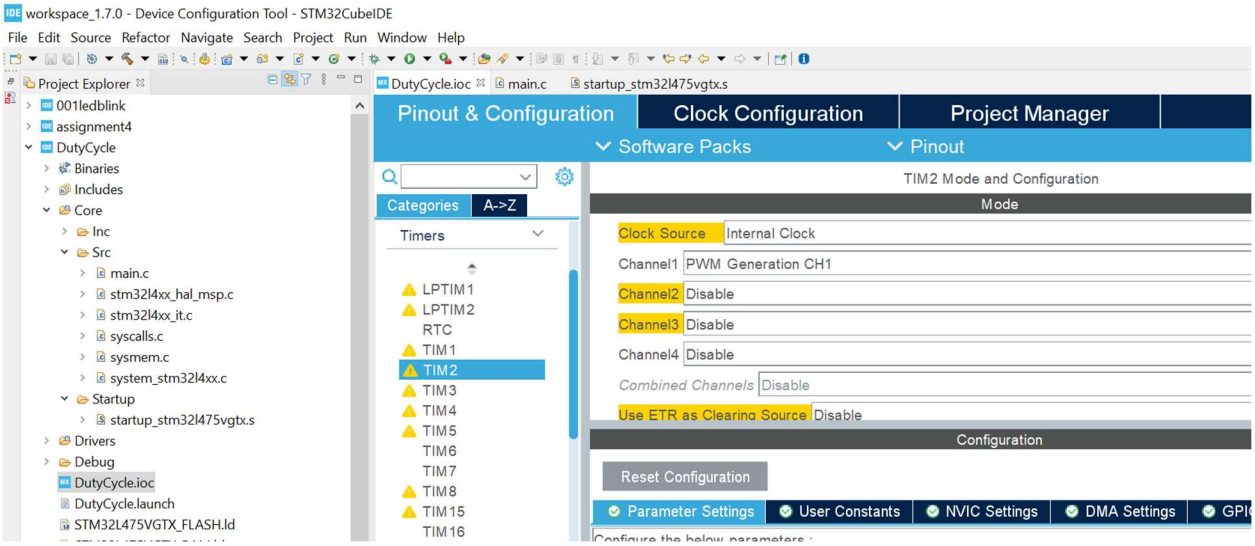
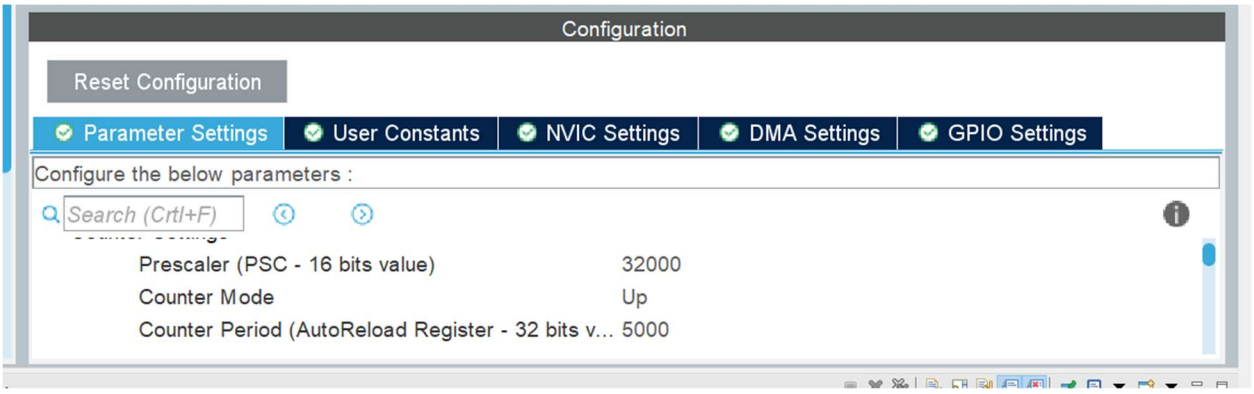
MCU Assignment-3

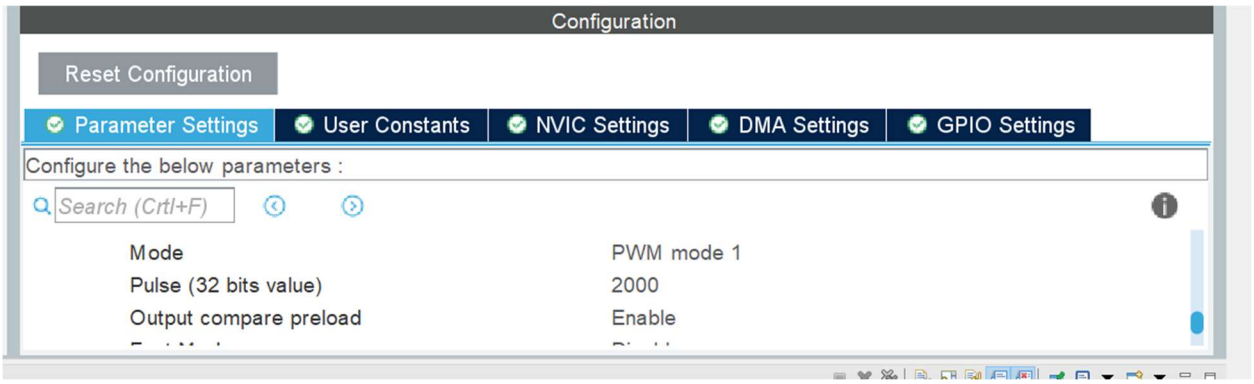
Generate a duty cycle of 40% on Timer 1 (TIM1) and verify it with logic analyzer

Code:

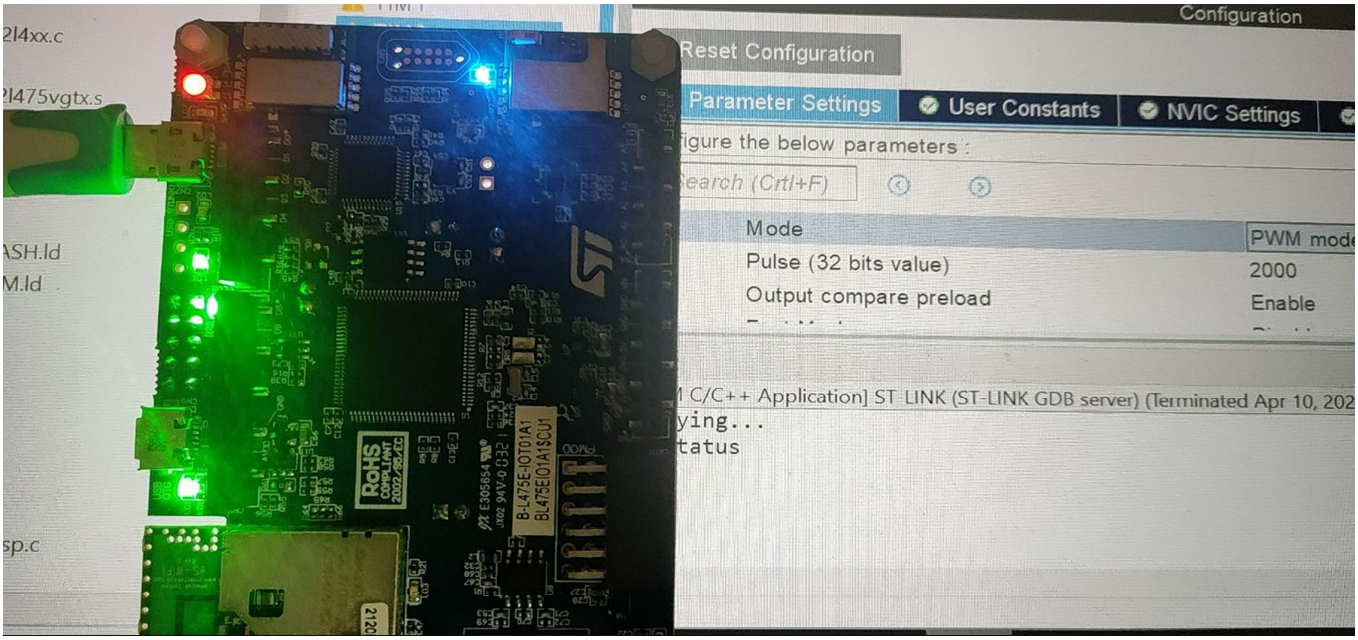





Configuration
Reset Configuration
Parameter Settings
User Constants
NVIC Settings
DMA Settings
GPIO Settings
Configure the below parameters :
Search (Crtl+F)
Prescaler (PSC - 16 bits value) 32000
Counter Mode Up
Counter Period (AutoReload Register - 32 bits v... 5000


Configuration
Reset Configuration
Parameter Settings
User Constants
NVIC Settings
DMA Settings
GPIO Settings
Configure the below parameters :
Search (Crtl+F)
Mode PWM mode 1
Pulse (32 bits value) 2000
Output compare preload Enable

OUTPUT :


Configuration
Reset Configuration
Parameter Settings
User Constants
NVIC Settings
Mode
Pulse (32 bits value) 2000
Output compare preload Enable

Video Link:

https://drive.google.com/file/d/1mHA9DJJD3bQOHlGV08M4Vvw4GV6oYAh6/view?usp=sharing