



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET

B.Tech. ELECTRONICS & COMMUNICATION ENGINEERING

CMM No. : **C0871847**

Serial No. : 21277000089

Name : **KANDIMALLA LAXMI PRASANNA**



Name of the College : 25-GNIT, HYDERABAD

Month & Year of Final Exam : April, 2018

Hall Ticket No. : 18471847

Year of Admission : 2014-2015

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
I YEAR											
1	ENGLISH**	24	50	74	4	2	MATHEMATICS - I**	20	44	64	6
3	MATHEMATICAL METHODS**	22	50	72	6	4	ENGINEERING PHYSICS**	22	40	62	6
5	ENGINEERING CHEMISTRY**	22	66	88	6	6	COMPUTER PROGRAMMING**	20	60	80	6
7	ENGINEERING DRAWING**	23	60	83	6	8	COMPUTER PROGRAMMING LAB.**	23	45	68	4
9	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	22	44	66	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB.**	19	37	56	4
11	ENGINEERING WORKSHOP / IT WORKSHOP	23	49	72	4						
I SEMESTER											
II YEAR											
1	MATHEMATICS - III	19	62	81	4	1	PRINCIPLES OF ELECTRICAL ENGINEERING	22	26	48	4
2	PROBABILITY THEORY AND STOCHASTIC PROCESSES	21	54	75	4	2	ELECTRONIC CIRCUIT ANALYSIS	17	38	55	4
3	SWITCHING THEORY AND LOGIC DESIGN	17	61	78	4	3	PULSE AND DIGITAL CIRCUITS	17	30	47	4
4	ELECTRICAL CIRCUITS	22	44	66	4	4	ENVIRONMENTAL STUDIES	17	52	69	4
5	ELECTRONIC DEVICES & CIRCUITS	15	43	58	4	5	ELECTROMAGNETIC THEORY AND TRANSMISSION LIN	18	29	47	4
6	SIGNALS AND SYSTEMS	23	63	86	4	6	DIGITAL DESIGN USING VERILOG HDL	17	43	60	4
7	ELECTRONIC DEVICES AND CIRCUITS LAB	24	45	69	2	7	ELECTRICAL TECHNOLOGY LAB	20	39	59	2
8	BASIC SIMULATION LAB	23	50	73	2	8	ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB	23	41	64	2
						9	GENDER SENSITIZATION* A	22	47	69	2
I SEMESTER											
III YEAR											
1	CONTROL SYSTEMS ENGINEERING	19	54	73	4	1	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS*	14	26	40	4
2	COMPUTER ORGANIZATION AND OPERATING SYSTEMS	18	30	48	4	2	DISASTER MANAGEMENT	21	39	60	4
3	ANTENNAS AND WAVE PROPAGATION	17	46	63	4	3	DIGITAL COMMUNICATIONS	18	32	50	4
4	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	19	50	69	4	4	VLSI DESIGN*	18	26	44	4
5	ANALOG COMMUNICATIONS	18	35	53	4	5	MICROPROCESSORS AND MICROCONTROLLERS	16	30	46	4
6	LINEAR AND DIGITAL IC APPLICATIONS	17	44	61	4	6	DIGITAL SIGNAL PROCESSING	14	43	57	4
7	ANALOG COMMUNICATIONS LAB	19	40	59	2	7	MICROPROCESSORS AND MICROCONTROLLERS LAB	21	25	46	2
8	IC APPLICATIONS AND HDL SIMULATION LAB	23	45	68	2	8	DIGITAL SIGNAL PROCESSING LAB	22	42	64	2
I SEMESTER											
IV YEAR											
1	MANAGEMENT SCIENCE	15	36	51	4	1	SATELLITE COMMUNICATIONS	20	26	46	4
2	MICROWAVE ENGINEERING	18	33	51	4	2	RADAR SYSTEMS	16	31	47	4
3	COMPUTER NETWORKS	17	27	44	4	3	WIRELESS COMMUNICATIONS AND NETWORKS	16	34	50	4
4	CELLULAR AND MOBILE COMMUNICATIONS	16	29	45	4	4	INDUSTRY ORIENTED MINI PROJECT	-	43	43	2
5	OBJECT ORIENTED PROGRAMMING THROUGH JAVA	16	39	55	4	5	SEMINAR	44	-	44	2
6	OPTICAL COMMUNICATIONS	19	26	45	4	6	MAJOR PROJECT	49	145	194	10
7	ADVANCED COMMUNICATION SKILLS LAB	21	47	68	2	7	COMPREHENSIVE VIVA	-	73	73	2
8	MICROWAVE ENGINEERING AND DIGITAL COMMUNICATI	22	48	70	2						

** Subjects completed from G. Narayanamma Institute of Technology and Science (Autonomous), Hyderabad

Number of Credits registered for : 226 Aggregate Marks Secured for best: 218

Aggregate Marks Secured : 3563 OUT OF 5250 (67.87%)

Date of Issue : July 3, 2018

(see overleaf for Rules concerned to award of class)
A indicates 'ABSENT'

(* Credits considered for award of degree)

(* Courses registered but not counted for calculation of aggregate)



S. Tan kalyan

CONTROLLER OF EXAMINATIONS