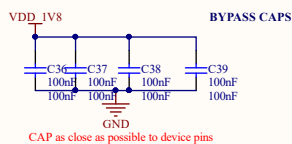
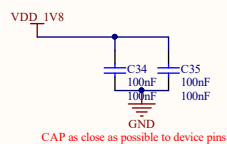


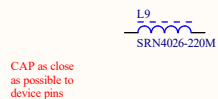
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RADIO		
Size	Number	Revision
A3		
Date:	1/31/2020	Sheet6 of
File:	C:\Users\... \HIGHSPED v0 RADIO.Sch	Down By:



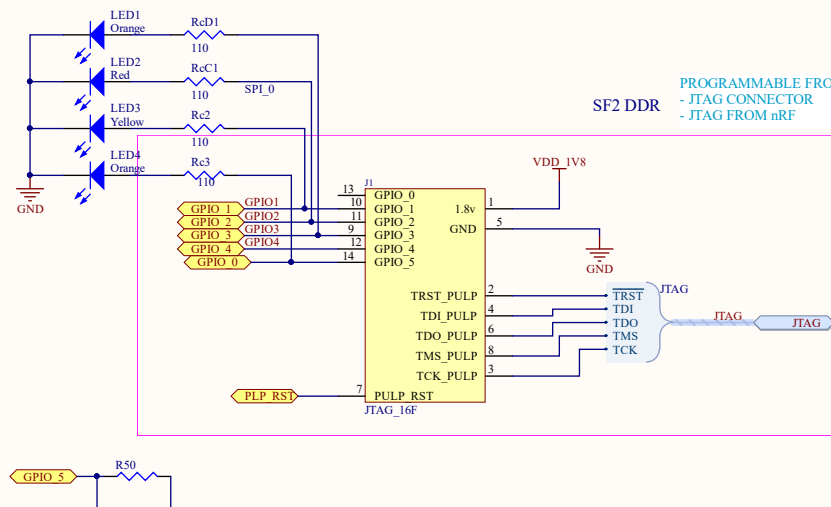
Mr. Wolf PULP and Interfaces

CAP as close as possible to device pins

SPI WOLF MASTER
 csn0 SHARED nRF & FPGA
 csn1 nRF ONLY

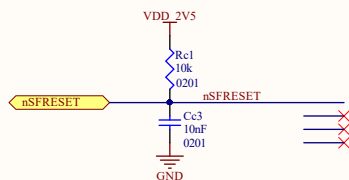


JTAG CONNECTOR



PROGRAMMABLE FROM:
 - JTAG CONNECTOR
 - JTAG FROM nRF

Title		
Size	Number	Revision
A3		
Date:	1/31/2020	Sheet of
File:	C:\Users\...HIGH SPEED v0 SF2 DDR Schematic By:	



Title SF2 IO & JTAG			
Size A3	Number		Revision
Date: 1/31/2020	File: C:\Users\ADMINI~1\Documents\HIGHSPED v0 SF2-COMM\Sheet1 of		Sheet 1 of

