
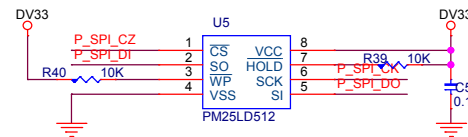
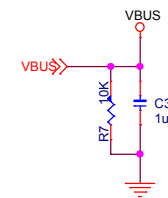
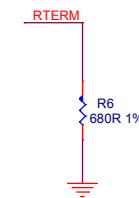
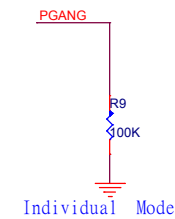
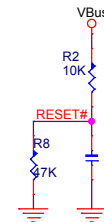
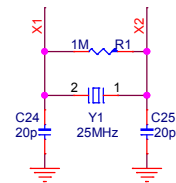
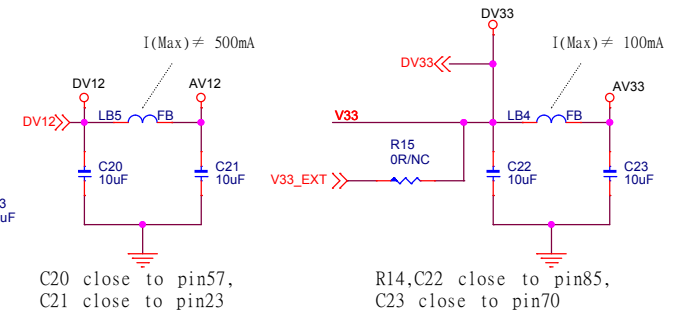
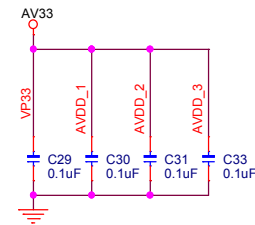
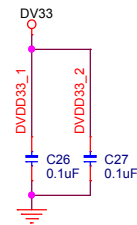
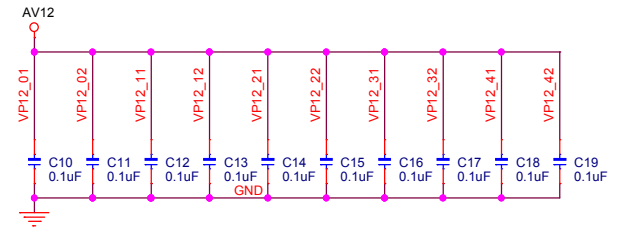
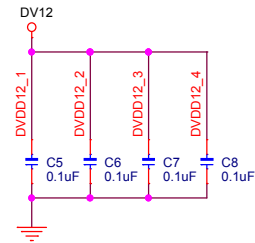
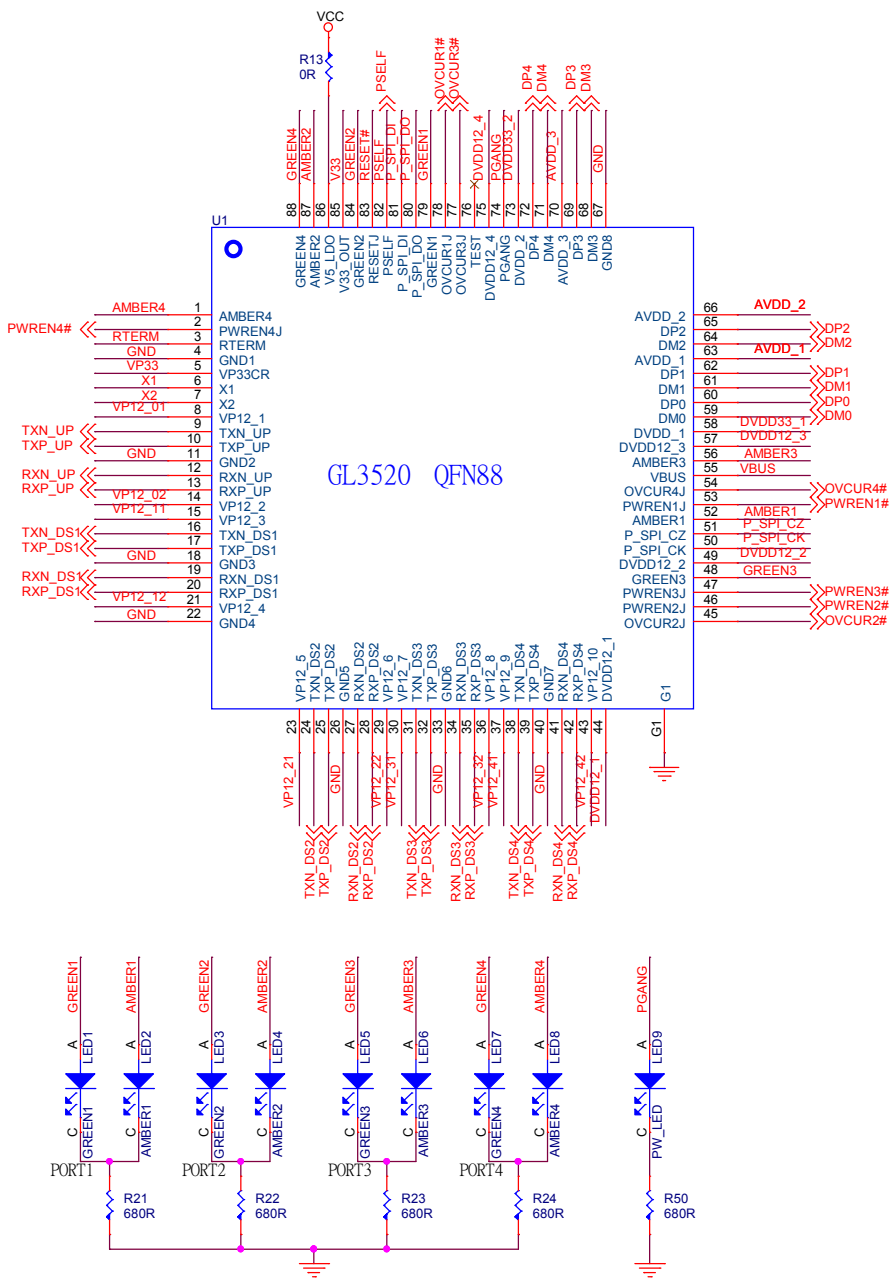



History

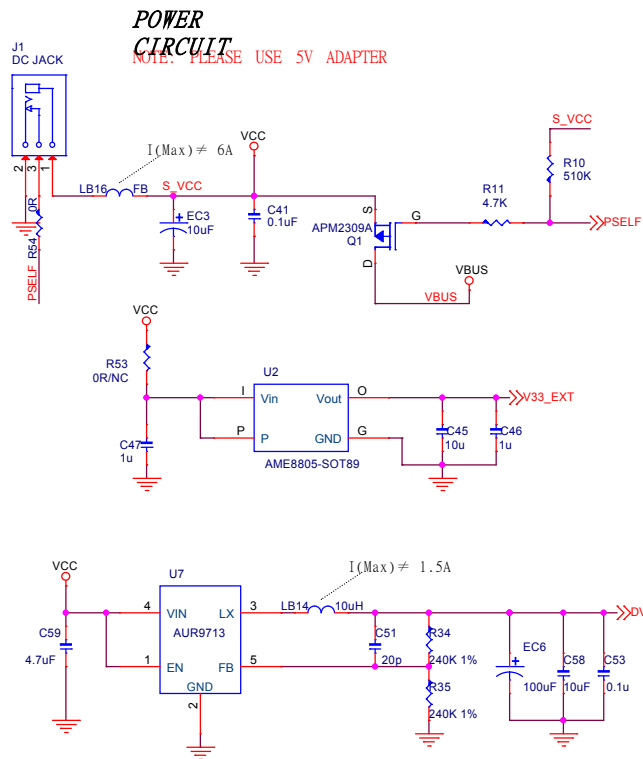
Rev.	Date	Comments	Made by	Approved by
1.00	2011/03/11	Preliminary release.	Chris Lin	Eric Hsu

		GENESYS LOGIC, INC.	
		GL3250-10 QFN88 Demo Board	
Title			
GL3520 USB3.0 4 Port Hub			
Size A	Document Number		Rev 1.00
History			
Date: Monday, March 14, 2011		Sheet 1 of 3	

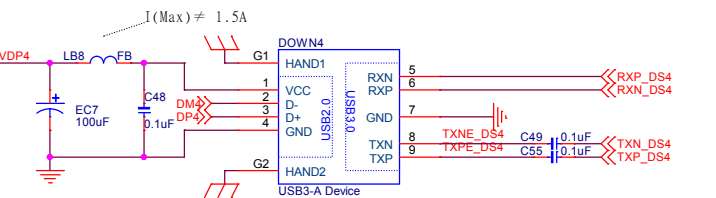
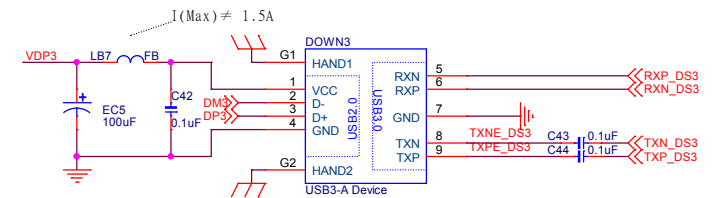
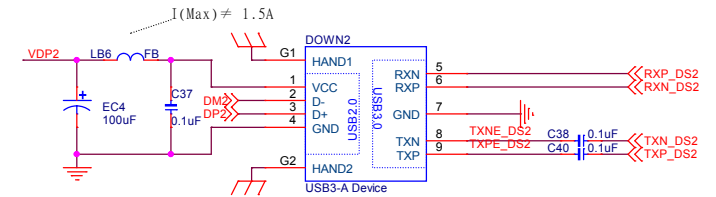
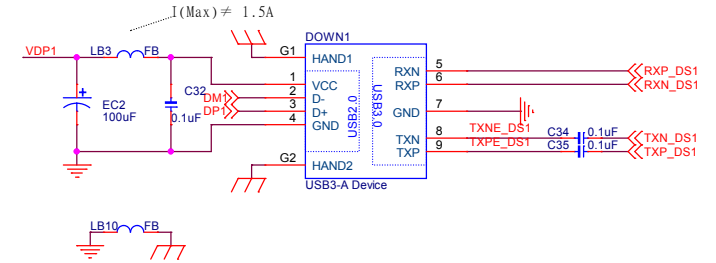
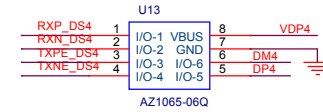
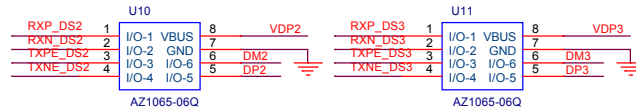
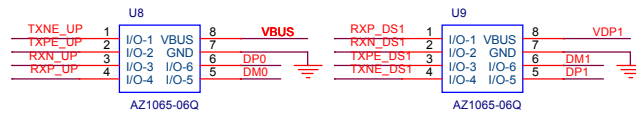
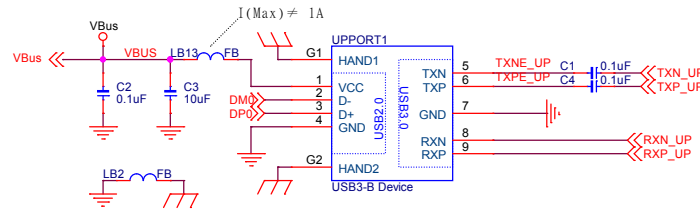
R13 :Mount , R15/R53 remove -> Internal regulator 5V to 3.3V LDO
R15,R53 :Mount R13 remove-> External regulator 5V to 3.3V LDO



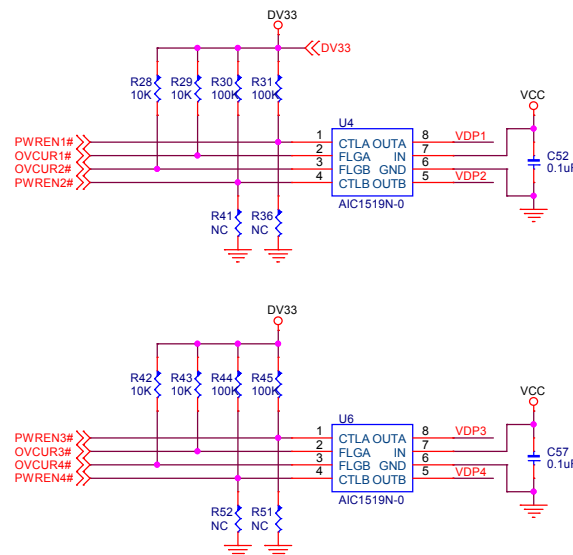
		GENESYS LOGIC, INC.	
GL3250-10 QFN88 Demo Board			
Title		GL3520 USB3.0 4 Port Hub	
Size B	Document Number	GL3520-QFN88	Rev 1.00
Date: Monday, March 14, 2011		Sheet 2 of 3	



Differential trace, space, trace is 8, 7, 8 (mil).
Ground gap > 20mil. Impedance is 90ohm.
Differential pair mismatch < 5mil.



Option: Power Switch CIRCUIT



CTL pin put 100k pull down resisiter for charger(CDP) function.

Option: POLY-FUSE CIRCUIT

