

PRASHANTH DHARAMSOTH

Artificial Intelligence/Machine Learning & Software Developer Enthusiast

✉ dharamsothprashanth@gmail.com in Prashanth Dharamsoth 🌐 Prashanth 📁 Portfolio

WORK EXPERIENCE

Frontend Developer

Nirmaan IITM - TeamTasty (Startup)

📅 March 2020 - August 2020 📍 India, Tamil Nadu, Chennai

- Developed using **HTML, CSS, Javascript on Angular CLI** platform to enhance functionality and user experience on web pages.
- Worked with senior developer to code from scratch for users.
- Files can be found on [Github](#)

Computer Vision Engineer Intern (Guide - Ms S.Irene)

CDAC (Centre for Development of Advanced Computing)

📅 March 2021 - Present 📍 India, Tamil Nadu, Chennai

- Working on **Attribute Recognition** under Collaborative Live Video Analytics for Large Camera Deployments using **5G domain**.
- Developing **Real-time tracking of vehicles** over larger areas using **computer vision, python and deep learning technology**.
- Working with Joint Director and Project Engineer.
- Developed user interface template using **PyQt5**.

Networking Software Engineer Intern (Guide - Manikantan)

NEC Corporation

📅 June 2021 - Present 📍 India, Karnataka, Bangalore

- Working on **5G core network**.
- working with open source technologies like **ONAP, ODL, FRR and DANOS**.

PROJECTS

Pattern Recognition and Machine Learning

Academic Project under Prof. Manikandan Narayanan

📅 January 2021 - May 2021 📍 IIT Madras, Chennai, Tamil Nadu

- Developed **recommendation models** for a Music Platform to predict what ratings customers will assign to songs, using **Collaborative Filtering Method** in **Python**. Code can be found on [Github](#)

Computer System Design

Academic Project under Prof. V Kamakoti

📅 July 2019 - November 2019 📍 IIT Madras, Chennai, Tamil Nadu

- Constructed a modern, full-scale computer system - hardware and software - from the ground up.
- Designed **HACK computer** and its components using **HDL a NAND2TETRIS**.
- Designed an **assembler, virtual machine, a simple compiler** in C++.
- Codes can be found on [Github](#)

Computer Organization and Architecture

Academic Project under Prof. V Kamakoti

📅 January 2020 - May 2020 📍 IIT Madras, Chennai, Tamil Nadu

- Worked to design **Cache Simulator and a Scalar Pipelined Processor (RISC-V)** in Computer Organization and Architecture using **Assembly language**. Codes can be found on [Github](#)

EDUCATION

Bachelor's Degree. in Computer Science and Engineering

Indian Institute of Technology Madras

📅 Expected : 2022 📍 India, Tamil Nadu, Chennai

Senior Secondary School

Velocity Junior College

📅 2016 - 2018 📍 India, Telangana, Hyderabad

ACHIEVEMENTS

🏆 Secured All India Rank of 37 in Joint Entrance Examination Advanced among 0.15million.

☆ Secured All India Rank of 256 in Joint Entrance Examination Main among 1.05 million

COURSE WORK

- Data Structures and Algorithms
- Theory of Computation
- Design Analysis of Algorithm
- Foundations of Computer System Design
- Computer Organization Architecture
- Pattern Recognition and Machine Learning
- Mathematical Foundations of Data Science

TECHNICAL SKILLS

- Python, C, C++, PyQt5, LaTeX

- Programming Experience: 3 years in C/C++, Python

- Platforms: MacOS, Windows, Linux

Others :- HTML/CSS Django Angular

x86 Assembly Language

HDL (Hardware Description Language)

POSITIONS OF RESPONSIBILITIES

- Cadet at NCC (National Cadet Corps)
- Campus Ambassador for Cisco Systems and Verzeo.
- Coordinator for saarang's Facilities and Requirements team, IIT Madras
- Volunteer for saarang's Security team, IIT Madras