

Prateeth Nayak

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“Make the change that you want to see in the world.”

Education

University of Minnesota

MASTERS IN ELECTRICAL AND COMPUTER ENGINEERING

- Prof. Mingyi Hong Research Group; GPA 3.7/4.0

Twin Cities, USA

Aug. 2017 - Dec. 2019

Visvesvaraya Technological University

BACHELORS IN ELECTRONICS AND COMMUNICATION ENGINEERING

- Graduated *Summa Cum Laude*; GPA 4.0/4.0

Belgaum, IND

Aug. 2013 - Jun. 2017

Research Interests

Applied Machine learning, Computer Vision, Model Quantization/Pruning, On-device ML, Deep Learning Optimization

Skills

Programming Python (proficient), C/C++ (basic), MATLAB, SQL, Bash, R, SWIFT, LaTeX

Frameworks Tensorflow (proficient), Pytorch (proficient), Keras, pytorch-lightning, CUDA, git, docker, OPENCV, iOS-Metal

Experience

Stanford Research Institute (S.R.I.) International

COMPUTER SCIENTIST, ARTIFICIAL INTELLIGENCE GROUP

- Main contributions in cross-disciplinary DARPA accelerated molecular discovery (AMD) program [Paper]
- Research and framework development for new paradigm of NN learning through logic rules on prior Knowledge [DASL]
- Collaborative research contributions with SRI vision group on Multi-Modal, Zero-Shot Learning [Paper]
- Research directions - *Logic with NNs; Unsupervised Learning; Efficient encoding spaces for representations*

San Diego, CA

Feb. 2020 - Present

Publications

Conference/Workshops

Transformer based molecule encoding for property prediction [Paper]

Prateeth Nayak, Andrew Silberfarb, Ran Chen, Tulay M., John Byrnes

Machine Learning for Molecules @ NeurIPS 2020

Dec. 2020

Bit Efficient Quantization for Deep Neural Networks [Paper][Poster]

Prateeth Nayak, David Zhang, Sek Chai

Energy Efficient Machine Learning & Cognitive Computing @ NeurIPS 2019

Dec. 2019

Manuscripts/Preprints

Zero-Shot Learning with Knowledge Enhanced Visual Semantic Embeddings [Paper]

Karan Sikka, Jihua Huang, Andrew Silberfarb, Prateeth Nayak, Luke Rohrer, Pritish Sahu, John Byrnes, Ajay

Divakaran, Richard Rohwer

arXiv: 2011.10889; *Under Review*

Nov. 2020

Internships

Stanford Research Institute (S.R.I.) International

DEEP LEARNING OPTIMIZATION RESEARCH INTERN, VISION GROUP

Princeton, NJ

Feb. 2019 - Aug. 2019

- Algorithm and system design for low-bit precision inference engine.
- Built a framework for post-training and quantization-aware training of NNs.
- Participated in the MicroNet Challenge at NeurIPs 2019.
- Involved in successful SRI spin-off [[LatentAI](#)] catering to Model Quantization/Pruning and NN-Throttling using RL.

Boston Scientific Corp.

Arden Hills, MN

MACHINE LEARNING RESEARCH INTERN

May 2018 - Aug. 2018

- Developed end-to-end machine learning pipelines for analyzing time-series data obtained from wearable devices.
- Built pain-score prediction models to correlate the physiological signals of Chronic pain in patients.

Aindra Systems

Bangalore, IND

ENGINEERING INTERN

Feb. 2017 - Jun. 2017

- Worked closely with R&D team on building components of a detection pipeline for cervical cancer cells.
- Evaluating and benchmarking novel image segmentation algorithms for alienating the nuclei in high-res pap smear images.

Bharat Heavy Electronics Ltd. (B.H.E.L)

Bangalore, IND

RESEARCH INTERN

Jun. 2016 - Nov. 2016

- Integrated the TMS320F2812 Dev board onto a SBC Controller for a Utility Solar Inverter.
- Implementation of touchscreen display for ease of monitoring

Presentation/Invited Talks

Carlson School of Management Brown Bag Series

Twin Cities

INVITED SPEAKER

Sept. 2019

- Introduced Computer Vision practices and advanced approaches in image detection, segmentation & tracking.

Selected Projects

Learning using Privileged Information (LUPI) for time series prediction

Implemented a unique learning paradigm SVM+ to time series prediction called 'SVM-plus'. Evaluated on synthetic and real-life datasets.

YOLO-based real-time object detection for damaged roads on mobile platform

Implemented YOLO object detector on iOS platform in SWIFT for road damage detection with model accuracy of 70 mAP @ 15-20 FPS. Weight-only quantized model also run on the iOS platform.

Parallelizing of Image segmentation algorithm - CHAN-VESE for CUDA-GPU

Accelerated segmentation algorithm via implementing cuda-kernels on GForce GTX 1080 using CUDA-C; achieved 200x speed compared to serialized CPU implementation for large images

Relevant Courses

Artificial Intelligence, Computational Aspects of Matrix theory, Computer Vision, Data Modeling with R, Machine learning, Nonlinear Optimization, Predictive Learning from Data, Probability & Stochastic processes, Robot Vision, Algos. & Data Structures, Applied Parallel Programming

Professional Responsibilities

Fall 2018 **Grad. Teaching Assistant**, Univ. of Minnesota for *Linear Control Systems*; Prof. Murti S.

Twin Cities

2016-2017 **Cultural-Committee Head**, B.I.T College BLR Core-Committee

Bangalore

2016-2017 **Program Coordinator**, Electrical & Comm. Student Association ECSA

Bangalore