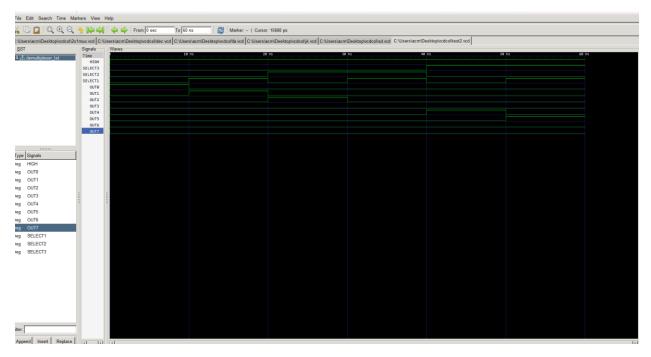
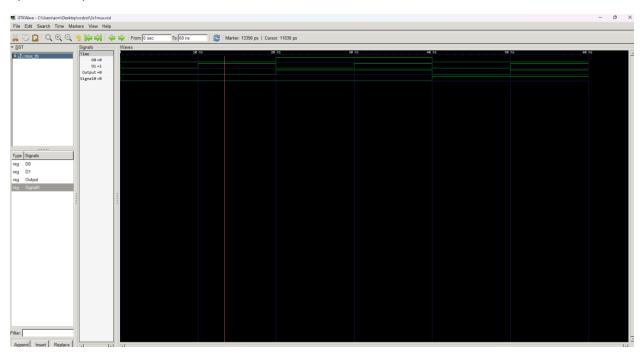
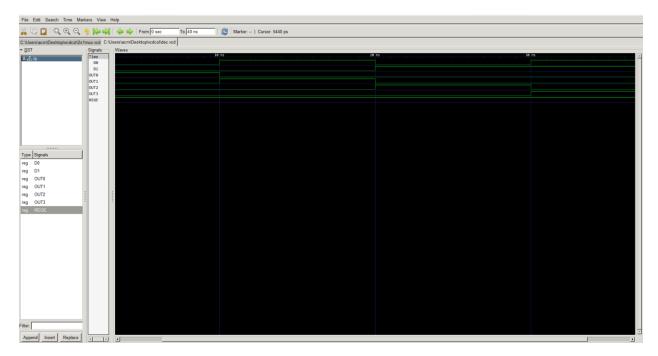
# 1)1x8 multiplexer



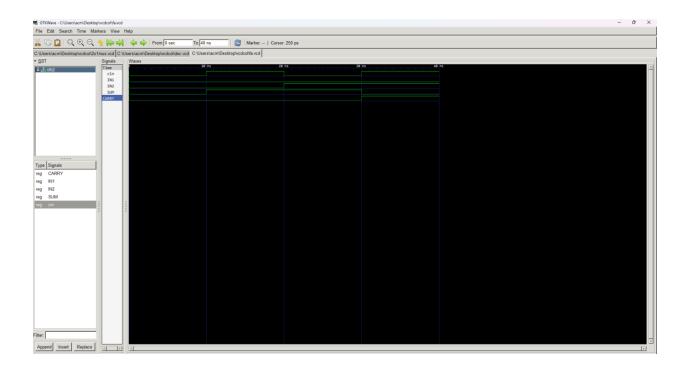
### 2)2X1 multiplexer



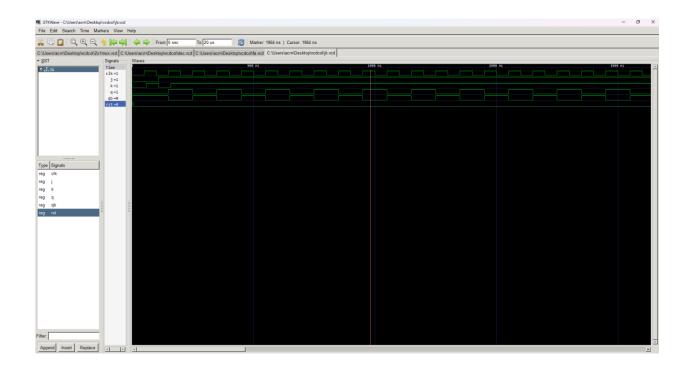
3)Decoder



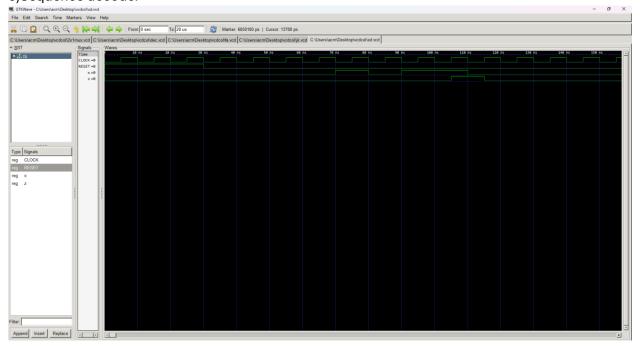
## 4)Full adder



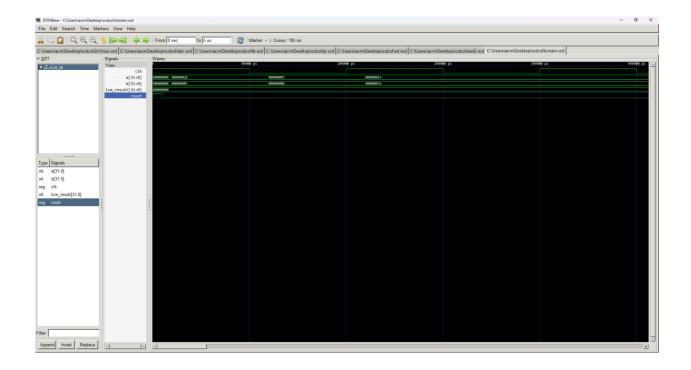
# 5)Jk flip flop



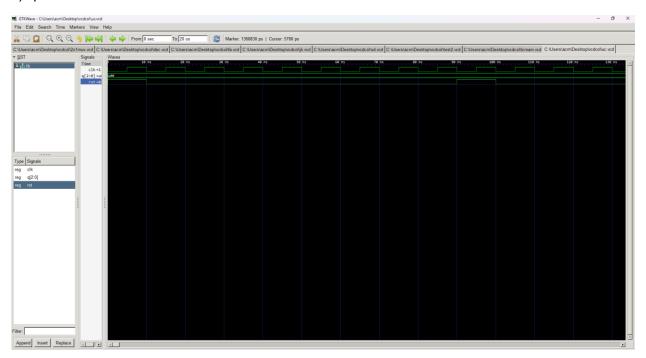
## 6)Sequence decoder



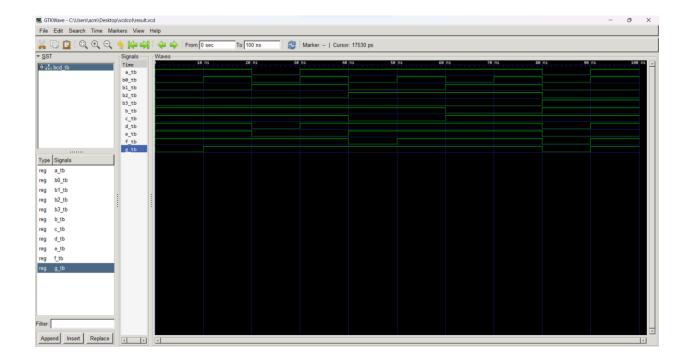
# 7)Lcm



### 8)Upcounter



7\_state counter



# d-flipflop

