

Race Conditions

Due Oct 24, 2019 at 11:59pm

Points 7

Questions 7

Time Limit None

Allowed Attempts Unlimited

This quiz is no longer available as the course has been concluded.

Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	4 minutes	7 out of 7

Score for this attempt: **7** out of 7

Submitted Oct 22, 2019 at 1:36pm

This attempt took 4 minutes.

Question 1

1 / 1 pts

Inst	Thread 0	Thread 1
0	1000 mov 2000(%bx), %ax	
	----- Interrupt -----	
1		1000 mov 2000(%bx), %ax
		----- Interrupt -----
2	1001 add \$1, %ax	
	----- Interrupt -----	
3		1001 add \$1, %ax
4		1002 mov %ax, 2000(%bx)
		----- Interrupt -----
5	1002 mov %ax, 2000(%bx)	

Assume memory address 2000(%bx) initially contains the value 0, %bx, and %ax also contain 0.

What is the value held in %ax AFTER instruction 0 has executed?

Correct!

Correct Answers

0 (with margin: 0)

Question 2**1 / 1 pts**

What is the value held in %ax AFTER instruction 1 has executed?

Correct!**Correct Answers**

0 (with margin: 0)

Question 3**1 / 1 pts**

What is the value held in %ax after instruction 2?

Correct!**Correct Answers**

1 (with margin: 0)

Question 4**1 / 1 pts**

What is the value in %ax after instruction 3?

Correct!**Correct Answers**

1 (with margin: 0)

Question 5**1 / 1 pts**

What is the value in 2000(%bx) after instruction 4?

Correct!**Correct Answers**

1 (with margin: 0)

Question 6**1 / 1 pts**

What is the value in 2000(%bx) after instruction 5?

Correct!**Correct Answers**

1 (with margin: 0)

Question 7**1 / 1 pts**

What would the value have been in 2000(%bx) if there had been no interrupts, or context switches, at inopportune times between the two threads?

Correct!**Correct Answers**

2 (with margin: 0)

Quiz Score: 7 out of 7