



1. Description

1.1. Project

Project Name	Bheem_110
Board Name	custom
Generated with:	STM32CubeMX 6.12.0
Date	07/20/2024

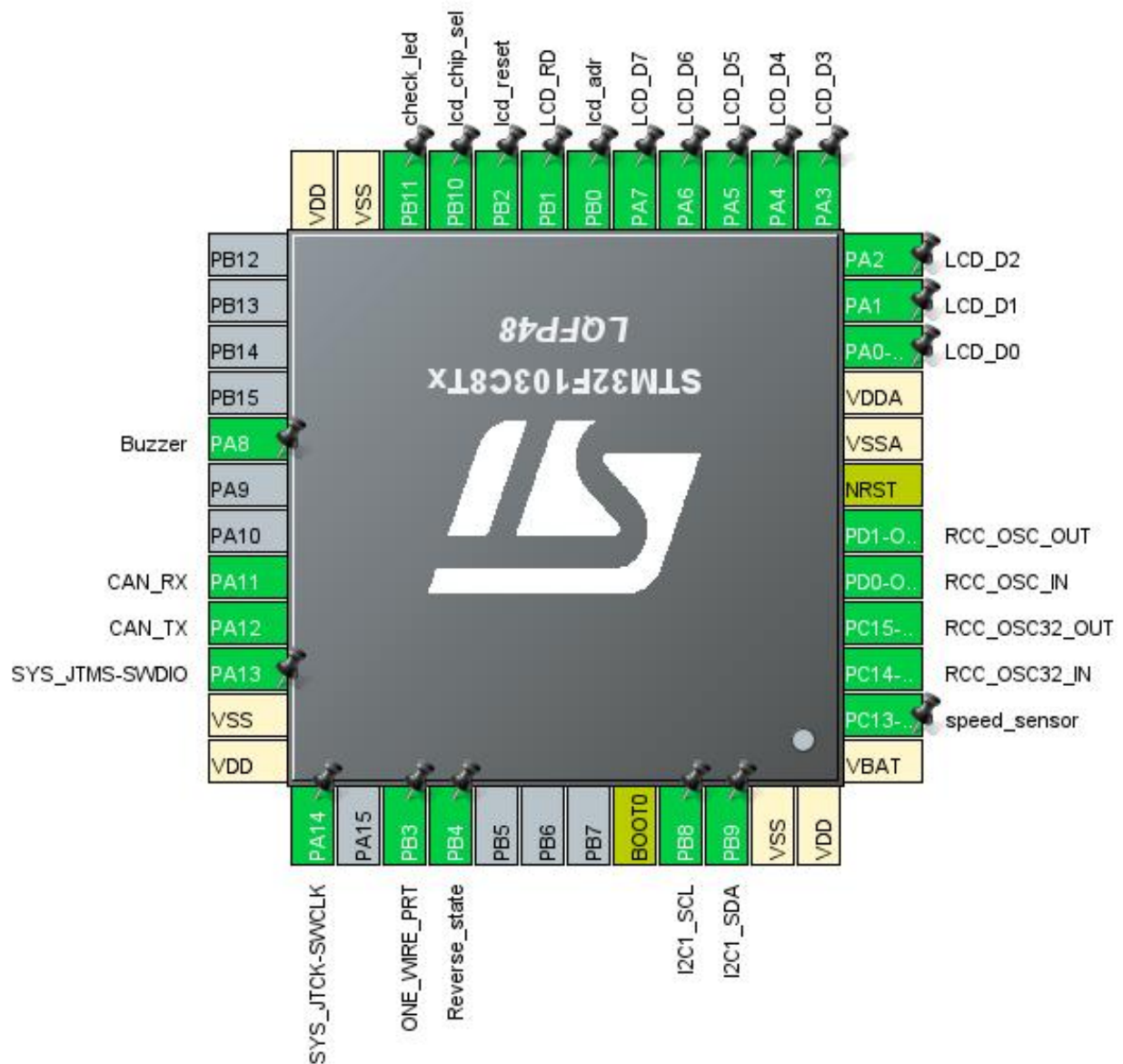
1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	Arm Cortex-M3
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2. Pinout Configuration



(Rotated -180°)

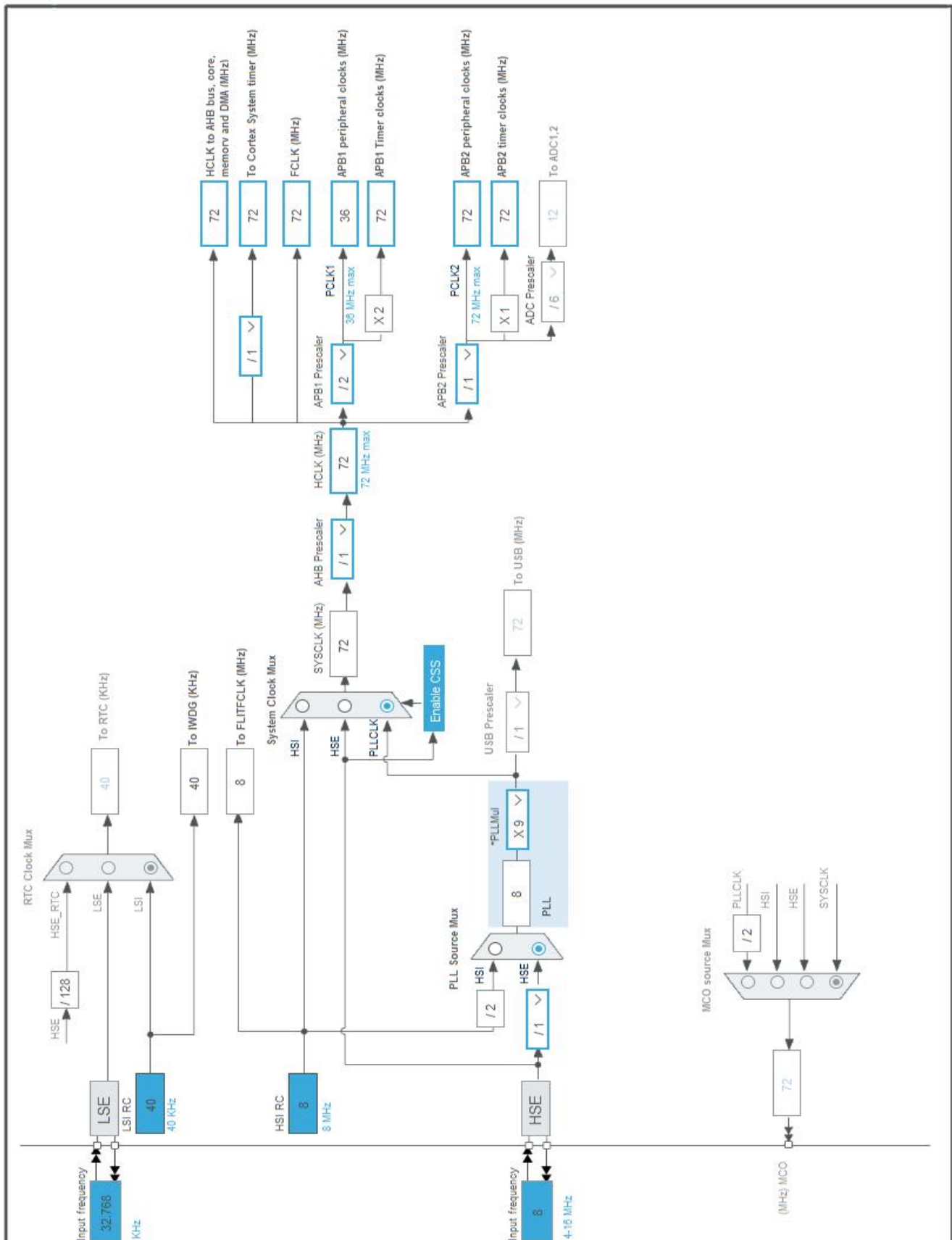
3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-TAMPER-RTC	I/O	GPIO_EXTI13	speed_sensor
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0-WKUP *	I/O	GPIO_Output	LCD_D0
11	PA1 *	I/O	GPIO_Output	LCD_D1
12	PA2 *	I/O	GPIO_Output	LCD_D2
13	PA3 *	I/O	GPIO_Output	LCD_D3
14	PA4 *	I/O	GPIO_Output	LCD_D4
15	PA5 *	I/O	GPIO_Output	LCD_D5
16	PA6 *	I/O	GPIO_Output	LCD_D6
17	PA7 *	I/O	GPIO_Output	LCD_D7
18	PB0 *	I/O	GPIO_Output	lcd_adr
19	PB1 *	I/O	GPIO_Output	LCD_RD
20	PB2 *	I/O	GPIO_Output	lcd_reset
21	PB10 *	I/O	GPIO_Output	lcd_chip_sel
22	PB11 *	I/O	GPIO_Output	check_led
23	VSS	Power		
24	VDD	Power		
29	PA8 *	I/O	GPIO_Output	Buzzer
32	PA11	I/O	CAN_RX	
33	PA12	I/O	CAN_TX	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
39	PB3	I/O	GPIO_EXTI3	ONE_WIRE_PRT
40	PB4 *	I/O	GPIO_Input	Reverse_state
44	BOOT0	Boot		
45	PB8	I/O	I2C1_SCL	
46	PB9	I/O	I2C1_SDA	

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C8Tx
Datasheet	DS5319_Rev17

1.2. Parameter Selection

Temperature	25
Vdd	3.3

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

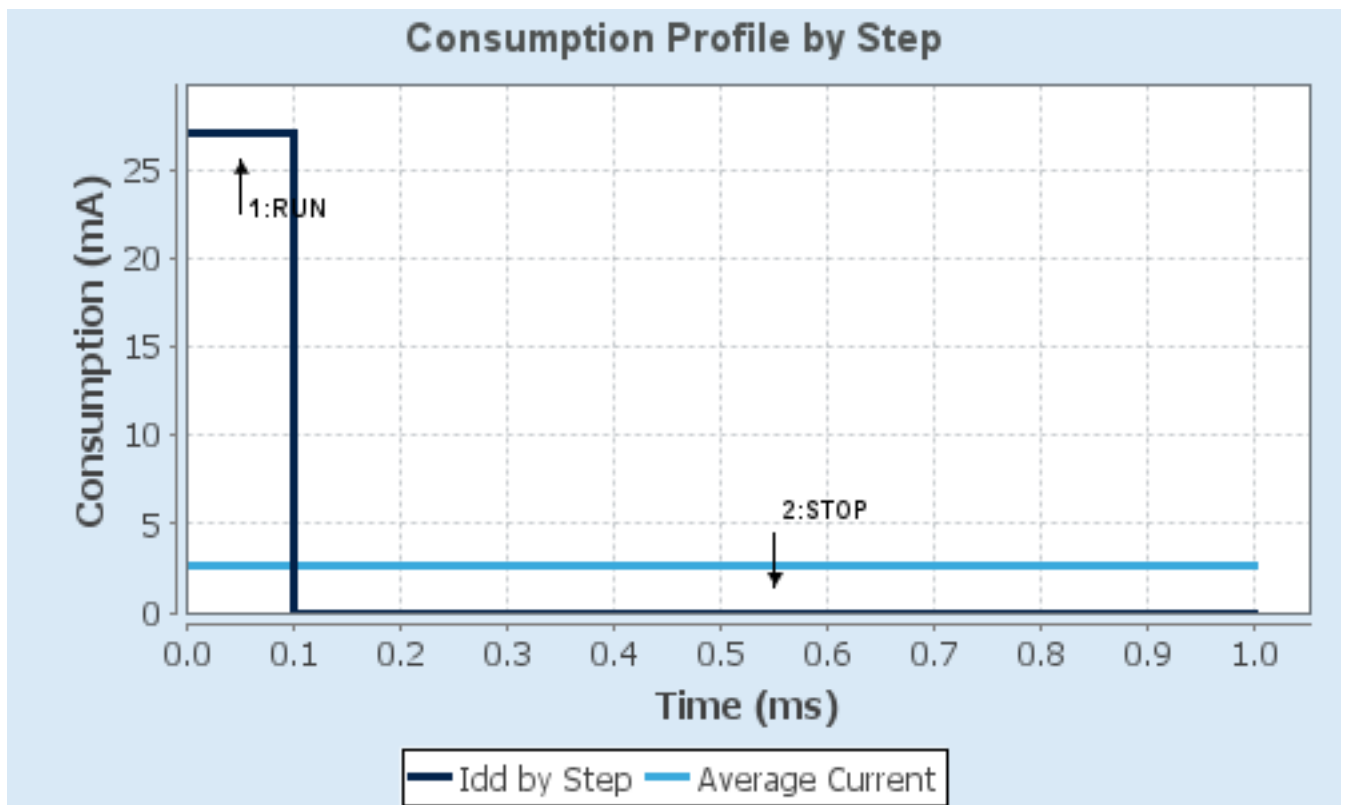
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	27 mA	14 μ A
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	100.1	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	2.71 mA
Battery Life	1 month, 21 days, 17 hours	Average DMIPS	61.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	Bheem_110
Project Folder	D:\Project\OA_23_EE_03-V1\lcd_project\Bheem 110
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.5
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_CAN_Init	CAN
4	MX_TIM2_Init	TIM2
5	MX_I2C1_Init	I2C1
6	MX_IWDG_Init	IWDG

3. Peripherals and Middlewares Configuration

3.1. CAN

mode: Activated

3.1.1. Parameter Settings:

Bit Timings Parameters:

Prescaler (for Time Quantum)	8 *
Time Quantum	222.2222222222223 *
Time Quanta in Bit Segment 1	15 Times *
Time Quanta in Bit Segment 2	2 Times *
Time for one Bit	4000 *
Baud Rate	250000 *
ReSynchronization Jump Width	1 Time

Basic Parameters:

Time Triggered Communication Mode	Disable
Automatic Bus-Off Management	Disable
Automatic Wake-Up Mode	Disable
Automatic Retransmission	Disable
Receive Fifo Locked Mode	Disable
Transmit Fifo Priority	Disable

Advanced Parameters:

Test Mode	Normal
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3.2. I2C1

I2C: I2C

3.2.1. Parameter Settings:

Master Features:

I2C Speed Mode	Fast Mode *
I2C Clock Speed (Hz)	400000
Fast Mode Duty Cycle	Duty cycle Tlow/Thigh = 2

Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

3.3. IWDG

mode: Activated

3.3.1. Parameter Settings:

Clocking:

IWDG counter clock prescaler	128 *
IWDG down-counter reload value	1000 *

3.4. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

3.4.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

3.5. SYS

Debug: Serial Wire

Timebase Source: SysTick

3.6. TIM2

Clock Source : Internal Clock

3.6.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	100-1 *
Counter Mode	Up

Counter Period (AutoReload Register - 16 bits value) **72-1 ***

Internal Clock Division (CKD) No Division

auto-reload preload Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

*** User modified value**

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
CAN	PA11	CAN_RX	Input mode	No pull-up and no pull-down	n/a	
	PA12	CAN_TX	Alternate Function Push Pull	n/a	High *	
I2C1	PB8	I2C1_SCL	Alternate Function Open Drain	n/a	High *	
	PB9	I2C1_SDA	Alternate Function Open Drain	n/a	High *	
RCC	PC14-OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15-OSC32_OUT	RCC_OSC32_OUT	n/a	n/a	n/a	
	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
GPIO	PC13-TAMPER-RTC	GPIO_EXTI13	External Interrupt Mode with Falling edge trigger detection	Pull-up *	n/a	speed_sensor
	PA0-WKUP	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D0
	PA1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D1
	PA2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D2
	PA3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D3
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D4
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D5
	PA6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D6
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_D7
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	lcd_adr
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LCD_RD
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	lcd_reset
	PB10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	lcd_chip_sel
	PB11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	check_led
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	Buzzer
	PB3	GPIO_EXTI3	External Interrupt Mode with Rising edge trigger detection	Pull-up *	n/a	ONE_WIRE_PRT

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
	PB4	GPIO_Input	Input mode	Pull-up *	n/a	Reverse_state

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
EXTI line3 interrupt	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
TIM2 global interrupt	true	0	0
EXTI line[15:10] interrupts	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		
CAN RX1 interrupt	unused		
CAN SCE interrupt	unused		
I2C1 event interrupt	unused		
I2C1 error interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
EXTI line3 interrupt	false	true	true
USB low priority or CAN RX0 interrupts	false	true	true
TIM2 global interrupt	false	true	true

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
EXTI line[15:10] interrupts	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

Middleware

System Core

Analog

Timers

Connectivity

Computing

DMA

TIM2 

CAN 

GPIO 

I2C1 

IWDG 

IVIC 

RCC 

SYS 

6. Docs & Resources

Type	Link
BSDL files	https://www.st.com/resource/en/bsdl_model/stm32f1_bsdل.zip
IBIS models	https://www.st.com/resource/en/ibis_model/stm32ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32f1_svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_eval_tools_portfolio.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf
Presentations	https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf
Brochures	https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf
Flyers	https://www.st.com/resource/en/flyer/flstm32nucleo.pdf
Flyers	https://www.st.com/resource/en/flyer/flstmcsuite.pdf
Flyers	https://www.st.com/resource/en/flyer/fldpstpfc11120.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/1239988349.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an2586-getting-started-with-stm32f10xxx-hardware-development-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an2604-stm32f101xx-and-stm32f103xx-rtc-calibration-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3095-stevalisv002v1-stevalisv002v2-3-kw-gridconnected-pv-system-based-on-the-stm32f103xx-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3108-stlm75-firmware-library-for-the-stm32f10x-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an3422-migration-of-microcontroller-applications-from-stm32f1-to-stm32l1-series-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an3427-migrating-a-microcontroller-application-from-stm32f1-to-stm32f2-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3429-stm32-proprietary-code-protection-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3961-stevalime003v1-demonstration-board-based-on-the-sthv748-ultrasound-pulser-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4070-250-w-grid-connected-microinverter-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4076-two-or-three-shunt-resistor-based-current-sensing-circuit-design-in-3phase-inverters-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4088-migrating-between-stm32f1-and-stm32f0-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4228-migrating-from-stm32f1-series-to-stm32f3-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4649-migrating-from-stm32f1-series-to-stm32l4-series--stm32l4-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf
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Application Notes https://www.st.com/resource/en/application_note/an2548-introduction-to-dma-controller-for-stm32-mcus-stmicroelectronics.pdf

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Application Notes https://www.st.com/resource/en/application_note/an4277-how-to-use-pwm-shutdown-for-motor-control-and-digital-power-conversion-on-stm32-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4908-getting-started-with-uart-automatic-baud-rater-detection-for-stm32-mcus-stmicroelectronics.pdf

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for related Tools inapplication-programming-using-the-usart-stmicroelectronics.pdf
& Software

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for related Tools timer-resolution-with-software-expansion-for-stm32cube-and-standard-peripheral-library-stmicroelectronics.pdf
& Software

Application Notes https://www.st.com/resource/en/application_note/an2594-eeeprom-emulation-in-stm32f10x-microcontrollers-stmicroelectronics.pdf
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& Software

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