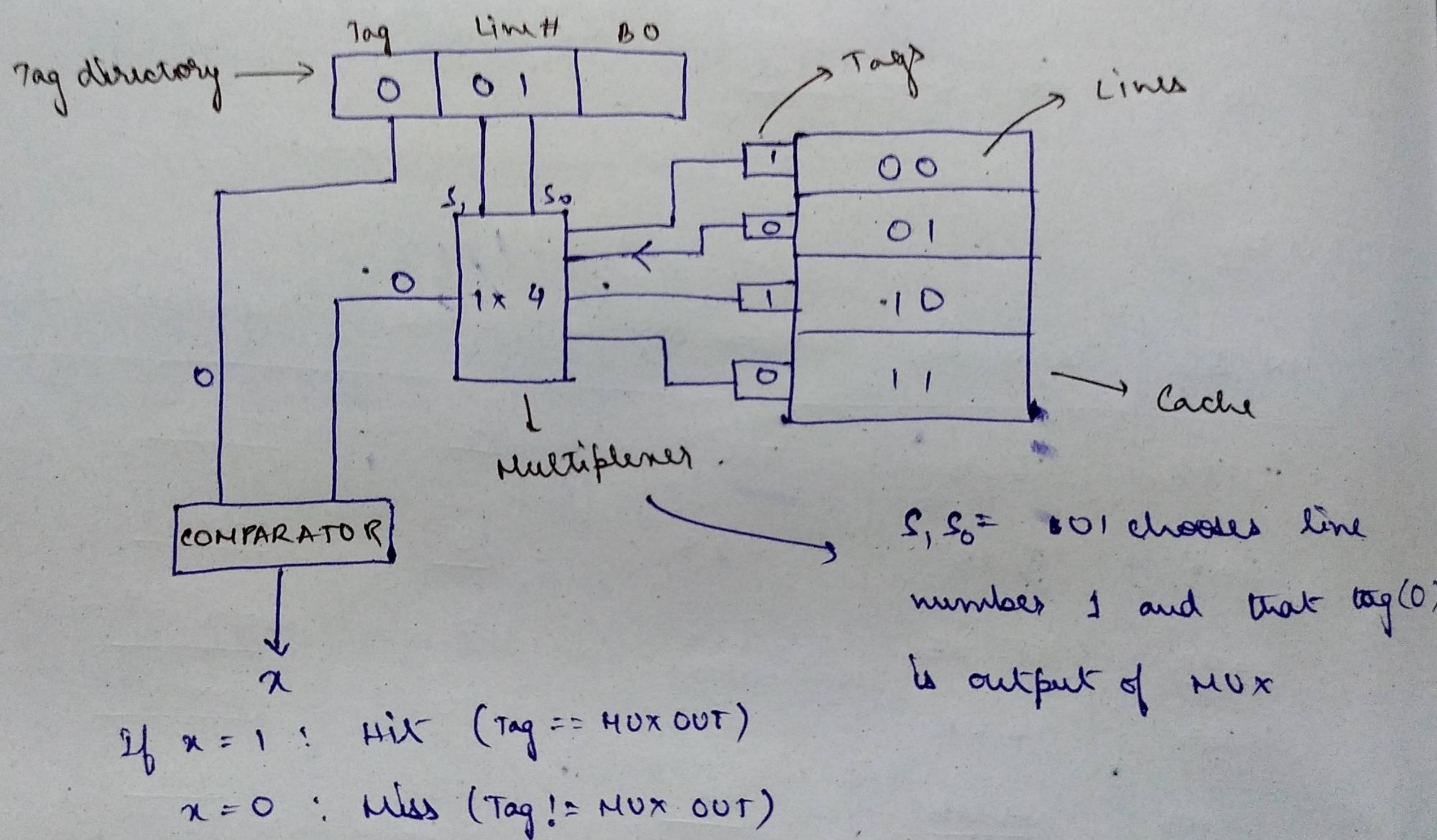


## DIRECT MAPPING IMPLEMENTATION





⑤ If number of tag bits is 2 then we need to have two multiplexers.

For  $K$  tag bits,  $\# \text{MUX} = K$ .  
No. of comparator required is 1. ( $K$  bit comparator)

⑥ Hit / Miss Latency = MUX delay + COMPARATOR delay

Q. If  $MM = 1 \text{ GB}$ , cache size =  $1 \text{ MB}$ ,

comparator delay =  $10 \text{ K ns}$  ( $K = \# \text{ tag bits}$ )

Hit latency = ?

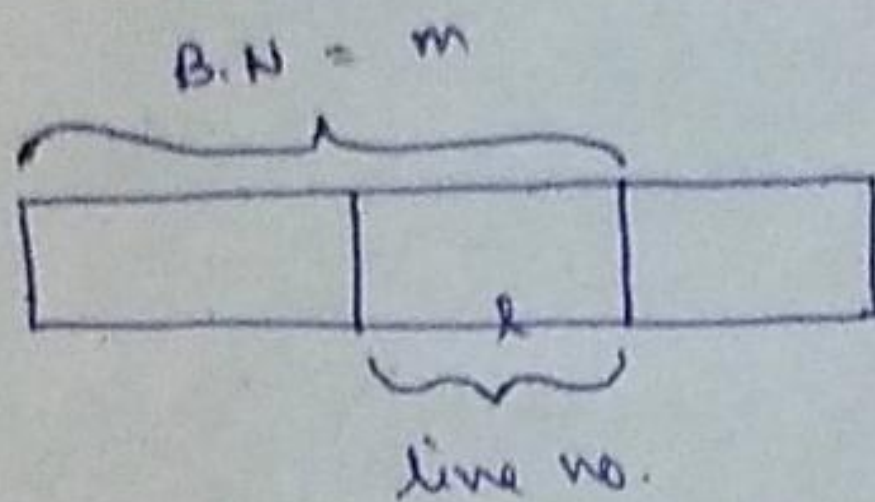
$$\rightarrow \# \text{ tag bits} = \log_2 \frac{2^{10}}{2^0} = 10$$

Comp. delay = ~~10 K ns~~  $100 \text{ ns}$

So, Hit latency =  $100 \text{ ns}$ . (Ans)



## Disadvantage of Direct Mapping



$$\# \text{ Blocks} = 2^m$$

$$\# \text{ Lines} = 2^l$$

- ⑤ Position of  $x^{\text{th}}$  block of main memory in a cache containing ' $l$ ' lines is  $(x \% 2^l) = \text{line number}$ .

0	1	8, 12, 20
1	8	9
2	6	
3	15	

5, 6, 4, 8, 9, 12, 15, 20

It does not use the free space. This is called conflict miss.

- ⑥ If there is not space left then it is called capacity miss.

## ASSOCIATIVE MAPPING

In associative mapping any byte from a block of main memory can go to any line of cache.

$$\text{So here } [\# \text{ bits in tag} = \log_2(\text{blocks}) = \# \text{ lines}]$$

In this case for searching an element we need to compare it with all bytes in cache.

So, we use  $n$  comparators where  $n = \# \text{ lines}$ .

If any comparator produces 1 then it is a 'Hit'!



Q. Given, Main memory size = 32 GB

Block size = 32 KB

Propagation delay of comparator =  $10K$  ns ( $K$  = # tag bits)

Propagation delay of OR GATE = 10 ns

Hit latency = ?

$$\rightarrow \# \text{Tag bits} = \log_2 \left( \frac{2^{35}}{2^{15}} \right) = 20$$

$$\# \text{ comparators} = 20$$

$$\begin{aligned} \text{Total delay by comparators} &= 20 \times 10 \text{ ns} \\ &= 200 \text{ ns} \end{aligned}$$

$$\text{Hit latency} = (200 + 10) = 210 \text{ ns}$$

Q.	MM Size	Cache Size	Block Size	Tag Size	Tag directory Size	# comp.
a)	128 MB	512 KB	1 KB	17	$17 \times 2^9$	512
b)	16 GB	NA	4 KB	10	NA	NA
c)	64 MB	NA	64 KB	10	NA	NA
d)	NA	512 KB	NA	7	NA	NA



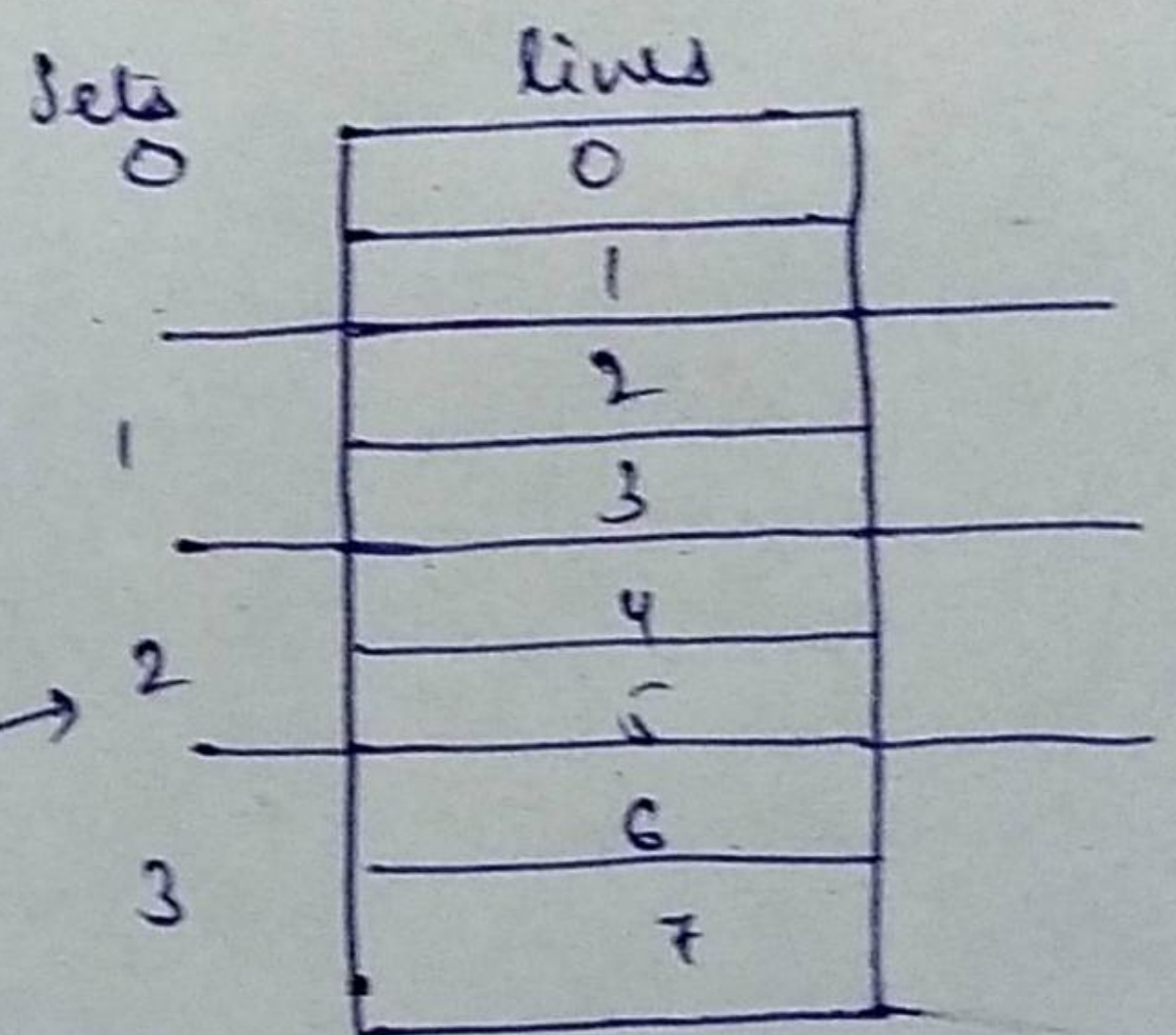
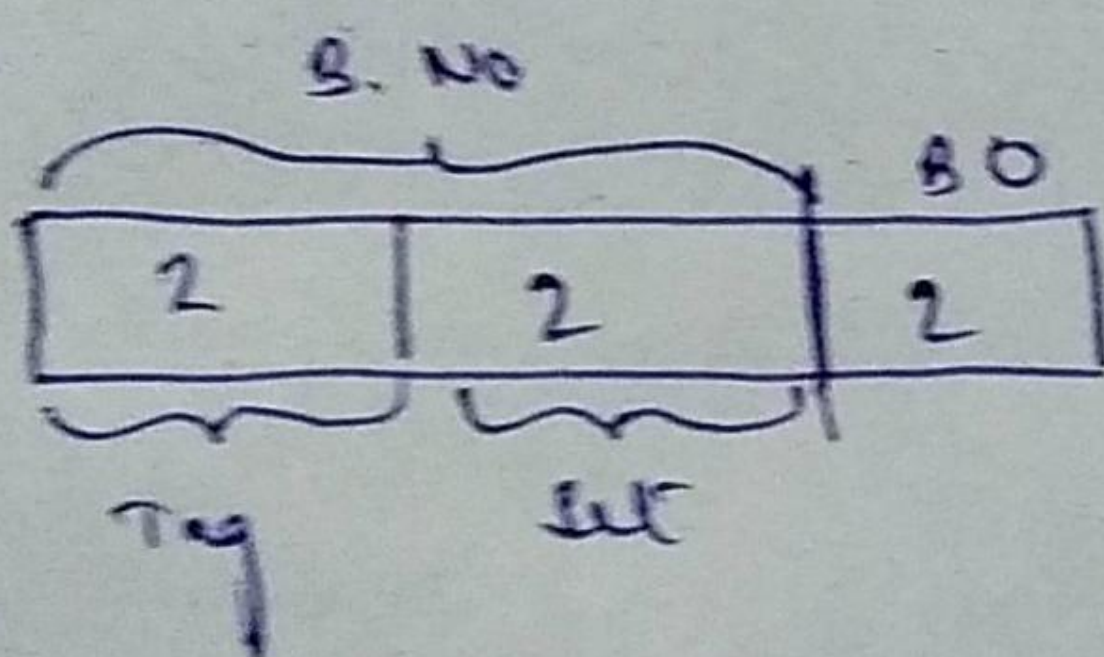
## SET ASSOCIATIVE MAPPING

Let  $MM = 64B$ ,  $CS = 32B$ ,  $BS = 4B$ .

1 set size =  $2^{\text{lines}}$  (also called 2way set associative)

$$\rightarrow \# \text{ lines} = \frac{CS}{BS} = 8$$

$$\# \text{ set} = \frac{\# \text{ lines}}{\text{set size}} = \frac{8}{2} = 4$$



So, given any address  $011011$ , so here only two comparators will be required rather than 8.

• Size of each comparator = 2 bytes (# Tag bytes).