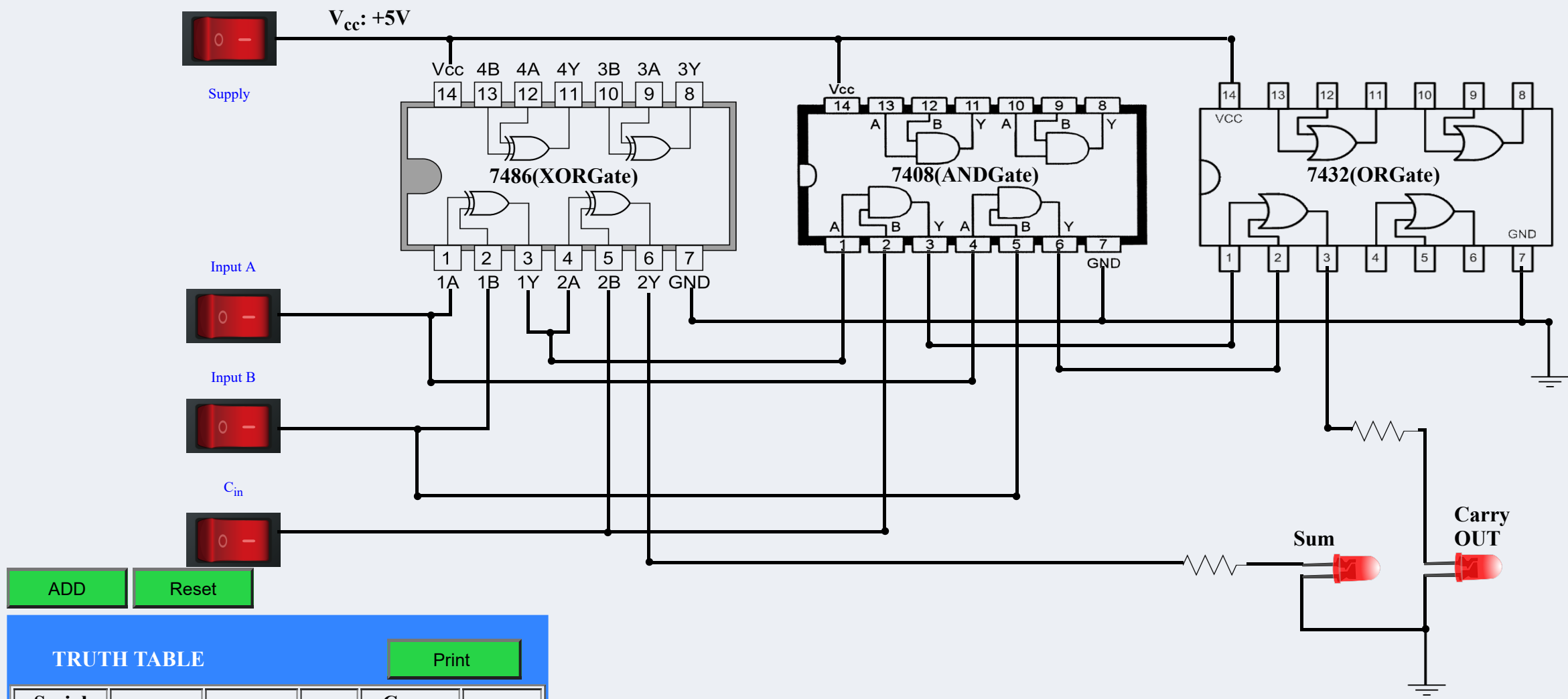


INSTRUCTIONS

Experiment to perform logic of Full Adder on kit



ADDReset

Print

Serial No.	Input A	Input B	C _{in}	Carry OUT	Sum
1	0	0	0	0	0
2	0	0	1	0	1
3	0	1	0	0	1
4	0	1	1	1	0
5	1	0	0	0	1
6	1	0	1	1	0
7	1	1	0	1	0
8	1	1	1	1	1