INSTRUCTIONS Experiment to perform logic of half adder on kit Vcc 4B 4A 4Y 3B 3A 3Y 14 13 12 11 10 9 8 Vcc 14 13 12 11 10 9 8 A B Y A B Y __7408(ANDGate) 7486(XORGate) 1 2 3 4 5 6 7 1A 1B 1Y 2A 2B 2Y GND Input A __GND R₁: 330 ohm R₂: 330 ohm SUM CARRY SUM I GND TRUTH TABLE Print Serial No. Input A Input B Sum Carry 0 0 0 2 0 1 1 0 3 1 0 1 0 4 1 1 0 1

7/8/2021

https://de-iitr.vlabs.ac.in/exp/half-full-adder/simulation/Simulator.html

HALF ADDER