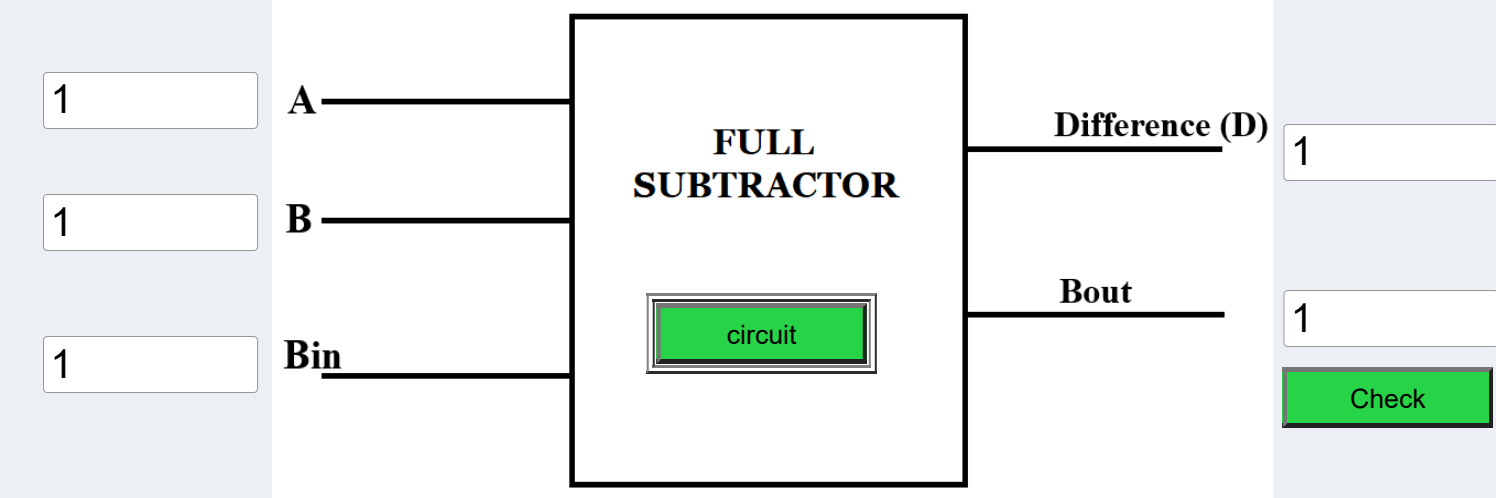


INSTRUCTIONS

Verification of truth table for Full Subtractor Circuit



TRUTH TABLE						Print
Serial No.	A	B	Bin	D	Bout	Remarks
1	0	0	0	0	0	Correct
2	0	0	1	1	1	Correct
3	0	1	0	1	1	Correct
4	0	1	1	0	1	Correct
5	1	0	0	1	0	Correct
6	1	0	1	0	0	Correct
7	1	1	0	0	0	Correct
8	1	1	1	1	1	Correct

Reset