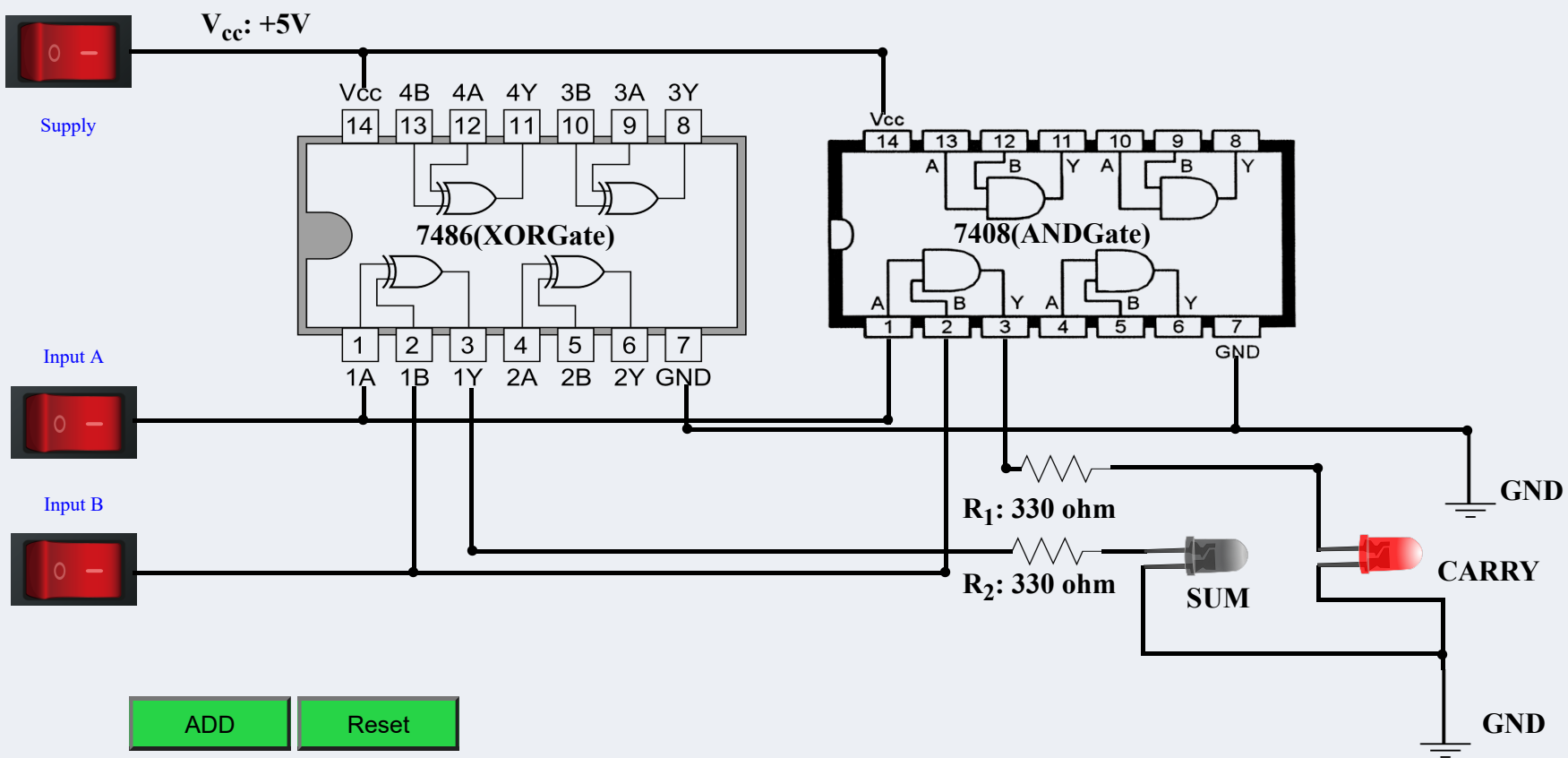


INSTRUCTIONS

Experiment to perform logic of half adder on kit



ADD

Reset

TRUTH TABLE

Print

| Serial No. | Input A | Input B | Sum | Carry |
|------------|---------|---------|-----|-------|
| 1 | 0 | 0 | 0 | 0 |
| 2 | 0 | 1 | 1 | 0 |
| 3 | 1 | 0 | 1 | 0 |
| 4 | 1 | 1 | 0 | 1 |