

INSTRUCTIONS

Experiment to perform logic of 2x4 Decoder

V_{cc} +5V

Supply

A

B

ADD

TRUTH TABLE

Print

S.No.	A	B	Q ₀	Q ₁	Q ₂	Q ₃
1	0	0	1	0	0	0
2	0	1	0	1	0	0
3	1	0	0	0	1	0
4	1	1	0	0	0	1

V_{cc} 5A 6V 5A 5V 4A 4V

7404 Hex Inverters

V_{cc}

7408(ANDGate)

Q₀

Q₁

Q₂

Q₃

GND

R₀: 330 ohm

Q₀

R₁: 330 ohm

Q₁

R₂: 330 ohm

Q₂

R₃: 330 ohm

Q₃

<https://de-itr.vlabs.ac.in/exp/decoder-demultiplexer-encoder/simulation/decoder/Decoder.html>

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