

INSTRUCTIONS

Experiment to perform SR Flip Flop on kit

POWER

S

R

Clock

7400(NAND)

V_{cc}

13 12 11 10 9 8

1 2 3 4 5 6

GND

$R_1: 330\text{ ohm}$

$R_2: 330\text{ ohm}$

Q

GND

\bar{Q}

TRUTH TABLE

PRINT

ADD

Serial No.	clock	S	R	Q(n-1)	$\bar{Q}(n-1)$	Q	\bar{Q}	Remark
1	0	0	0	X	X	0	1	No Change
2	1	0	1	0	1	0	1	Reset
3	1	1	0	0	1	1	0	set
4	1	1	1	1	0	0	0	INVALID

CLOCK DIAGRAM

Q=1

Q=0

Q=1

Q=0

R=1

R=0

S=1

S=0

Clock=1

Clock=0

Y-axis

X axis