Pratik P Patil

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Flat no. 105, Classic Pride Apartment, Somwarpeth Tilakwadi
Belagavi, Karnataka.

Brief Overview/ Career Objective/Summary

Electronics and Communication Engineer with knowledge in hardware and software. To work for the organization with dedication and make use of my knowledge for the growth of the organization and to explore technologies.

Internship

Vayavya Labs Pvt. Ltd. *Belagavi, Karnataka | April 6th, 2024 to May 25th, 2024*

- Tested, designed, and analyzed the feasibility, operation, and performance of device drivers like UART and I2C, with hands-on experience in ddgen software.

Skills

Cadence, C programming, Matlab

Education-B.TECH, XII, X- College/School name, Passing Year & Percentage

Bachelor of Engineering (Electronics andcommunication Engineering)
- KLE Technological university Belagavi campus
- Pursuing (Expected Year of Passing 2025)
- CGPA: 7.51 (up to 6th semester)

Pre-University Course (PUC)
- Govindram Seksaria SciencePU College Belagavi
- Year of Passing 2021
- Percentage: 74.83%

Secondary School Leaving Certificate (SSLC)
- M.V. Herwadkar English Medium High School Belagavi
- Year of Passing 2019
- Percentage: 85.44%

Projects

- **Background Noise Detection (Digital Signal Processing Using PBL)**
  This project involves a MATLAB script that is able to remove background noise from a recorded audio file with different noises in the background (traffic sound, jet engine).

- **Design and approach of high Performance SRAM.**
  An 8T SRAM cell with a minimum transistor width of 120nm was design and verifies stability via the butterfly curve. Optimization involved adjusting pull-down and pass transistor widths to 240nm and comparing average power consumption.

- **4-Bit Carry Select Adder using CMOS 180nm technology**
  The project explores the application of techniques such as Modified Gate Diffusion Input (MGDI) to enhance power efficiency, Designed and optimized attransistor level i.e. 28T and 10T, The optimized model is 10T.

Certifications

MATLAB onramp

Personal Interests/Hobbies

Playing sports

Personal Details

- DOB: 06/05/2003
- Fathers name: Parasharam M Patil
- Languages: English, Hindi, Marathi, Kannada.

DECLARATION:

I hereby declare that the details provided by me in this resume are correct and I have knowingly not omitted/ misrepresented any information.