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Assignment 6

- Title : To read & display contents pointed by GDTR, LDTR & IDTR.

- Problem Statement:
Write an ALP to read & display the table content pointed out by GDTR, LDTR & IDTR

- Objective:
To understand how to read & display contents of GDTR, LDTR & IDTR

- Outcome:
I will study different descriptor table in system along with different registers associated with it.

- Software Package & Hardware Apparatus:
1) Core 2 duo / i3 / i5 / i7
2) Linux 32/64 bit os
3) gedit
4) NASM, GDB

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- Theory:

1) GDTR & IDTR:
   a) Hold 32 bit linear base address
   b) 16 bit limit for GDT/IDT
   c) The segments are global to all system tasks.

2) LDTR & TR
   a) Hold 16 bit selector for LDT descriptor & TSS
   b) LDT & TSS segments are defined by selector values stored in the system segment registors.

- Algorithm:

1) START
2) Store values of GDTR, LDTR, IDTR & TR in separate memory locations.
3) Display contents stored in memory loations.
4) STOP

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- Test Cases:

	Input	Expected Output	Actual output
1)	GDTR: 1F39000:007F	GDTR: 1F39000:007F	
	IDTR: FF56B000:0FFF	IDTR: FF56B000:0FFF	
	LDTR: 0000	LDTR: 0000	Yes
	MSW: 8005FFFF	MSW: 8005FFFF	
	TR : 0040	TR : 0040	

- Conclusion:
We have studied different descriptor tables in system & also different registers associated with it.