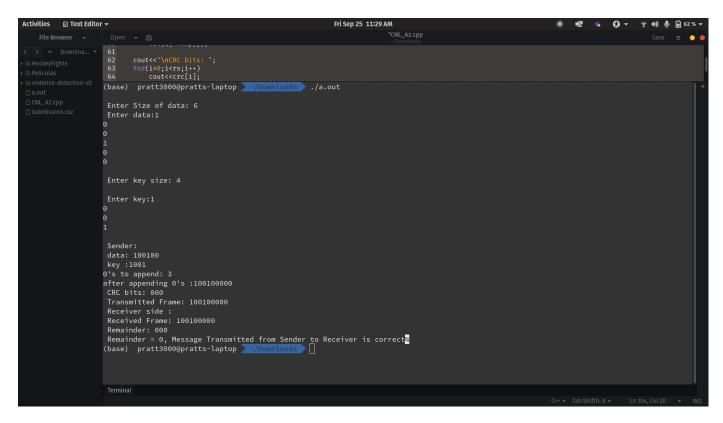
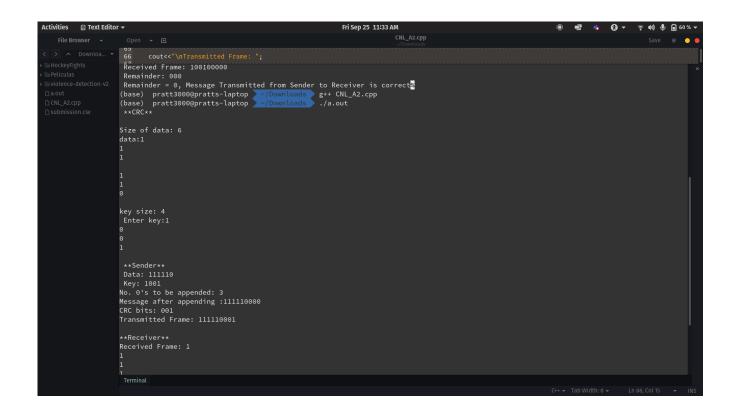
CODE

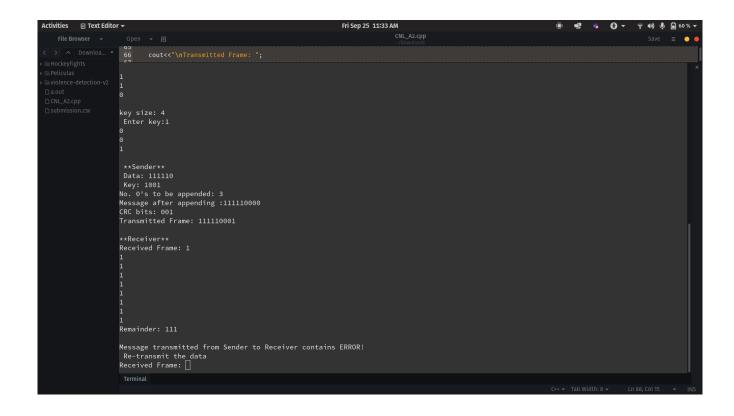
```
#include <bits/stdc++.h>
using namespace std;
void division(int temp[], int g[], int fs, int gs){
  for(int i=0;i< fs;i++){
     int j=0,k=i;
     if (temp[k] > = g[j]){
        for(j=0,k=i;j < gs;j++,k++){
           if((temp[k]==1 \&\& g[j]==1) || (temp[k]==0 \&\& g[j]==0))
             temp[k]=0;
          else
             temp[k]=1;
        }
     }
  }
int main(){
        cout << " **CRC**\n";
  int i,j,k,l;
  int fs, f[20], gs, g[20], temp[20], tf[15], crc[15];
  cout<<"\nSize of data: ";</pre>
  cin>>fs;
  cout << "data:";
  for(i=0;i<fs;i++)
     cin >> f[i];
  cout<<"\nkey size: ";</pre>
  cin>>gs;
  cout<<" Enter key:";</pre>
  for(i=0;i < gs;i++)
     cin >> g[i];
  cout<<"\n **Sender**";</pre>
  cout<<"\n Data: ";
  for(i=0;i<fs;i++)
     cout<<f[i];
  cout << "\n Key: ";
```

```
for(i=0;i < gs;i++)
  cout<<g[i];
int rs=gs-1;
cout<<"\nNo. 0's to be appended: "<<rs;
for (i=fs;i<fs+rs;i++)
  f[i]=0;
for(i=0;i<20;i++)
  temp[i]=f[i];
cout<<"\nMessage after appending :";</pre>
for(i=0; i<fs+rs;i++)
  cout<<temp[i];</pre>
division(temp,g,fs,gs);
for(i=0,j=fs;i< rs;i++,j++)
  crc[i]=temp[j];
cout<<"\nCRC bits: ";</pre>
for(i=0;i<rs;i++)
  cout<<crc[i];
cout<<"\nTransmitted Frame: ";</pre>
for(i=0;i<fs;i++)
  tf[i]=f[i];
for(i=fs,j=0;i<fs+rs;i++,j++)
  tf[i]=crc[j];
for(i=0;i< fs+rs;i++)
  cout<<tf[i];</pre>
cout << "\n^**Receiver^**";
label:
cout<<"\nReceived Frame: ";</pre>
     for(i=0;i<fs+rs;i++)
  cin>>temp[i];
division(temp,g,fs,gs);
cout<<"Remainder: ";</pre>
int rrem[15];
for (i=fs,j=0;i<fs+rs;i++,j++)
  rrem[j]=temp[i];
```

```
for(i=0;i<rs;i++)
     cout<<rrem[i];</pre>
libr
  int flag=0;
  for(i=0;i<rs;i++){
     if(rrem[i]!=0)
       flag=1;
   }
  if(flag==0)
    cout<<"\n\nMessage transmitted from Sender to Receiver is correct";</pre>
     cout << "\n\nMessage transmitted from Sender to Receiver contains ERROR!\n Re-transmit the
data ";
     goto label;
  }
return 0;
}
```

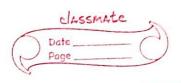






Assignment 2
Title: CRC (Cyclic Redundancy Check)
Problem Statement:
Write a program for error detection
4 correction for 7/8 bits ASCII codes
Using CRC. Demonstrate the packets
captured using wireshark Packet
Analyzer Tool for peer to peer mode
a is out it pride weather the
Software & Hordware Req.:
C++ Compiler, Wireshark Packet Analyzer
Tool, IDE, processor.
of the trusion forms to make sell of
Theory: sout tollowing & such the set
In digital systems, the analog signals
Will change into digital sequence
(in form of bits). This sequence of
bits is called of "Data Stream" The
change in position of single bit also
leads to catastropic error in data
out put with a source trust and told
ited at words to the land and
Error:
1) The data can be corrupted during
transmission.
2) It may be affected by external noise.
3) This mismatch is caller "Error"
Satisficon Sharpatah tomason about it so
na i lu state

*	Types of Error:
)	Single Bit Error;
	a) The change in one bit in whole data
	Søequence is called Single Bit Error.
	b) Occurrence of single bit error is
1	very rare in serial Communication
	system.
0	
	Multiple Bit Data Errors
	a) It there is change in two or more
	bits of data sequence of transmitter
	bo receiver, it is called 'multiple Bit Error'
	b) This type of error occurs both in
	serial type & parallel type data
2.1	communication networks.
	and the second of the second o
3>	Burst Errors:
	a) The change of set of bits in
	ordarios is colle l
	The Dorst Programme
	form first bit change to last bit change.
*	
	Error Detecting Codes: Detecting Codes:
	TIOTISTERYED Excess
	OI IOINS!
	data will be lost.
	10Sb.



	2 1
Receiver	Sender -
	SIME SOM I FA
Data CRC 19 10 15 3	Data 000F
Viewellinking a serior mills	nbits
Divisor Dato CRC	Divisor bit
mused the hadren 10 100 2000 5	H A C
Remainder 1/1 1/3 /200	
Zero jaccept in and 1913	1901.4
Non-zero, Reject Marie	delle CRC
Tell into Solow Solow Solow St.	Cr/MSC Co bits
MCRCA	10/2-001
wom he of 5 blooms where	
* Error Detecting Codes:	
) Parity Checking 3> LRG	C (e)P
2) CRC 4) Ch	ack Sum
	MIEWER D
· CRC (Cyclic Redundancy Ch	
) Amcyclic mode is a line	ard (nik) block
code with the property	that every
aydic shift of a code	word results
in another code word	Land Car Ca
C	
	adding check bits.
	on whom .
5) These are called CRC codes 5) These are implemented	using shift
registers with feedback	Connections
re gisters with reea back	Constitution of the second
	CILL LANGUAGE TO
70.00-10	



*	Algorithm:			
+	A) ENCODING:			
1) M(n): Size o	of message		
	2) G(x): Gener	ator polynomial		
	3) r : orde	er of G(n)		
	4) Y bits are	appended to	lower	
	order end	of Mcn). Th	is makes	
	- block size	bits the value	e of	
	which is			
		divided by GC	n)	
	[Modulo 2 d			
	6] The remainder	r is added t	O XM(x)	
	i) The result is	frame to b	e transmitted	
	T(x)	F(1,3,12/1)	Winds (
	3 300			
	B) DECODING			
	D T(x) is divided by G(x) [modulo 2 div			
	2) If there is no error than frame is accepted 3) The remainder indicates error for		trame	
	in which co	ase Frame is	rejected.	
а	Tooling	Grand In 1	lean see	
-	Testing:	Pacairax S. Ja	Accept Reject	
	Sender Site	Received:	1-(CEP) Ngc	
>	Data: 1010101		Accept	
	CRC: 901	101010100 Remainder:0	1200	
	Divisor: 100	ICENTATION .O		
	Transmitted: 101010100			

	classmate
5	Date
1	Page
1	

Data: 10011	Received:	Rejected
Divisor: 1101	10011001	3
CRC: 011	Remainder:	
Transmitted: 10011011	001	
Conclusion:		
	ion & correcti	on using
	studied & im	
successfully.		,
13 12 33 7		
9.30		,
'		
A AYELLANDA		<u> </u>
3.5	7	The state of
		20
No. 1944		
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		Service Care
		The second secon