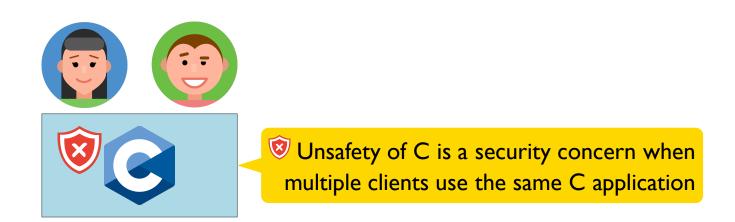
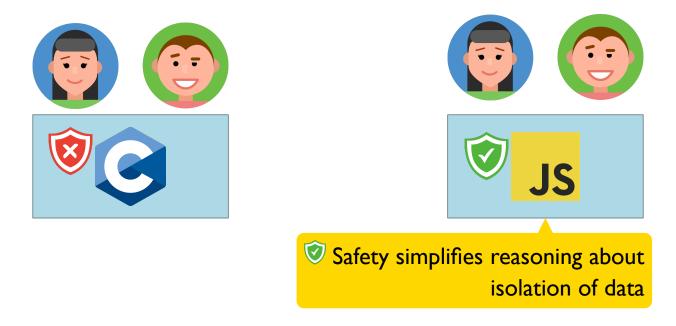


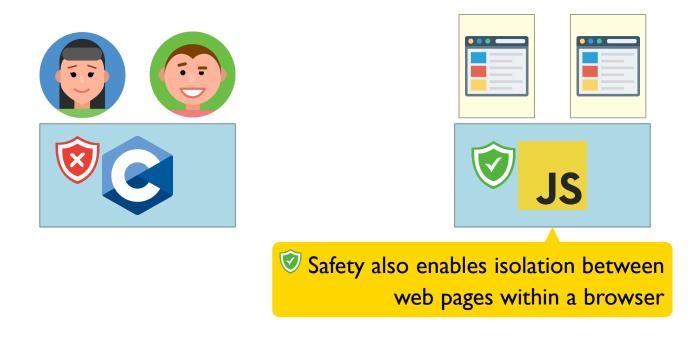
Interesting because they're

- recently discovered
- relatively practical (before mitigations)
- attacks about fundamental strategies for system performance

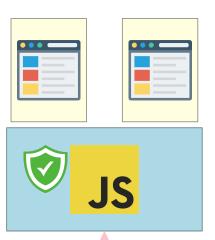
Like other timing channels, these attacks are probably not something you'll need to worry about in day-to-day programming but they're worth understanding as an example of security as a system-wide, cross-cutting concern







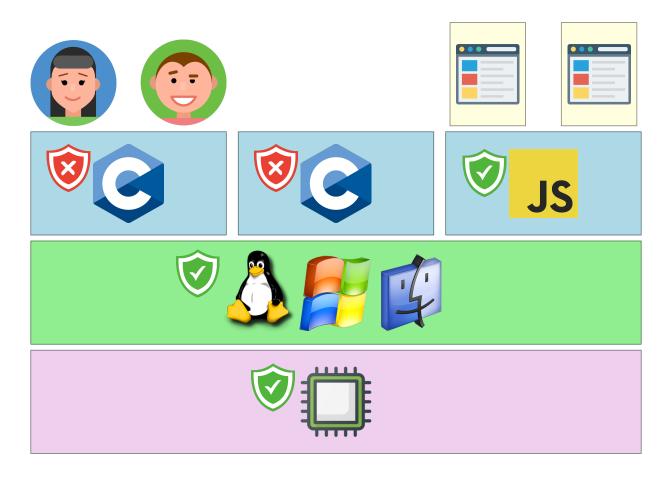


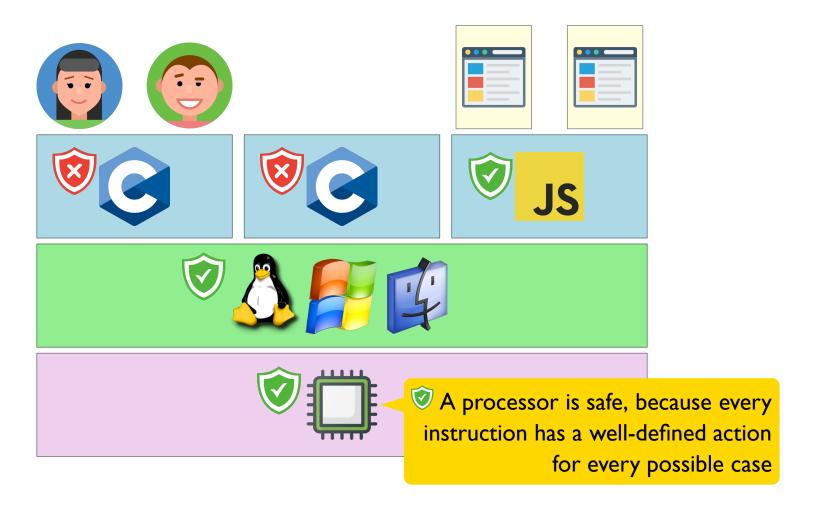


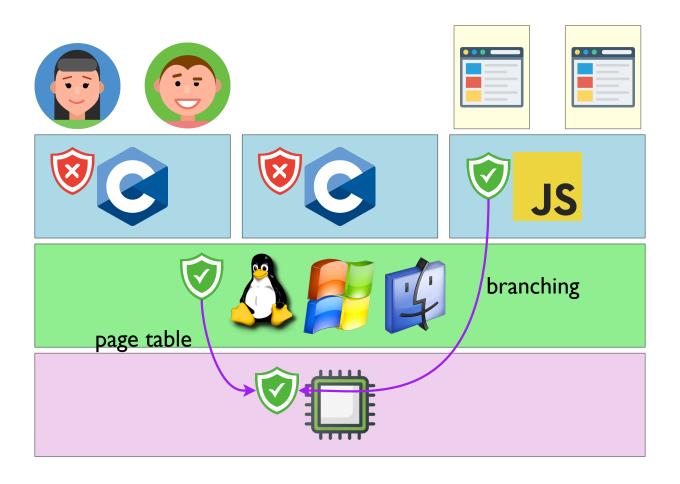
- Safety depends on run-time checks:
 - bounds checks
 - tag checks

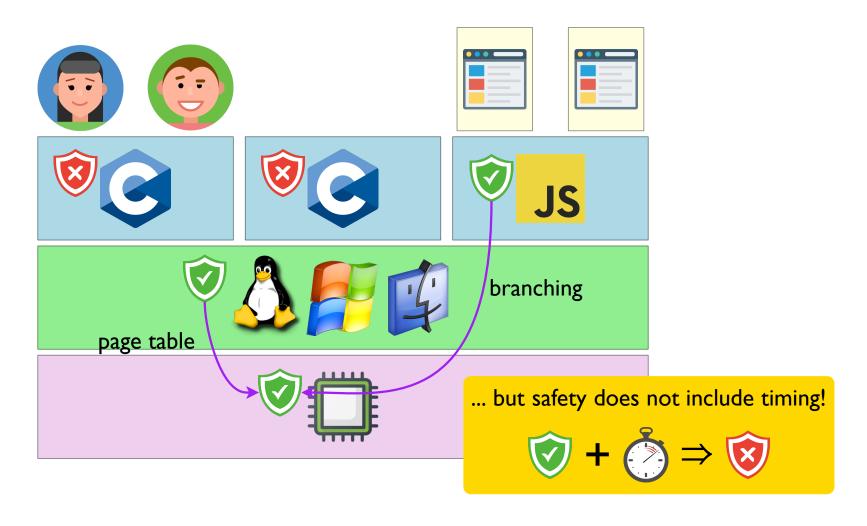


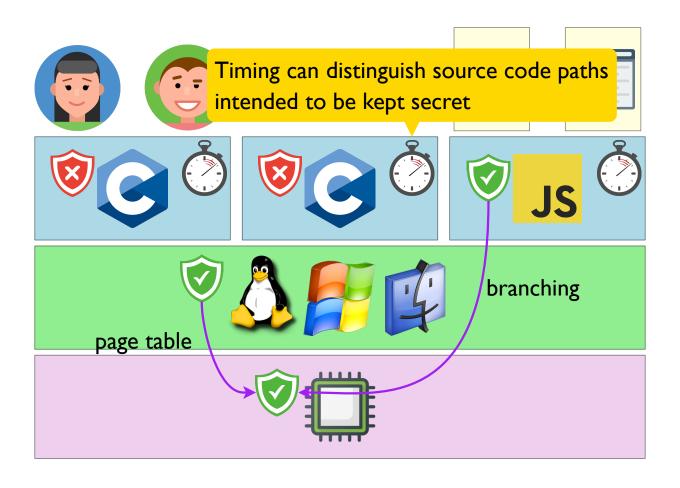


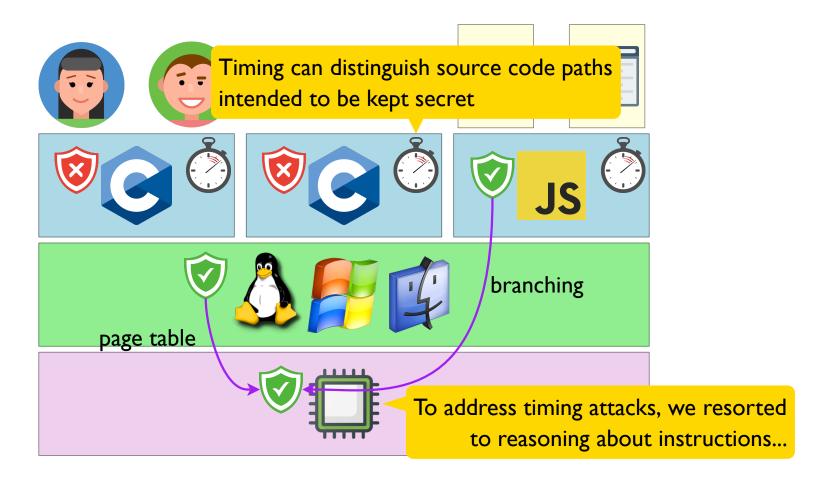


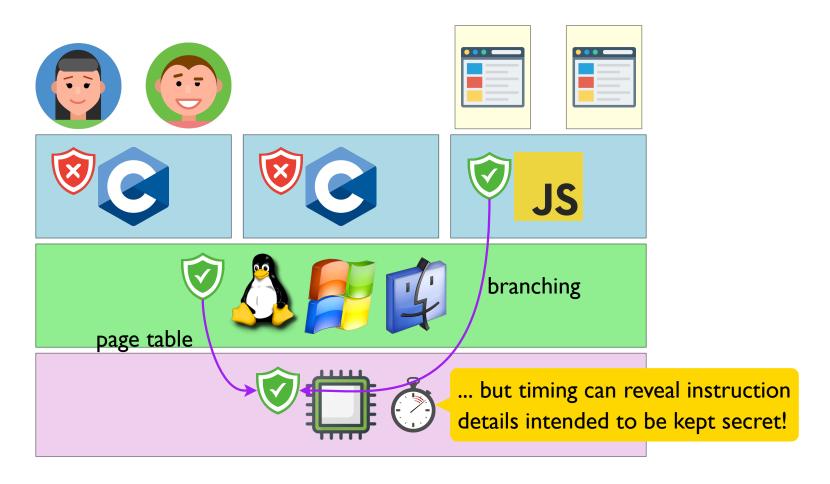


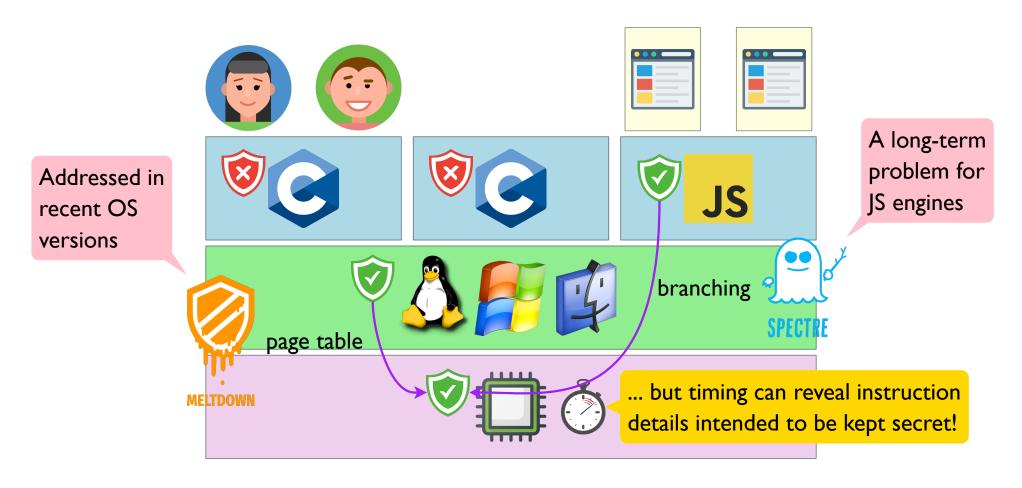














```
100003 de4 cmp x3, #0x1
100003de8 b.lt 0x100003e1c
100003dec mov x9, #0x0
100003df0 mov x8, \#-0x1
100003df4 tbnz x2, #0x3f, 0x100003e14
100003df8 cmp x2, x1
100003dfc b.ge 0x100003e14
100003e00 ldr x2, [x0, x2, lsl #3]
100003e04 add x9, x2, x9
100003e08 mov x8, x9
100003e0c subs x3, x3, #0x1
100003e10 b.ne 0x100003df0
100003e14 mov x0, x8
100003e18 ret
100003e1c mov x0, #0x0
100003e20 ret
```



Simple model of a processor: a minion that steps through instructions one-by-one

```
100003 de4 cmp x3, #0x1
100003de8 b.lt 0x100003e1c
100003dec mov x9, #0x0
100003df0 mov x8, \#-0x1
100003df4 tbnz x2, #0x3f, 0x100003e14
100003df8 cmp x2, x1
100003dfc b.ge 0x100003e14
100003e00 ldr x2, [x0, x2, lsl
100003e04
          add x9, x2, x9
100003e08 mov x8, x9
100003e0c subs x3, x3, #0x1
100003e10 b.ne 0x100003df0
100003e14 mov
               x0, x8
100003e18 ret
100003e1c mov
               x0, #0x0
100003e20 ret
```

Each instruction might take a different amount of time from other instructions, but always the same itself?

#3]

Example Program

```
long sum path(long *array, long len, long pos, long count) {
  long i = 0, sum = 0;
  while (i < count) {</pre>
    if (pos >= 0 && pos < len)
     pos = array[pos];
    else
     return -1;
    sum = sum + pos;
    i = i + 1;
  return sum;
#define N 10
int aray[N] = \{ 9, 0, 1, 2, 3, 4, 5, 6, 7, 8 \};
```

Example Program

```
long sum path(long *array, long len, long pos, long count) {
  long i = 0, sum = 0;
  while (i < count) {</pre>
    if (pos >= 0 \&\& pos < len)
      pos = array[pos];
    else
      return -1;
    sum = sum + pos;
    i = i + 1;
                                   Same instructions, very different times!
                   small array with small jumps:
                                                   150ms
  return sum;
                                                   750ms
                   big array with big jumps:
for (long i = 0; i < 100; i++)
  v = sum path(array, N, i % N, 1000000);
```



100003de4 cmp x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e1c mov x0, #0x0

100003e18 ret

100003e20 ret

registers

| x 0 | 0000 |
|------------|------|
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

| 100003de8 | b.lt | 0x10 | 00003e1c |
|-------------------------|------|------|--------------------|
| 100003dec | mov | х9, | #0x0 |
| 100003df0 | mov | x8, | #-0x1 |
| 100003df4 | tbnz | x2, | #0x3f, 0x100003e14 |
| 100003df8 <mark></mark> | cmp | x2, | x1 |
| 100003dfc | b.ge | 0x10 | 00003e14 |
| 100003e00 | ldr | x2, | [x0, x2, 1s1 #3] |
| 100003e04 | add | x9, | x2, x9 |
| 100003e08 | mov | x8, | x9 |
| 100003e0c | subs | х3, | x3, #0x1 |

memory

•••

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|--------|--------|--------|--------|--------|--------|
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

•••



| _ | |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

| 100003de4 | cmp | x3, | #0x1 |
|-----------|------|-----|--------------------|
| 100003de8 | b.lt | 0x1 | 00003e1c |
| 100003dec | mov | x9, | #0×0 |
| 100003df0 | mov | x8, | #-0×1 |
| 100003df4 | tbnz | x2, | #0x3f, 0x100003e14 |
| 100003df8 | cmp | x2, | x1 |
| 100003dfc | b.ge | 0x1 | 00003e14 |
| 100003e00 | ldr | x2, | [x0, x2, ls1 #3] |
| 100003e04 | add | x9, | x2, x9 |
| 100003e08 | mov | x8, | x9 |
| 100003e0c | subs | х3, | x3, #0x1 |
| 100003e10 | b.ne | 0x1 | 00003df0 |
| 100003e14 | mov | x0, | x8 |
| 100003e18 | ret | | |

100003e1c mov x0, #0x0

100003e20 ret

memory

•••

| | | | - | - | | | |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

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| C | • |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

| 100003de4 | cmp | x3, #0x1 |
|-----------|------|-------------|
| 100003de8 | b.lt | 0x100003e1c |

100003dec mov x9, #0x0100003df0 mov x8, #-0x1

100003df4 tbnz x2, #0x3f, 0x100003e14

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

100003e1c mov x0, #0x0

100003e20 ret

memory

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| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

...



| x 0 | 0000 |
|------------|------|
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

| 100003de4 | cmp | x3, | #0x1 |
|-----------|------|-----|--------------------|
| 100003de8 | b.lt | 0x1 | 00003e1c |
| 100003dec | mov | x9, | #0×0 |
| 100003df0 | mov | x8, | #-0x1 |
| 100003df4 | tbnz | x2, | #0x3f, 0x100003e14 |
| 100003df8 | cmp | x2, | x1 |
| 100003dfc | b.ge | 0x1 | 00003e14 |
| 100003e00 | ldr | x2, | [x0, x2, 1s1 #3] |
| 100003e04 | add | x9, | x2, x9 |
| 100003e08 | mov | x8, | x9 |
| 100003e0c | subs | х3, | x3, #0x1 |
| 100003e10 | b.ne | 0x1 | 00003df0 |
| 100003e14 | mov | x0, | x8 |
| 100003e18 | ret | | |

x0, #0x0

100003e1c mov

100003e20 ret

memory

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| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|-------------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0 3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | * 00 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 00x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

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100003de4 cmp x3, #0x1

100003e14 mov x0, x8

100003e1c mov x0, #0x0

100003e18 ret

100003e20 ret

registers

| _ | |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

| 100003de8 | b.lt | 0x1(| 00003e1c |
|-------------------------|------|------|--------------------|
| 100003dec | mov | х9, | #0x0 |
| 100003df0 | mov | x8, | #-0x1 |
| 100003df4 | tbnz | x2, | #0x3f, 0x100003e14 |
| 100003df8 | cmp | x2, | x1 |
| 100003dfc | b.ge | 0x1(| 00003e14 |
| 100003e00 <mark></mark> | ldr | x2, | [x0, x2, 1s1 #3] |
| 100003e0400 | add | x9, | x2, x9 |
| 100003e08 | mov | x8, | x9 |
| 100003e0c | subs | х3, | x3, #0x1 |
| 100003e10 | b.ne | 0x10 | 0003df0 |

memory

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| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

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| x 0 | 0000 |
|------------|------|
| x 1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

100003de4 cmp x3, #0x1

100003de8 b.lt 0x100003e1c

100003dec mov x9, #0x0

100003df0 mov x8, #-0x1

100003df4 tbnz x2, #0x3f, 0x100003e14

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

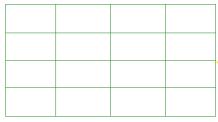
100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

cache



More realistic: memory reads are cached to speed up uses of the same address.

memory

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| x 0 | 0000 |
|------------|------|
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003de4 cmp x3, #0x1

100003dec mov x9, #0x0

100003df0 mov
$$x8, \#-0x1$$

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

100003e1c mov x0, #0x0

100003e20 ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| | 0000 | 0000 | 0000 | 0000 | | | 0000 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 0000 | 0x3049 0000 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |

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| x 0 | 0000 |
|------------|------|
| x1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003de4 cmp x3, #0x1

100003dec mov x9, #0x0

100003df0 mov
$$x8, \#-0x1$$

100003e08 mov x8, x9

| 1 | 00003e0c | guha | ~ 3 | ~3 | #0 \$21 |
|---------|----------|------|---------------|-------|---------|
| \perp | 00003600 | SUDS | $X \supset I$ | , XJ, | # U X 1 |

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

100003e1c mov x0, #0x0

100003e20 ret

memory

•••

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|----------|-------------------|--------|--------|--------|--------|
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | <u></u> | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 7 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x3020Ax | 3 02:3 02B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 00000 | 000000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |

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| | • |
|------------|------|
| x 0 | 0000 |
| x1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

0x3023

100003de4 cmp x3, #0x1

100003dec mov x9, #0x0

100003df0 mov
$$x8, \#-0x1$$

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

100003e1c mov x0, #0x0

100003e20 ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 0000 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 0000 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 0000 | 0x3036 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 | 0x3046 0000 | 0x3047 |
| 0x3048 0000 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |

...



| x 0 | 0000 |
|------------|------|
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

100003de4 cmp x3, #0x1 100003de8 b.lt 0x100003e1c 100003dec mov x9, #0x0100003df0 mov x8, #-0x1

100003df4 tbnz x2, #0x3f, 0x100003e14

100003df8 cmp x2, x1 100003dfc b.ge 0x100003e14

100003e00 ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

cache

| 0x3023 0000 | |
|----------------|--|
| | |

Typical LI cache holds

most recently accessed 128k bytes

memory

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 0000 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 | 0x3036 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 | 0x3046 | 0x3047 |
| 0x3048 | 0x3049 0000 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |



| C | • |
|------------|------|
| x 0 | 0000 |
| x1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

100003de4 cmp x3, #0x1

| 100003de8 | b.lt | 0x10 |)0003e1c |
|-----------|------|------|----------|
| 100003dec | mov | х9, | #0x0 |

100003df0 mov
$$x8$$
, $\#$ -0 $x1$

100003df4 tbnz x2, #0x3f, 0x100003e14

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 $\stackrel{\text{?}}{=}$ ldr x2, [x0, x2, lsl #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

cache

| 0x3023 0000 | |
|----------------|--|
| | |

Time needed to fetch address contents tells us whether it was used recently

memory

• • •

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|--------|--------|--------|--------|--------|--------|
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

•••



| x 0 | 0000 |
|------------|------|
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

100003de4 cmp x3, #0x1 100003de8 b.lt 0x100003e1c

100003dec mov x9, #0x0

100003df0 mov x8, #-0x1

100003df4 tbnz x2, #0x3f, 0x100003e14

100003df8 cmp x2, x1

100003dfc b.ge 0x100003e14

100003e00 $\stackrel{\square}{=}$ ldr x2, [x0, x2, ls1 #3]

100003e04 add x9, x2, x9

100003e08 mov x8, x9

100003e0c subs x3, x3, #0x1

100003e10 b.ne 0x100003df0

100003e14 mov x0, x8

100003e18 ret

memory

•••

| 0x3000 | 0x3001 0000 | 0x3002 | 0x3003 | 0x3004 0000 | 0x3005 | 0x3006 | 0x3007 |
|--------|----------------|--------|--------|----------------|--------|--------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |

•••

cache

| 0x3023 0000 | |
|----------------|--|
| | |

Time needed to fetch address contents tells us whether it was used recently

which is no problem in itself...

Example Program

```
long sum_path(long *array, long len, long pos, long count) {
  long i = 0, sum = 0, product = 1;

while (i < count) {
  if (pos >= 0 && pos < len)
    pos = array[pos];
  else
    return -1;
  sum = sum + pos;
  product = product * pos;
  i = i + 1;
}

return sum + product;
}</pre>
```

small array with small jumps: 150ms
big array with big jumps: 750ms

Example Program

```
long sum_path(long *array, long len, long pos, long count) {
  long i = 0, sum = 0, product = 1;

while (i < count) {
  if (pos >= 0 && pos < len)
     pos = array[pos];
  else
     return -1;
  sum = sum + pos;
  product = product * pos;
  i = i + 1;
}

return sum + product;
}</pre>
No difference with more instructions?
```

small array with small jumps: 150ms
big array with big jumps: 750ms



| _ | • |
|------------|------|
| x 0 | 0000 |
| x 1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1

100003db8 b.lt 0x100003df4 100003dbc mov x9, #0x0

100003dc0 mov x1, #0x1

 $100003dc4 = mov \times 8, #-0x1$

100003dc8 tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, lsl #3]

100003dd8 add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 0000 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | 0x3023 | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 0000 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 0000 | 0x3036 0000 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 0000 | 0x3046 0000 | 0x3047 |
| 0x3048 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |

. .

More realistic: multiple independent instructions execute at once



| | • |
|------------|------|
| x 0 | 0000 |
| x 1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

100003db4 cmp x3, #0x1 100003db8 b.lt 0x100003df4 100003dbc mov x9, #0x0100003dc0 mov x1, #0x1 100003dc4 mov x8, #-0x1100003dc8 tbnz x2, #0x3f, 0x100003dec 100003dcc cmp x2, x1 100003dd0 b.ge 0x100003dec 1 dr x2, [x0, x2, lsl #3] 100003dd8 add x9, x2, x9

memory •••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 0000 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | 0x3023 | 0x3024 0000 | 0x3025 0000 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 0000 | 0x3034 0000 | 0x3035 | 0x3036 0000 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 0000 | 0x3044 0000 | 0x3045 0000 | 0x3046 0000 | 0x3047 |
| 0x3048 0000 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |

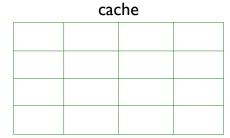
cache



| 100003ddc | mul | x1, | x2, | x1 |
|-----------|------|------|-------|------|
| 100003de0 | subs | х3, | х3, | #0x1 |
| 100003de4 | b.ne | 0x10 | 00003 | 3dc8 |
| 100003de8 | add | x8, | x1, | x9 |
| 100003dec | mov | x0, | x8 | |
| 100003df0 | ret | | | |
| 100003df4 | mov | w8, | #0x1 | L |
| 100003df8 | mov | x0, | x8 | |
| 100003dfc | ret | | | |
| | | | | |



| C | , |
|------------|------|
| x 0 | 0000 |
| x 1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |



| 100003db4 | cmp | х3, | #0x1 |
|-----------|-----|-----|------|
|-----------|-----|-----|------|

100003db8 b.lt 0x100003df4

100003dbc mov x9, #0x0 100003dc0 mov x1, #0x1

100003dc4 mov x8, #-0x1

100003dc8 tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 | ldr x2, [x0, x2, lsl #3]

1000@dd8 add x9, x2, x9

1000@ddc mul x1, x2, x1

 $10000\overline{3}$ de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |

...



| reg | isters | 100003db4 | cmp | x3, | #0x1 |
|------------|---------|---|--------|------|--------------------|
| x 0 | 0000 | 100003db8 | b.lt | 0x10 |)0003df4 |
| x 1 | 0000 | 100003dbc | mov | х9, | #0x0 |
| x2 | 0000 | 100003dc0 | mov | x1, | #0×1 |
| x3 x4 | 0000 | 100003dc4 | | - | #-0x1 |
| x5 | 0000 | | | • | #0x3f, 0x100003dec |
| ж6 | 0000 | 100003dcc | | - | · |
| x 7 | 0000 | 100003dd0 | _ | | |
| x8 | 0000 | | _ | | [x0, x2, ls1 #3] |
| x 9 | 0000 | | | | |
| | | 1000@dd8 | | | |
| cache | | 1000 \text{\text{\$\exitil{\$\text{\$\exititt{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\text{\$\}}\$}}}}\$}}}}} | | | |
| | | 10000 <mark>3de0</mark> | subs | х3, | x3, #0x1 |
| | | 10000 <mark>3</mark> de4 | b.ne | 0x10 |)0003dc8 |
| | | | | | x1, x9 |
| | Data-de | ependent inst | ructio | ons | x8 |
| | must w | ait for earlier | resul | t | |
| | | 100003014 | mov | wo, | #0x1 |
| | | 100003df8 | mov | х0, | x8 |
| | | 100003dfc | ret | | |

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| | 0000 | 0000 | 0000 | 0000 | | | 0000 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 0000 | 0x3049 0000 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |



| C | • |
|------------|------|
| x 0 | 0000 |
| x1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1

| 100003db8 | b.lt | 0x100003df4 |
|-----------|------|-------------|
| | | |

100003dbc mov x9, #0x0

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, lsl #3]

100003dd8 add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

1000 @de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|--------|--------|--------|--------|--------|--------|
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| | 0000 | 0000 | 0000 | 0000 | | | 0000 |



registers **x0** 0000 **x**1 0000 **x2** 0000 **x3** 0000 0000 x4 **x5** 0000

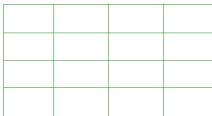
```
100003db4 cmp x3, #0x1
100003db8 b.lt 0x100003df4
100003dbc mov x9, #0x0
100003dc0 mov x1, #0x1
100003dc4 \text{ mov } x8, \#-0x1
100003dc8 tbnz x2, #0x3f, 0x100003dec
```

Addition and multiplication cc cmp x2, x1 can proceed in parallel

10 b.ge 0x100003dec

14 ldr x2, [x0, x2, ls1 #3]

cache



| 100003dd8 | add | х9, | x2, | x9 |
|--------------------------|------|------|-------|------|
| 100003ddc | mul | x1, | x2, | x1 |
| 100003de0 | subs | х3, | х3, | #0x1 |
| 100003de4 <mark>2</mark> | b.ne | 0x10 | 00003 | 3dc8 |
| 1000 <u>@</u> de8 | add | x8, | x1, | x9 |
| 10000 <mark>3</mark> dec | mov | x0, | x8 | |
| 100003df0 | ret | | | |
| 100003df4 | mov | w8, | #0x1 | L |
| 100003df8 | mov | x0, | x8 | |
| 100003dfc | ret | | | |

memory

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|--------|--------|--------|--------|--------|--------|
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| x 0 | 0000 | | | | |
|------------|------|--|--|--|--|
| х1 | 0000 | | | | |
| x 2 | 0000 | | | | |
| x 3 | 0000 | | | | |
| x4 | 0000 | | | | |
| x 5 | 0000 | | | | |
| x 6 | 0000 | | | | |
| x 7 | 0000 | | | | |
| x 8 | 0000 | | | | |
| x 9 | 0000 | | | | |

cache

100003db4 cmp x3, #0x1

| 100003ab8 | b.lt | OXI |)0003a14 |
|-----------|------|-----|----------|
| 100003dbc | mov | х9, | #0x0 |

100003dc0 mov x1, #0x1 100003dc4 mov x8, #-0x1

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, ls1 #3]

 $100003dd8^{2}$ add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

1000 @de8 add x8, x1, x9

100003dec mov x0, x8

100003 branch prediction: guess at branch

100003c

100003 result based on previous times through

100003dfc ret

memory

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 0000 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | 0x3023 | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 | 0x3033 | 0x3034 0000 | 0x3035 | 0x3036 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 | 0x3046 | 0x3047 |
| 0x3048 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |



| x 0 | 0000 | | | |
|------------|------|--|--|--|
| x 1 | 0000 | | | |
| x 2 | 0000 | | | |
| x 3 | 0000 | | | |
| x4 | 0000 | | | |
| x 5 | 0000 | | | |
| x 6 | 0000 | | | |
| x 7 | 0000 | | | |
| x 8 | 0000 | | | |
| x 9 | 0000 | | | |

cache



| 100003db4 | cmp | х3, | #0x1 |
|-----------|-----|-----|------|
|-----------|-----|-----|------|

100003db8 b.lt 0x100003df4 100003dbc mov x9, #0x0

100003dc0 mov x1, #0x1

100003dc4 mov x8, #-0x1

1000@dc8 tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, lsl #3]

100003dd8 = add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

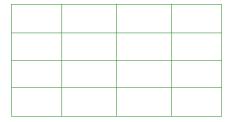
•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| _ | |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache



Unlike a data dependency, we could start early 100003d and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and then undo if the predicted branch was wrong and the predicted branch was w 100003d — and that's speculative execution 100003de4 mov xx, #-UXI 1000@dc8 tbnz x2, #0x3f, 0x100003dec 100003dcc cmp x2, x1 100003dd0 b.ge 0x100003dec 100003dd4 ldr x2, [x0, x2, lsl #3] $100003dd8^{2}$ add x9, x2, x9 100003ddc mul x1, x2, x1 100003de0 subs x3, x3, #0x1 100003de4 b.ne 0x100003dc8 100003de8 add x8, x1, x9 100003dec mov x0, x8 100003df0 ret 100003df4 mov w8, #0x1 100003df8 mov x0, x8 100003dfc ret

ory

0x3030 0x3031 0x3032 0x3033 0x3038 0x3039 0x303A 0x303B 0x303C 0x303D 0x303E 0x303E 0x3040 0x3041 0x3042 0x3043 0x3044 0x3045 0x3046 0x304 0x3048 0x3049 0x304A 0x304B 0x304C 0x304D 0x304E 0x304F



| ۰. ۵ | |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1

100003db8 b.lt 0x100003df4

100003dbc mov x9, #0x0

100003dc0 mov x1, #0x1

100003dc4; mov x8, #-0x1 100003dc8; tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, ls1 #3]

 $100003dd8^{2}$ add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

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| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

...

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| C | • |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1100003db8 b.lt 0x100003df4 100003dbc mov x9, #0x0

100003dc0 mov x1, #0x1

100003dc4 mov x8, #-0x1

100003dc8 tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, ls1 #3]

100003dd8 add x9, x2, x9

100003ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | 0x3023 | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 0000 | 0x3036 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 0000 | 0x3044 0000 | 0x3045 0000 | 0x3046 0000 | 0x3047 |
| 0x3048 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |



| 0000 |
|------|
| 0000 |
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| 0000 |
| 0000 |
| 0000 |
| |

100003db4 cmp x3, #0x1

100003db8 b.lt 0x100003df4 100003dbc mov x9, #0x0 100003dc0 mov x1, #0x1 100003dc4 mov x8, #-0x1

100003dc8 tbnz x2, #0x3f, 0x100003dec

100003dcc cmp x2, x1

100003dd0; b.ge 0x100003dec

100003dd42 ldr x2, [x0, x2, lsl #3]

100 dd8 add x9, x2, x9

100003ddc

100003daC 100003de0 100003de4

Speculative read can start fetching memory, which might affect the cache

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

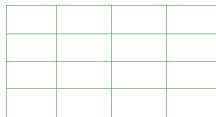
memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x3011 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3021 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x3021 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3031 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x304 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x3041 |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |

...

cache





| C | • |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x 4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1

| 100003db8 b.lt 0x100003df4 | 4 |
|----------------------------|---|
|----------------------------|---|

100003dbc mov x9, #0x0

100003dc0 mov
$$x1$$
, $\#0x1$

100003dc4 mov
$$x8, \#-0x1$$

| 100 <mark>=(=</mark> dd8 | add | х9, | x2, | x9 |
|--------------------------|-----|-----|-----|----|
|--------------------------|-----|-----|-----|----|

| | 100003ddc | mul | x1, | x2, | x1 |
|--|-----------|-----|-----|-----|----|
|--|-----------|-----|-----|-----|----|

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| | | | - | - | | | |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300F |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | 0x3023 | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303F |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| C | • |
|------------|------|
| x 0 | 0000 |
| x 1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

100003db4 cmp x3, #0x1

| 100003db8 | b.lt | 0x1000 | 03df4 |
|-----------|------|--------|-------|
| 100003dbc | mov | x9, #0 |)x0 |

$$100003dc4 \text{ mov } x8, \#-0x1$$

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, ls1 #3]

100 dd8 add x9, x2, x9

 $100\overline{003}$ ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 | 0x3001 | 0x3002 | 0x3003 | 0x3004 | 0x3005 | 0x3006 | 0x3007 |
|--------|--------|--------|-------------|--------|--------|--------|--------|
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | - | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | •• | 0000 | | | 0000 |
| 0x3028 | 0x3029 | 0x302A | B | 0x302C | 0x302D | 0x302E | 0x302E |
| 0000 | 0000 | 0000 | 0 0 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | °0923 | വരായുദ | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | NW- | 6 44 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x30BA | (0x/303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| C | • |
|------------|------|
| x 0 | 0000 |
| х1 | 0000 |
| x 2 | 0000 |
| x 3 | 0000 |
| x4 | 0000 |
| x 5 | 0000 |
| x 6 | 0000 |
| x 7 | 0000 |
| x 8 | 0000 |
| x 9 | 0000 |

cache

0x3023

100003db4 cmp x3, #0x1

| 100003db8 | b.lt | 0x100003df4 |
|-----------|------|-------------|
| | | |

100003dbc mov
$$x9$$
, $\#0x0$

$$100003dc0$$
 mov $x1$, $\#0x1$

$$100003dc4 \text{ mov } x8, \#-0x1$$

100003dcc cmp x2, x1

100003dd0 b.ge 0x100003dec

100003dd4 ldr x2, [x0, x2, ls1 #3]

100 dd8 add x9, x2, x9

 $100\overline{003}$ ddc mul x1, x2, x1

100003de0 subs x3, x3, #0x1

100003de4 b.ne 0x100003dc8

100003de8 add x8, x1, x9

100003dec mov x0, x8

100003df0 ret

100003df4 mov w8, #0x1

100003df8 mov x0, x8

100003dfc ret

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|--------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A | 0x300B | 0x300C | 0x300D | 0x300E | 0x300E |
| | 0000 | 0000 | 0000 | 0000 | 0000 | | 0000 |
| 0x3010 | 0x3011 | 0x3012 | 0x3013 | 0x3014 | 0x3015 | 0x3016 | 0x3017 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3018 | 0x3019 | 0x301A | 0x301B | 0x301C | 0x301D | 0x301E | 0x301F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3020 | 0x3021 | 0x3022 | | 0x3024 | 0x3025 | 0x3026 | 0x3027 |
| | 0000 | 0000 | | 0000 | | | 0000 |
| 0x3028 | 0x3029 | 0x302A | 0x302B | 0x302C | 0x302D | 0x302E | 0x302F |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3030 | 0x3031 | 0x3032 | 0x3033 | 0x3034 | 0x3035 | 0x3036 | 0x3037 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3038 | 0x3039 | 0x303A | 0x303B | 0x303C | 0x303D | 0x303E | 0x303E |
| | 0000 | 0000 | 0000 | 0000 | | 0000 | 0000 |
| 0x3040 | 0x3041 | 0x3042 | 0x3043 | 0x3044 | 0x3045 | 0x3046 | 0x3047 |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |
| 0x3048 | 0x3049 | 0x304A | 0x304B | 0x304C | 0x304D | 0x304E | 0x304E |
| 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 | 0000 |



| | registe | ers | 100003db4 | cmp | x3, #0x | κ1 | | |
|--------|----------------------|--------|-----------------------------|--------|------------|-------|--------|-------|
| | x0 0 x | 000 | 100003db8 | b.lt | 0x10000 |)3df4 | | |
| _ | | 000 | 100003dbc | mov | x9, #0x | ς0 | | |
| | | 000 | 100003dc0 | mov | x1, #0x | κ1 | | |
| | | 000 | 100003dc4 | mov | x8, #-(|)x1 | | |
| | | 000 | 100003dc8 | | x2, #0x | | 0x1000 | 03dec |
| | x6 00 | 000 | 100003dcc | | • | , | | |
| | | 000 | 100003dd0 | _ | 0x10000 |)345c | ı | |
| - | | 000 | | | | | | # O 1 |
| | x9 00 | 000 | 100003dd4 | | x2, [$x($ | • | , ISI | #3] |
| | | | 100 <mark>:::</mark> dd8 | add | x9, x2, | x9 | | |
| cache | | | 100003ddc | mul | x1, x2, | x1 | | |
| | | | 100003de0 | subs | x3, x3, | #0x | 1 | |
| ? | | | 100003de4 | b.ne | 0x10000 |)3dc8 | | |
| | | | 100003de8 | add | x8, x1, | x9 | | |
| 0x3023 | | | | | , , | | | |
| Sp | ecula | tive | cache changes | are no | ot | | | |
| re | verte | d if I | <mark>branch predict</mark> | ion wa | s wrong | L | | |
| | | | τοοοισαία | mov | XU, XX | | | |
| | | | 100003dfc | ret | | | | |

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 0000 | 0x3009 0000 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 0000 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 0000 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | | 0x3024 0000 | 0x3025 0000 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 0000 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 | 0x3036 0000 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 | 0x3046 0000 | 0x3047 |
| 0x3048 0000 | 0x3049 0000 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |



| | reg | isters | 100003db4 | cmp | х3, | #0×1 |
|-----|------------|--------|--------------------------|-------|------|--------------------|
| | x 0 | 0000 | 100003db8 | b.lt | 0x1 | 00003df4 |
| | x 1 | 0000 | 100003dbc | mov | х9, | #0×0 |
| | x 2 | 0000 | 100003dc0 | mov | x1. | #0×1 |
| | x 3 | 0000 | 100003dc4 | | • | #-0x1 |
| | x4 | 0000 | 100003404 | IIIOV | x0, | #-0%1 |
| | x 5 | 0000 | 100003dc8 | tbnz | x2, | #0x3f, 0x100003dec |
| | x 6 | 0000 | 100003dcc <mark>2</mark> | cmp | x2, | x1 |
| | x 7 | 0000 | | | | |
| | x 8 | 0000 | 100003dd0 | b.ge | OXI | 00003dec |
| | x 9 | 0000 | 100003dd4 | ldr | x2, | [x0, x2, ls1 #3] |
| | | | 100 <mark>::</mark> 2dd8 | add | х9, | x2, x9 |
| cac | he | | 100 <mark>003</mark> ddc | mul | x1, | x2, x1 |
| | | | 100003de0 | subs | х3, | x3, #0x1 |
| ? | | | 100003de4 | b.ne | 0x10 | 00003dc8 |
| 302 | | | 100003de8 | add | x8, | x1, x9 |

memory

•••

| 0x3000 0000 | 0x3001 0000 | 0x3002 0000 | 0x3003 | 0x3004 0000 | 0x3005 0000 | 0x3006 0000 | 0x3007 |
|----------------|----------------|----------------|----------------|----------------|----------------|----------------|--------|
| 0x3008 | 0x3009 | 0x300A 0000 | 0x300B 0000 | 0x300C 0000 | 0x300D 0000 | 0x300E 0000 | 0x300F |
| 0x3010 0000 | 0x3011 0000 | 0x3012 0000 | 0x3013 | 0x3014 0000 | 0x3015 0000 | 0x3016 0000 | 0x3017 |
| 0x3018 | 0x3019 0000 | 0x301A 0000 | 0x301B 0000 | 0x301C 0000 | 0x301D 0000 | 0x301E 0000 | 0x301F |
| 0x3020 0000 | 0x3021 0000 | 0x3022 0000 | | 0x3024 0000 | 0x3025 | 0x3026 | 0x3027 |
| 0x3028 | 0x3029 | 0x302A 0000 | 0x302B 0000 | 0x302C 0000 | 0x302D 0000 | 0x302E 0000 | 0x302F |
| 0x3030 0000 | 0x3031 0000 | 0x3032 0000 | 0x3033 | 0x3034 0000 | 0x3035 | 0x3036 | 0x3037 |
| 0x3038 | 0x3039 | 0x303A 0000 | 0x303B 0000 | 0x303C 0000 | 0x303D 0000 | 0x303E 0000 | 0x303F |
| 0x3040 0000 | 0x3041 0000 | 0x3042 0000 | 0x3043 | 0x3044 0000 | 0x3045 | 0x3046 | 0x3047 |
| 0x3048 | 0x3049 | 0x304A 0000 | 0x304B 0000 | 0x304C 0000 | 0x304D 0000 | 0x304E 0000 | 0x304F |

Speculative cache changes are not

0x3023

reverted if branch prediction was wrong

100003ars mov xu, xs 100003dfc ret which is no problem in itself...

Speculation and Caching Consequences

Caching and speculative execution are **crucial** to performance, and speculation may run many instructions — not just one!

Speculative execution can try things that shouldn't happen

- due to a bounds check or tag check that guards a read
- due to page protection, which is a similarly pipelined check

These things-that-should-never-happen are rolled back, so the program never sees the effects

... execpt via timing



```
int *my_pages[256]; // array of pointers to different pages
int probe(int check, void *addr) {
  if (check) {
    int8 index = *(int8 *)addr;
    return *(my_pages[index]);
  } else
    return 0;
}
```

Step I: call probe many times with true and a good address

 \Rightarrow convince the branch predictor that \mathtt{check} is probably true

```
int *my_pages[256]; // array of pointers to different pages
int probe(int check, void *addr) {
  if (check) {
    int8 index = *(int8 *)addr;
    return *(my_pages[index]);
  } else
    return 0;
}
```

Step 2: read a lot of memory not in my_pages

 \Rightarrow no pages in my_pages are cached

```
int *my_pages[256]; // array of pointers to different pages
int probe(int check, void *addr) {
  if (check) {
    int8 index = *(int8 *)addr;
    return *(my_pages[index]);
  } else
    return 0;
}

JavaScript variant: use an array and index
instead of a raw address
```

Step 3: call probe with false and an addr to attack

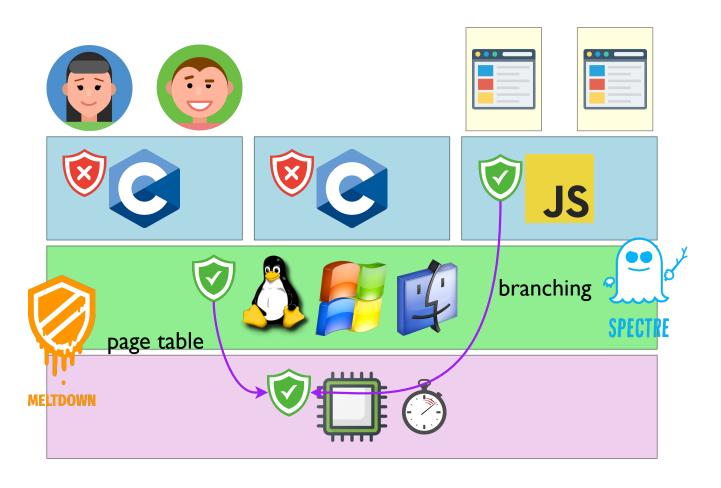
- ⇒ byte that you should never see at addr is read speculatively
- ⇒ byte is used to index my pages, bringing one page into the cache

```
int *my_pages[256]; // array of pointers to different pages
int probe(int check, void *addr) {
  if (check) {
    int8 index = *(int8 *)addr;
    return *(my_pages[index]);
  } else
    return 0;
}
```

Step 4: time a read of each page in my_pages

⇒ the fast one tells you what byte was read speculatively!

Safety and Isolation





JavaScript implication: use array+index to read any byte of memory

Browser implication: pages are not isolated

Solutions:

- turn off speculation, which makes things slow
- generate code to cooperate less, which makes things slow
- reduce timer resolution, which makes attacks harder

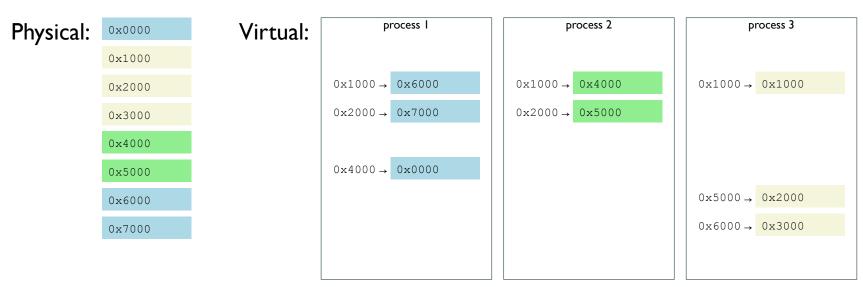


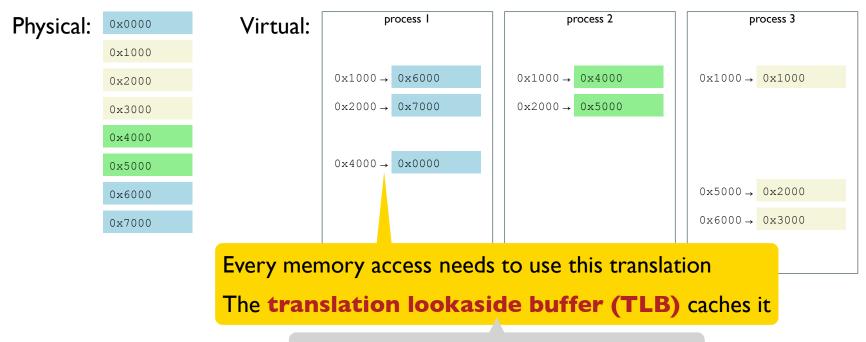


Different processes have different memory pages
Within one process, C can read any address, anyway
so what's the big deal?

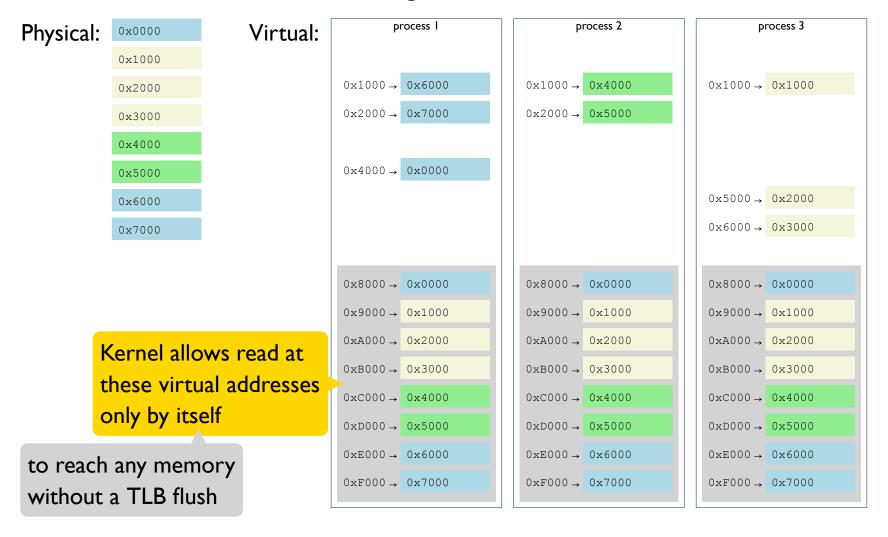
As it turns out, OS kernels map all physical memory in every process, but guards it with page protection

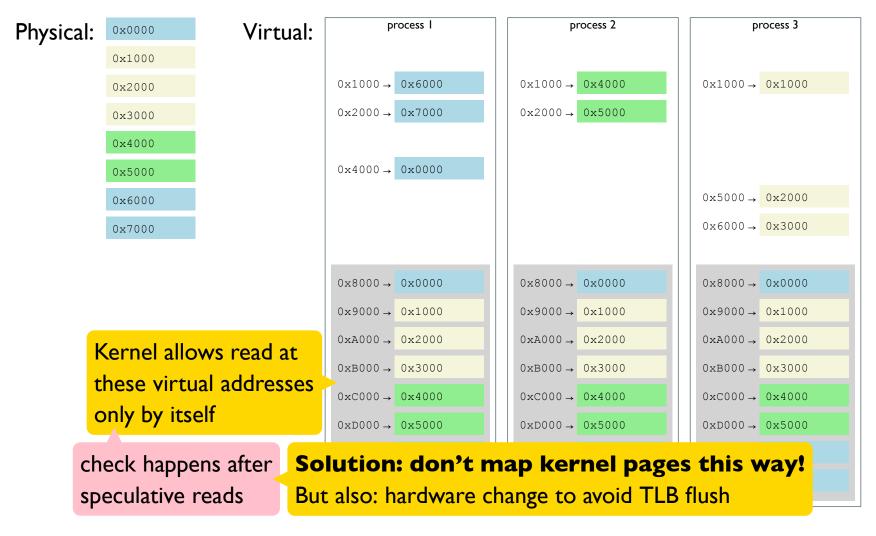
- enables access across process boundaries
- avoids expensive page-table resets





must be flushed when changing processes





Summary

Meltdown and **Spectre** are recently discovered side-channel attacks

These exploits are not easy to block, because they take advantage of key implementation techniques for making processors run fast:

- memory caches
- speculative execution