

eSim

An open source EDA tool

Circuit Simulation Project

qYyls

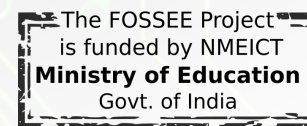


Certificate of Participation

This is to certify that
Chandragiri Praveen Gandhi
from Srm Institue Of Science And Technology
has successfully completed the circuit simulation of
Small Signal Power Amplifier
under eSim Circuit Simulation Project

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