Praveen Dhananjaya

Portfolio: https://sites.google.com/view/praveen-dhananjaya
Email:e16081@eng.pdn.ac.lk | phone: +94 0778094061
GitHub: https://github.com/praveendhananjaya

EDUCATION

COMPUTER ENGINEERING BSc(Eng) | University of Peradeniya Sri Lanaka
Present – Oct 2022 | Faculty of Engineering

- 3rd-year undergraduate
- Current GPA : 3.40 / 4.00

GCE | Advanced Level
2016

- Maths - A , Physics - B , Chemistry - B
- z-core 1.83
- 1206 from 30000+ participants nationwide

RELEVANT COURSEWORK

HARDWARE COURSES

- Computer Architecture, Digital Design, Embedded Systems, Signal Processing, Computer Systems Engineering: Industrial Networks, Electronics I ,Electronics II | Bsc Eng Degree Program
- Circuit Design

PROGRAMMING

- software construction, Data Structures and Algorithms, programming methodology, Software Engineering | Bsc Eng Degree Program
- Verilog , C++ , Arm , ARM assembly, Java, Python , Arduino

ARTIFICIAL INTELLIGENCE

- Neural Networks and Fuzzy Systems, Machine Learning and Data Mining | Bsc Eng Degree Program
- Neural Networks and Deep Learning | Coursera Deeplearning.AI

OTHER COURSES

- Operating Systems, Computer and Network Security | university course

TECHNICAL SKILLS

PROGRAMMING LANGUAGES | C , C++, Java, Python

HARDWARE PROGRAMMING | Verilog, arduino, AVR C, PIC, ARM Assembly

PCB DESIGN | Altium , eagle , easyeda

3D MODELING | Fusion 360 , solidworks

DATABASE | MySQL

PROJECTS

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SMART WAREHOUSE MANAGEMENT SYSTEM 2021

This is a fully automated warehouse management system using agvs and robotic arms. This system is able to support shopping websites while preparing customer orders autonomously.

- Technologies : circuit design, AVR C and Arduino, multitasking(real-time scheduling) and interrupt, active filters, encryption schemes(fernet and ect), Mysql, mqtt, AWS server
- https://github.com/cepdnaclk/e16-3yp-smart-pharmaceutical-warehousing

8-BIT SINGLE-CYCLE CPU 2020

8-bit single-cycle CPU based on Harvard architecture which uses a 32-bit instruction word. This CPU is able to execute most algorithms.

- Technologies : Verilog, Harvard architecture, pipelining mechanisms , memory hierarchy(direct-mapped cache), custom assembler
- github: https://github.com/praveendhananjaya/CPU-8-bit-FPGA-

8 BIT COMPUTER SAP-1 2020

This is a common bus architecture (SAP-1) computer. It can compute simple algorithms. For this implementation, I designed and developed a custom PCB and microinstruction set.

- Technologies : VSAP-1 architecture, circuit design, custom assembler
- github: https://github.com/praveendhananjaya/CPU-8-bit-common-bus

HOSPITAL MANAGEMENT SYSTEM 2020
This system is able to manage hospital resources without starving the services. While handling all the cashiers' workers. Ex:- billing and recording.
- Technologies : MySQL, HTML, PHP
- github: https://github.com/praveendhananjaya/hospital-managment-system

FRACTAL VISUALIZER 2020
JAVA OOP base multithreading programme.Fractal Visualizer is java based program and calculates 640000 mandelbrot or Julia fractal within seconds. In order to maximize the performance, the master thread creates a job bank with 400 entries and continuously feeds. And these jobs are processed by CPUs. As a result, both CPUs and jobs aren't starving without resources.
- Technologies : Java OOP, Witch is accelerated by tiled base multi threading
- github: https://github.com/praveendhananjaya/Fractals

SURVEILLANCE CAMERA SYSTEM 2020
Suspicious activity tracking. ex:- Face covers , Abandoned packages , suspicious object , unauthorized people
- Technologies : python , tensor-flow

MICRO-MOUSE 2019
develop a robot that can approach the destination of a maze.Real-time operation using interrupts and super loop approach.
- Technologies : custom PCB, Arduino and C flood fill algorithm A* algorithm, active filters, superloop programming architecture
- github: https://github.com/praveendhananjaya/micro-mouse

LANDSLIDE MONITORING SYSTEM 2018
This is low budget Land slide monitoring system. This system is capable to detect and monitor the land side and land behavior.
- Technologies : flexible piezoelectric sensor and single analyse, active filters, pattern recognition, UDP communication using WiFi network superloop programming architecture

ACHIEVEMENT

CYBERSECURITY CISCO | 2021
course authorized by CISCO and offered through CISCO networking academy

NERUAL NETWORKS AND DEEP LEARING | 2021
course authorized by Deeplearning.AI and offered through Coursera

ACES CODERS 4TH PLACE| 2020
12-hour algorithmic coding nationwide competition,nationwide competition over 150+ teams

ROBOFEST MICROMOUSE 3RD PLACE| 2019
14×14 Maze solving robot competition.Using small robot, nationwide competition over 100+ teams

ROBOFEST MICROMOUSE 3RD PLACE| 2019
14×14 Maze solving robot competition.Using small robot, nationwide competition over 100+ teams

ACES HACKATHON 1ST PLACE| 2019
Surveillance camera system Suspicious activity monitoring system, competition over 60+ teams

MORA XTREME 4.0 1ST PLACE| 2019
12-hour algorithmic coding nationwide competition,nationwide competition over 100+ teams

JAFFNA CODERS V1.0 4TH PLACE| 2019
12-hour algorithmic coding nationwide competition,nationwide competition over 80+ teams

ACES CODERS V7.0|2018 PARTICIPATION
12-hour algorithmic coding nationwide competition,nationwide competition over 150+ teams

ACES HACKATHON 1ST PLACE| 2018
landslide monitoring system , low cost landslid detection and alarming system, competition over 60+ teams