JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

CMM. No. : C0204318

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

Serial No. : 21046015166

Name : PASUPULETI PRAVEEN

Hall Ticket No. : 08C51A0493

Year of Admission : 2008-2009

Name of the College : C5-S S I T,GANGAVARAM

Month & Year of Final Exam : April,2012

Class Awarded : FIRST CLASS

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	20	80	100			Maximum Marks in Lab	25	50	75	
	I YEAR										
1	ENGLISH	15	64	79	4	2	MATHEMATICS - I	13	40	53	6
3	MATHEMATICAL METHODS	11	46	57	6	4	APPLIED PHYSICS	12	38	50	4
5	C PRG.& DATA STRUCTURES	15	35	50	6	6	NETWORK ANALYSIS	12	71	83	4
7	ELECTRONIC DEVICES & CKTS.	14	40	54	6	8	E4-ENGINEERING DRAWING	18	49	67	4
9	COMPUTER PROGRAMMING LAB	19	38	57	4	10	IT WORKSHOP	23	47	70	4
11	ELECTRONIC DEV.&CKTS.LAB	21	39	60	4	12	ENGLISH LANG.COMM.SKILLS LAB	21	44	65	4
	I SEMESTER						**II YEAR**				**II SEMESTER**
1	MATHEMATICS - III	09	31	40*	4	1	PULSE & DIGITAL CIRICUITS	11	48	59	4
2	PROBABILITY THEORY & STO. PROCESSES	10	30	40*	4	2	CONTROL SYSTEMS	15	61	76	4
3	ENVIRONMENTAL STUDIES	13	66	79	4	3	OBJECT ORIENTED PROGRAMMING	10	40	50	4
4	SIGNALS & SYSTEMS	06	42	48	4	4	SWITCHING THEORY & LOGIC DESIGN	13	30	43	4
5	ELECTRICAL TEHCNOLOGY	13	50	63	4	5	EM WAVES & TRANSMISSION LINES	12	36	48	4
6	ELECTRONICS CIRCUIT ANALYSIS	14	43	57	4	6	ANALOG COMMUNICATIONS	13	80	93	4
7	ELECTRONIC CIRCUITS (LAB)	21	40	61	2	7	ANALOG COMMUNICATIONS (LAB)	22	47	69	2
8	ELECTRICAL TECHNOLOGY (LAB)	20	50	70	2	8	PULSE & DIGITAL CIRCUITS (LAB)	18	45	63	2
	I SEMESTER						**III YEAR**				**II SEMESTER**
1	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	13	37	50	4	1	DIGITAL SIGNAL PROCESSING	16	59	75	4
2	COMPUTER ORGANIZATION	15	30	45	4	2	MICROPROCESSORS & INTERFACING	13	46	59	4
3	LINEAR IC APPLICATIONS	14	43	57	4	3	MANAGEMENT SCIENCE	14	73	87	4
4	DIGITAL IC APPLICATIONS	12	74	86	4	4	VLSI DESIGN	10	39	49	4
5	ANTENNAS AND WAVE PROPAGATION	07	55	62	4	5	MICROWAVE ENGINEERING	15	46	61	4
6	DIGITAL COMMUNICATIONS	16	54	70	4	6	TELECOMMUNICATION SWITCHING SYSTEMS	16	70	86	4
7	DIGITAL COMMUNICATIONS LAB	22	46	68	2	7	MICROPROCESSORS & INTERFACING (LAB)	21	47	68	2
8	IC APPLICATIONS AND ECAD LAB	22	45	67	2	8	ADVANCED ENGLISH COMMN. SKILLS (LAB)	21	39	60	2
	I SEMESTER						**IV YEAR**				**II SEMESTER**
1	COMPUTER NETWORKS	14	28	42	4	1	RADAR SYSTEMS	16	57	73	4
2	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	16	50	66	4	2	EMBEDDED AND REAL TIME SYSTEMS	13	36	49	4
3	CELLULAR AND MOBILE COMMUNICATIONS	16	52	68	4	3	DSP PROCESSORS AND ARCHITECTURES	12	56	68	4
4	OPTICAL COMMUNICATIONS	14	46	60	4	4	INDUSTRY ORIENTED MINI PROJECT	-	44	44	2
5	MICRO CONTROLLERS AND APPLICATIONS	12	39	51	4	5	SEMINAR	42	-	42	2
6	DIGITAL IMAGE PROCESSING	14	62	76	4	6	PROJECT WORK#	40	140	180	10
7	MICROWAVE AND OPTICAL COMMUNICATIONS (LAB)	23	44	67	2	7	COMPREHENSIVE VIVA	-	65	65	2
8	DIGITAL SIGNAL PROCESSING (LAB)	21	44	65	2						
							(# Project Internal= 40 , External=160)				

Number of Credits registered for : 224 Aggregate Marks Secured for best: 216

Aggregate Marks Secured : 3690 OUT OF 5350 (68.97%)

Date of Issue : June 15, 2012

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS